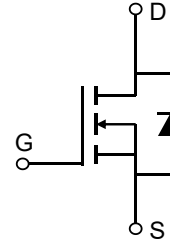


Description

Features

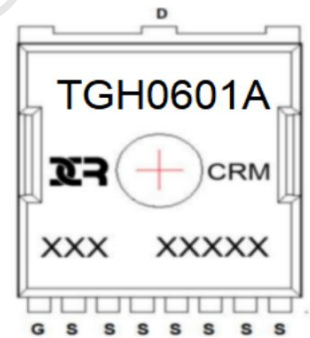
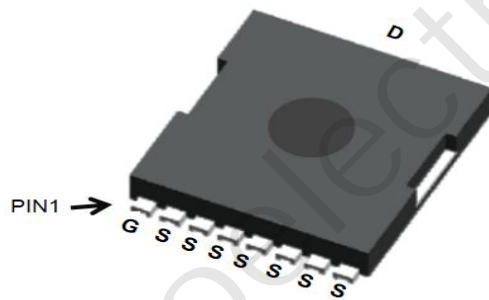
- 60V, 350A
- $R_{DS(ON)}$ Typ = 1.1mΩ @ $V_{GS} = 10V$
- Advanced Split Gate Trench Technology
- Excellent $R_{DS(ON)}$ and Low Gate Charge
- 100% UIS TESTED!
- 100% ΔV_{ds} TESTED!



Schematic Diagram

Application

- Load Switch
- PWM Application
- Power Management



Marking and Pin Assignment

Package Marking and Ordering Information

Device	Marking	Package	Outline	Reel Size	Reel (pcs)	Per Carton (pcs)
CRMTGH0601A	CRMTGH0601A	TOLL	TAPING	13"	2000	10000

Absolute Maximum Ratings (@ $T_J = 25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter	Value	Units	
V _{DS}	Drain-to-Source Voltage	60	V	
V _{GS}	Gate-to-Source Voltage	±20	V	
I _D	Continuous Drain Current	T _C = 25°C	350	A
		T _C = 100°C	210	A
I _{DM}	Pulsed Drain Current ⁽¹⁾	1400	A	
E _{AS}	Single Pulsed Avalanche Energy ⁽²⁾	756	mJ	
P _D	Power Dissipation	T _C = 25°C	290	W
R _{θJC}	Thermal Resistance, Junction to Case	0.43	°C/W	
T _J , T _{STG}	Junction & Storage Temperature Range	-55 to 150	°C	

Electrical Characteristics ($T_J = 25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
Off Characteristics						
V _{(BR)DSS}	Drain-Source Breakdown Voltage	I _D = 250μA, V _{GS} = 0V	60	-	-	V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = 60V, V _{GS} = 0V	-	-	1.0	μA
I _{GSS}	Gate-Body Leakage Current	V _{DS} = 0V, V _{GS} = ±20V	-	-	±100	nA
On Characteristics						
V _{GS(th)}	Gate Threshold Voltage	V _{DS} = V _{GS} , I _D = 250μA	2.4	3	3.6	V
R _{DS(ON)}	Static Drain-Source ON-Resistance ⁽³⁾	V _{GS} = 10V, I _D = 30A	-	1.1	1.4	mΩ
Dynamic Characteristics						
C _{iss}	Input Capacitance	V _{GS} = 0V, V _{DS} = 30V, f = 100KHz	-	6447	-	pF
C _{oss}	Output Capacitance		-	2778	-	pF
C _{rss}	Reverse Transfer Capacitance		-	66	-	pF
Q _g	Total Gate Charge	V _{GS} = 0 to 10V V _{DS} = 30V, I _D = 30A	-	130	-	nC
Q _{gs}	Gate Source Charge		-	43	-	nC
Q _{gd}	Gate Drain("Miller") Charge		-	17	-	nC
Switching Characteristics						
t _{d(on)}	Turn-On DelayTime	V _{GS} = 10V, V _{DD} = 30V I _D = 30A, R _{GEN} = 3Ω	-	35	-	ns
t _r	Turn-On Rise Time		-	73	-	ns
t _{d(off)}	Turn-Off DelayTime		-	76	-	ns
t _f	Turn-Off Fall Time		-	55	-	ns
Drain-Source Diode Characteristics and Max Ratings						
I _S	Maximum Continuous Drain to Source Diode Forward Current		-	-	350	A
I _{SM}	Maximum Pulsed Drain to Source Diode Forward Current		-	-	1400	A
V _{SD}	Drain to Source Diode Forward Voltage	V _{GS} = 0V, I _S = 30A	-	-	1.2	V
trr	Body Diode Reverse Recovery Time	I _F = 30A, di/dt = 100A/us	-	90	-	ns
Qrr	Body Diode Reverse Recovery Charge		-	125	-	nC

Notes:

1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature.
2. E_{AS} condition: Starting $T_J = 25^\circ\text{C}$, $V_{DD} = 30\text{V}$, $V_G = 10\text{V}$, $R_G = 25\text{ohm}$, $L = 0.5\text{mH}$, $I_{AS} = 55\text{A}$
3. Pulse Test: Pulse Width $\leq 300\mu\text{s}$, Duty Cycle $\leq 0.5\%$.

Test Circuit

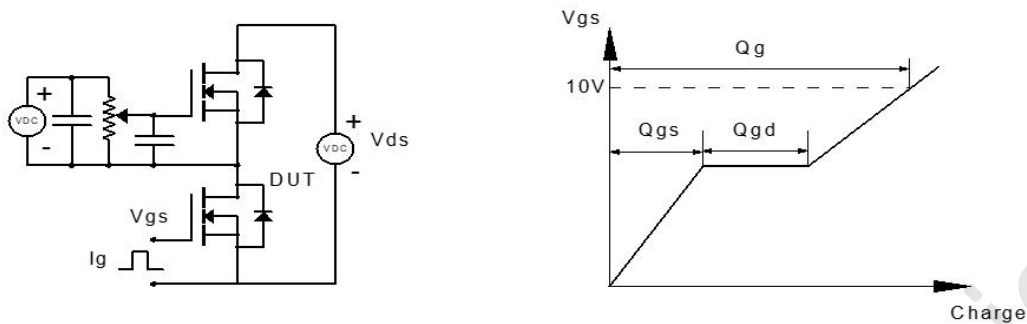


Figure 1: Gate Charge Test Circuit & Waveform

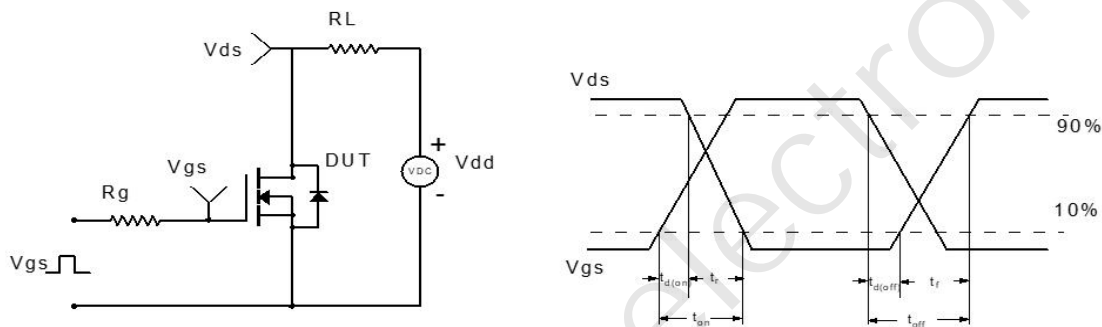


Figure 2: Resistive Switching Test Circuit & Waveform

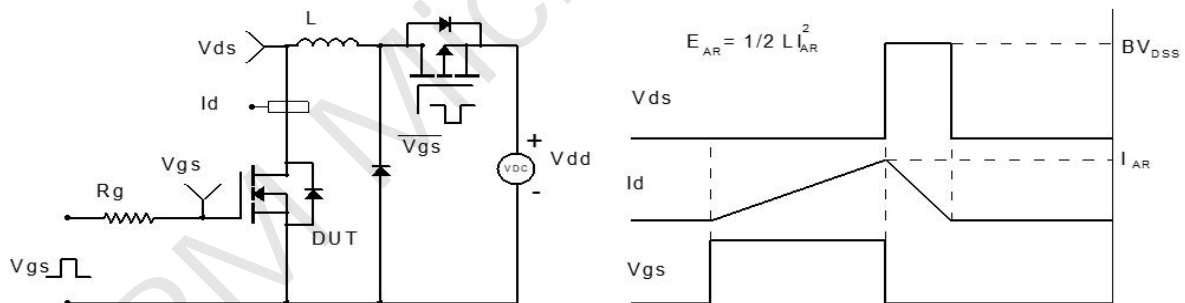


Figure 3: Unclamped Inductive Switching Test Circuit & Waveform

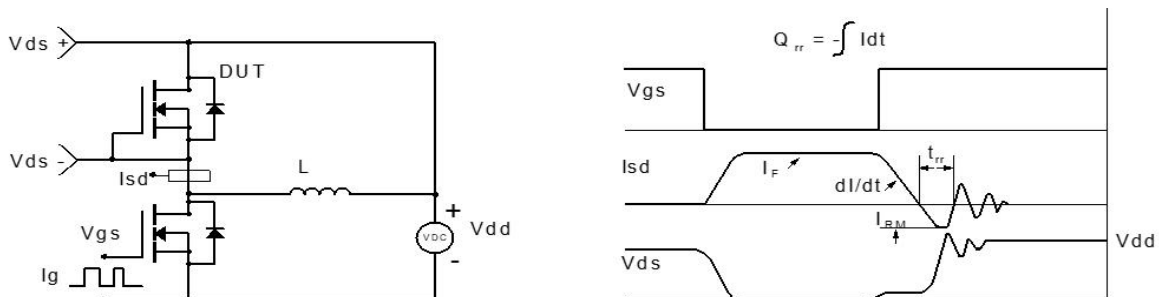
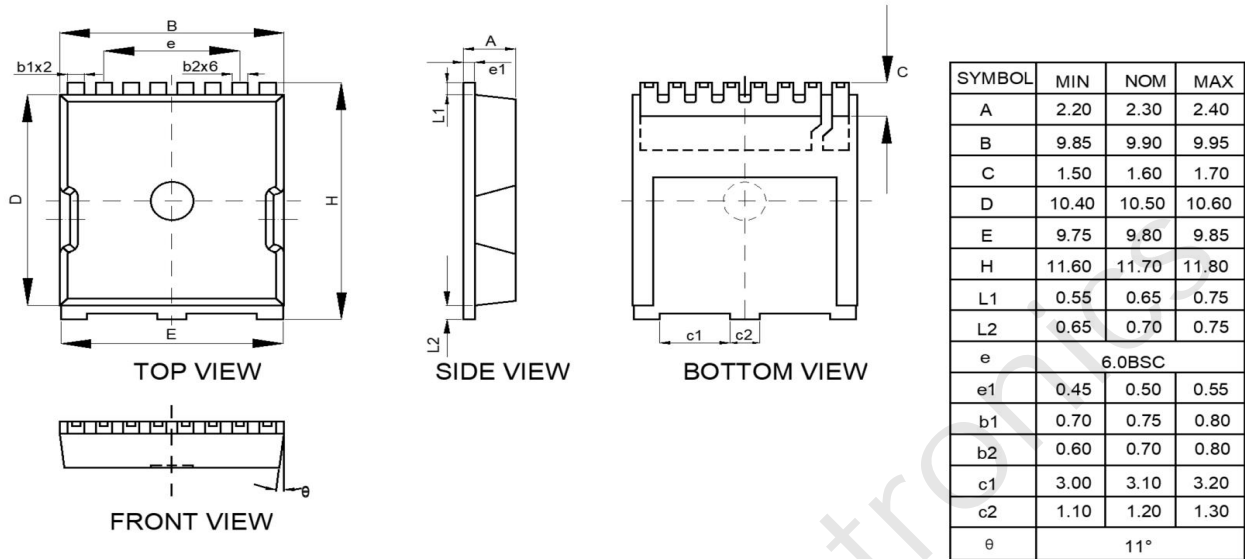


Figure 4: Diode Recovery Test Circuit & Waveform

Package Mechanical Data(TOLL)




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