

#### Description

#### **Features**

• 30V, 5A

 $R_{\text{DS(ON)}}$  Typ = 22m $\Omega$  @ V\_{GS} = 10V

 $R_{DS(ON)}$  Typ = 24m $\Omega$  @ V<sub>GS</sub> = 4.5V

 $R_{DS(ON)}$  Typ = 29m $\Omega$  @ V<sub>GS</sub> = 2.5V

- Advanced Trench Technology
- Excellent R<sub>DS(ON)</sub> and Low Gate Charge
- Lead Free

# Application

- Load Switch
- PWM Application
- Power Management

# Role Schematic Diagram

3400L

1 G 2 S

# Marking and Pin Assignment

#### Package Marking and Ordering Information

Device	Marking	Package	Outline	Reel Size	Reel (pcs)	Per Carton (pcs)
CRMLTU3400L	3400L	SOT-23	TAPING	7"	3000	120000

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#### Absolute Maximum Ratings (@ T<sub>J</sub> = 25°C unless otherwise specified)

Symbol	Parameter		Value	Units
V <sub>DS</sub>	Drain-to-Source Voltage		30	V
V <sub>GS</sub>	Gate-to-Source Voltage		±12	V
	Continuous Drain Current	T <sub>A</sub> = 25°C	5	А
Ι <sub>D</sub>		T <sub>A</sub> = 100°C	3	А
I <sub>DM</sub>	Pulsed Drain Current <sup>(1)</sup>		20	А
P <sub>D</sub>	Power Dissipation	T <sub>A</sub> = 25°C	1.2	W
$R_{ ext{ ext{ ext{ ext{ ext{ ext{ ext{ ext$	Thermal Resistance, Junction to Ambie	ent <sup>(2)</sup>	104	°C/W
$T_{J},T_{STG}$	Junction & Storage Temperature Rang	je	-55 to 150	°C



#### **Electrical Characteristics** (T<sub>J</sub> = 25°C unless otherwise specified)

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Uni
Off Chara	acteristics					
V <sub>(BR)DSS</sub>	Drain-Source Breakdown Voltage	I <sub>D</sub> = 250μA, V <sub>GS</sub> = 0V	30	-	-	V
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	V <sub>DS</sub> = 30V, V <sub>GS</sub> = 0V	-	-	1.0	μA
I <sub>GSS</sub>	Gate-Body Leakage Current	$V_{DS} = 0V, V_{GS} = \pm 12V$	-	-	±100	nA
On Chara	acteristics				6	
V <sub>GS(th)</sub>	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	0.45	0.8	1.25	V
R <sub>DS(ON)</sub>		V <sub>GS</sub> = 10V, I <sub>D</sub> = 3A	-	22	28	mΩ
	Static Drain-Source ON-Resistance <sup>(3)</sup>	V <sub>GS</sub> = 4.5V, I <sub>D</sub> = 2A	-	24	31	mΩ
		V <sub>GS</sub> = 2.5V, I <sub>D</sub> = 1A	-	29	39	mΩ
Dynamic	Characteristics					
C <sub>iss</sub>	Input Capacitance		Χ-	508	-	pF
C <sub>oss</sub>	Output Capacitance	$V_{GS} = 0V, V_{DS} = 15V,$ f = 1MHz	-	48	-	pF
C <sub>rss</sub>	Reverse Transfer Capacitance		<u> </u>	41	-	pF
Q <sub>g</sub>	Total Gate Charge		-	7	-	nC
$Q_gs$	Gate Source Charge	$V_{GS} = 0$ to 4.5V $V_{DS} = 15V, I_{D} = 3A$	-	1.7	-	nC
$Q_{gd}$	Gate Drain("Miller") Charge	$v_{\rm DS} = 13 v$ , $t_{\rm D} = 3 A$	-	1.6	-	nC
Switchin	g Characteristics					
t <sub>d(on)</sub>	Turn-On DelayTime	-	-	4	-	ns
t <sub>r</sub>	Turn-On Rise Time	V <sub>GS</sub> = 4.5V, V <sub>DD</sub> = 15V	-	17	-	ns
$t_{d(off)}$	Turn-Off DelayTime	$I_D = 3A, R_{GEN} = 3\Omega$	-	95	-	ns
t <sub>f</sub>	Turn-Off Fall Time		-	37	-	ns
Drain-So	urce Diode Characteristics and N	lax Ratings				
I <sub>S</sub>	Maximum Continuous Drain to Source Diode Forward Current		-	-	5	А
I <sub>SM</sub>	Maximum Pulsed Drain to Source Diode Forward Current		-	-	20	А
$V_{SD}$	Drain to Source Diode Forward Voltage	V <sub>GS</sub> = 0V, I <sub>S</sub> = 3A	-	-	1.2	V
trr	Body Diode Reverse Recovery Time		-	6.7	-	ns
Qrr	Body Diode Reverse Recovery Charge	I <sub>F</sub> = 3A, di/dt = 100A/us	-	2.3	-	nC

Notes:

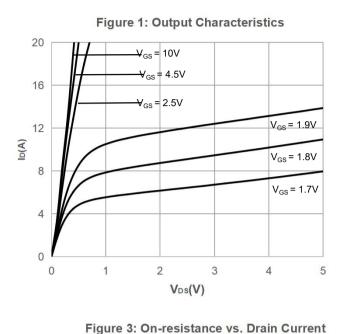
1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature.

2.  $R_{\scriptscriptstyle \theta JA}$  is measured with the device mounted on a 1inch² pad of 2oz copper FR4 PCB

3. Pulse Test: Pulse Width $\leq$ 300µs, Duty Cycle $\leq$ 0.5%.



# **Typical Performance Characteristics**



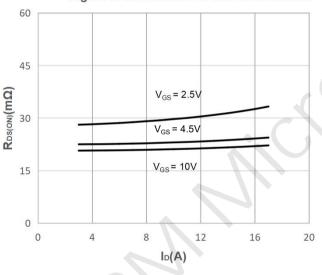
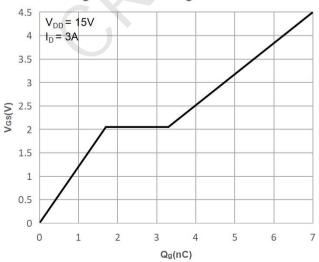


Figure 5: Gate Charge Characteristics



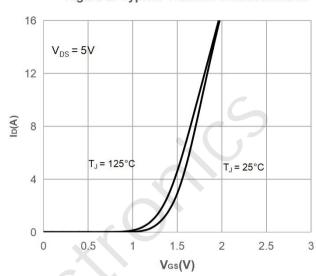


Figure 2: Typical Transfer Characteristics

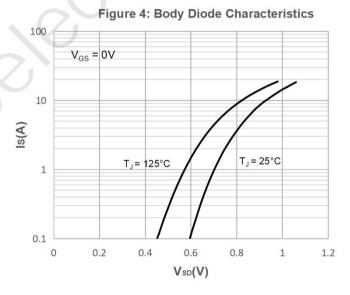
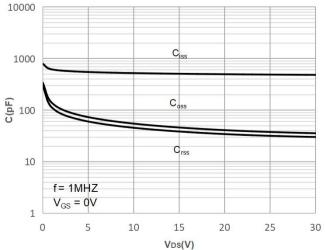


Figure 6: Capacitance Characteristics





## **Typical Performance Characteristics**

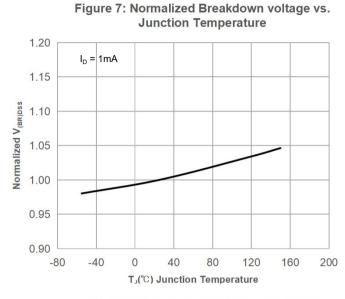
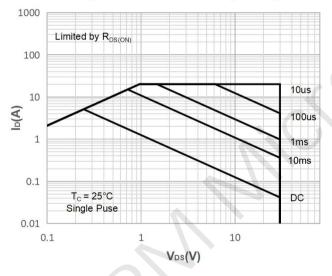
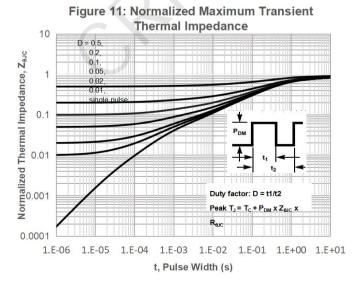


Figure 9: Maximum Safe Operating Area





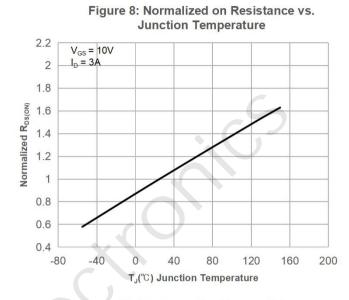


Figure 10: Maximum Continuous Drian Current vs. Case Temperature

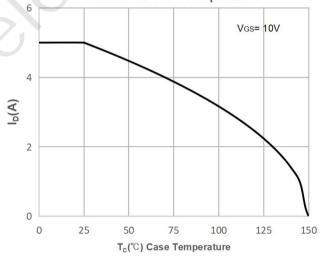
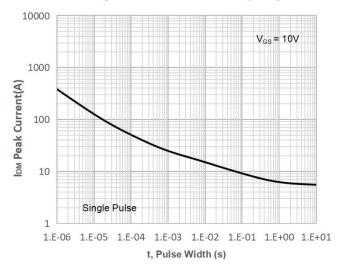
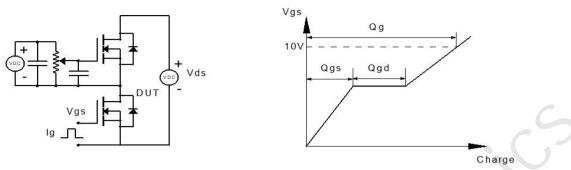


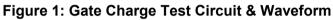
Figure 12: Peak Current Capacity





### **Test Circuit**





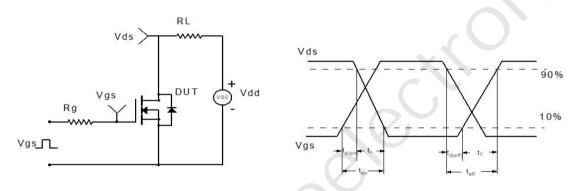


Figure 2: Resistive Switching Test Circuit & Waveform

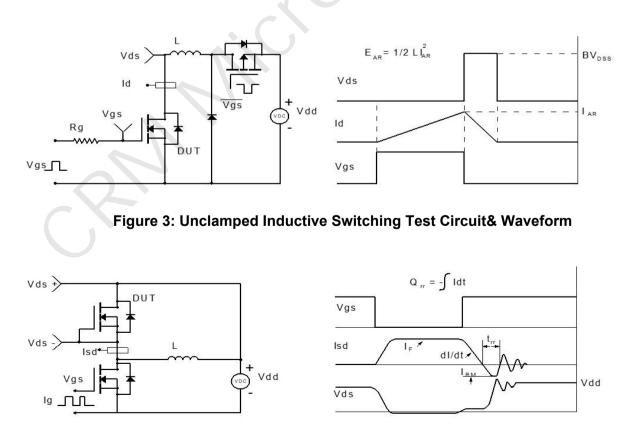
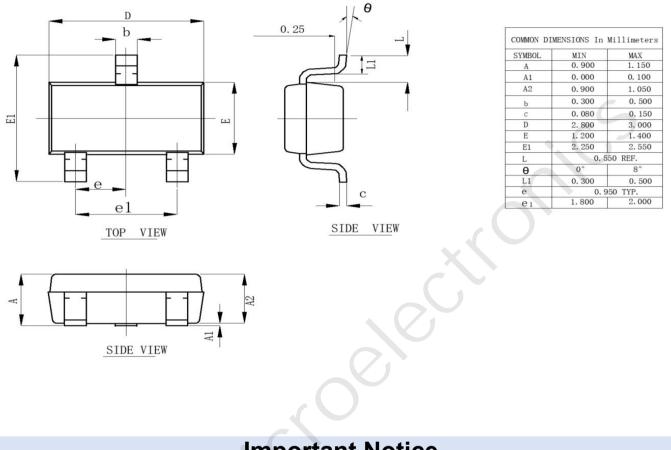


Figure 4: Diode Recovery Test Circuit & Waveform



## Package Mechanical Data(SOT-23)



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## **Contact information**

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