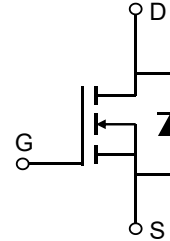


Description

Features

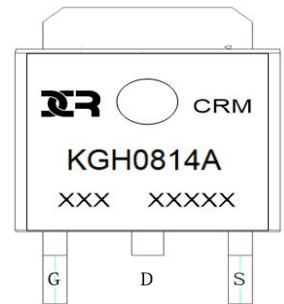
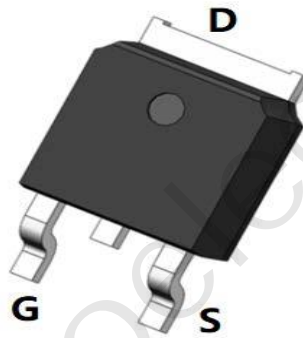
- 85V, 32A
 $R_{DS(ON)}$ Typ = 15mΩ @ $V_{GS} = 10V$
 Advanced Split Gate Trench Technology
- Excellent $R_{DS(ON)}$ and Low Gate Charge
- 100% UIS TESTED!
- 100% ΔV_{ds} TESTED!



Schematic Diagram

Application

- Load Switch
- PWM Application
- Power Management



Marking and Pin Assignment

Package Marking and Ordering Information

| Device | Marking | Package | Outline | Reel Size | Reel (pcs) | Per Carton (pcs) |
|-------------|-------------|-----------|---------|-----------|------------|------------------|
| CRMKGH0814A | CRMKGH0814A | TO-252-3L | TAPING | 13" | 2500 | 25000 |

Absolute Maximum Ratings (@ $T_J = 25^\circ\text{C}$ unless otherwise specified)

| Symbol | Parameter | Value | Units | |
|-----------------------------------|---|------------------------|-------|---|
| V _{DS} | Drain-to-Source Voltage | 85 | V | |
| V _{GS} | Gate-to-Source Voltage | ±20 | V | |
| I _D | Continuous Drain Current | T _C = 25°C | 32 | A |
| | | T _C = 100°C | 19.2 | A |
| I _{DM} | Pulsed Drain Current ⁽¹⁾ | 128 | A | |
| E _{AS} | Single Pulsed Avalanche Energy ⁽²⁾ | 36 | mJ | |
| P _D | Power Dissipation | T _C = 25°C | 42 | W |
| R _{θJC} | Thermal Resistance, Junction to Case | 3 | °C/W | |
| T _J , T _{STG} | Junction & Storage Temperature Range | -55 to 150 | °C | |

Electrical Characteristics ($T_J = 25^\circ\text{C}$ unless otherwise specified)

| Symbol | Parameter | Conditions | Min. | Typ. | Max. | Unit |
|--|--|---|------|------|------|------|
| Off Characteristics | | | | | | |
| V _{(BR)DSS} | Drain-Source Breakdown Voltage | I _D = 250μA, V _{GS} = 0V | 85 | - | - | V |
| I _{DSS} | Zero Gate Voltage Drain Current | V _{DS} = 85V, V _{GS} = 0V | - | - | 1.0 | μA |
| I _{GSS} | Gate-Body Leakage Current | V _{DS} = 0V, V _{GS} = ±20V | - | - | ±100 | nA |
| On Characteristics | | | | | | |
| V _{GS(th)} | Gate Threshold Voltage | V _{DS} = V _{GS} , I _D = 250μA | 2.4 | 3 | 3.6 | V |
| R _{DS(ON)} | Static Drain-Source ON-Resistance ⁽³⁾ | V _{GS} = 10V, I _D = 20A | - | 15 | 19.5 | mΩ |
| Dynamic Characteristics | | | | | | |
| C _{iss} | Input Capacitance | V _{GS} = 0V, V _{DS} = 40V, f = 1MHz | - | 576 | - | pF |
| C _{oss} | Output Capacitance | | - | 197 | - | pF |
| C _{rss} | Reverse Transfer Capacitance | | - | 10 | - | pF |
| Q _g | Total Gate Charge | V _{GS} = 0 to 10V V _{DS} = 40V, I _D = 10A | - | 25 | - | nC |
| Q _{gs} | Gate Source Charge | | - | 8 | - | nC |
| Q _{gd} | Gate Drain("Miller") Charge | | - | 7 | - | nC |
| Switching Characteristics | | | | | | |
| t _{d(on)} | Turn-On DelayTime | V _{GS} = 10V, V _{DD} = 40V I _D = 10A, R _{GEN} = 3Ω | - | 12 | - | ns |
| t _r | Turn-On Rise Time | | - | 8 | - | ns |
| t _{d(off)} | Turn-Off DelayTime | | - | 20 | - | ns |
| t _f | Turn-Off Fall Time | | - | 11 | - | ns |
| Drain-Source Diode Characteristics and Max Ratings | | | | | | |
| I _S | Maximum Continuous Drain to Source Diode Forward Current | | - | - | 32 | A |
| I _{SM} | Maximum Pulsed Drain to Source Diode Forward Current | | - | - | 128 | A |
| V _{SD} | Drain to Source Diode Forward Voltage | V _{GS} = 0V, I _S = 20A | - | - | 1.2 | V |
| trr | Body Diode Reverse Recovery Time | I _F = 10A, di/dt = 100A/us | - | 33 | - | ns |
| Qrr | Body Diode Reverse Recovery Charge | | - | 45 | - | nC |

Notes:

1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature.
2. E_{AS} condition: Starting $T_J = 25^\circ\text{C}$, $V_{DD} = 40\text{V}$, $V_G = 10\text{V}$, $R_G = 25\Omega$, $L = 0.5\text{mH}$, $I_{AS} = 12\text{A}$
3. Pulse Test: Pulse Width $\leq 300\mu\text{s}$, Duty Cycle $\leq 0.5\%$.

Typical Performance Characteristics

Figure 1: Output Characteristics

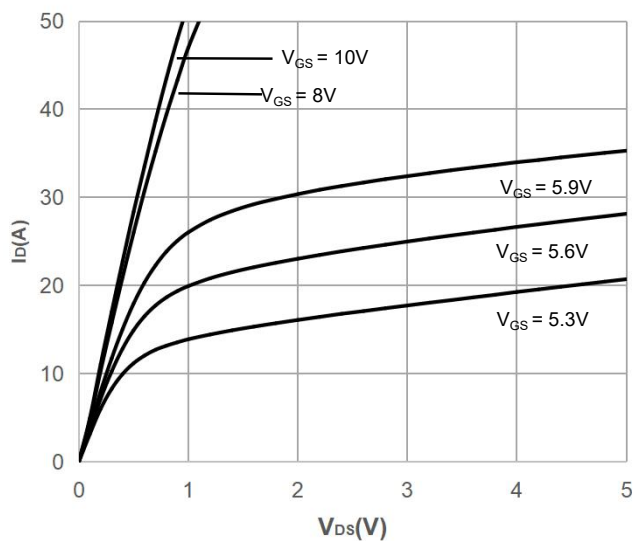


Figure 2: Typical Transfer Characteristics

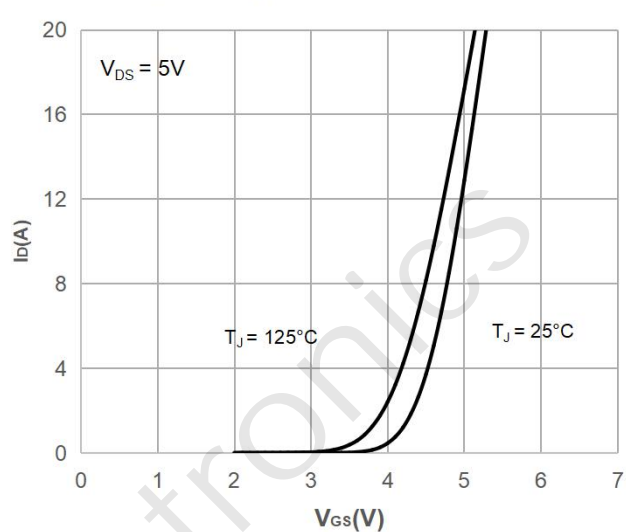


Figure 3: On-resistance vs. Drain Current

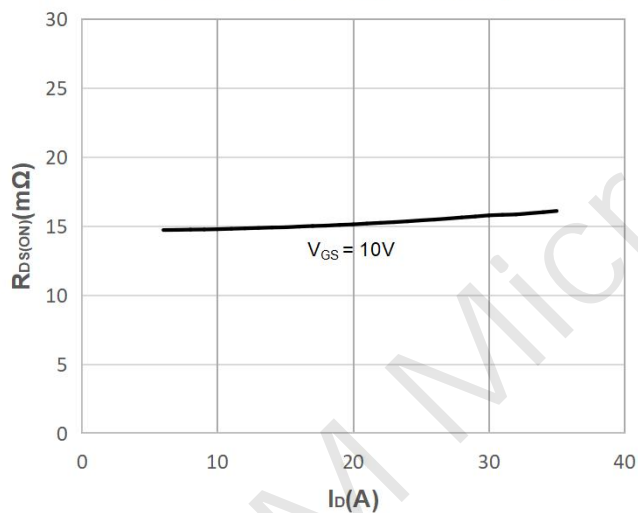


Figure 4: Body Diode Characteristics

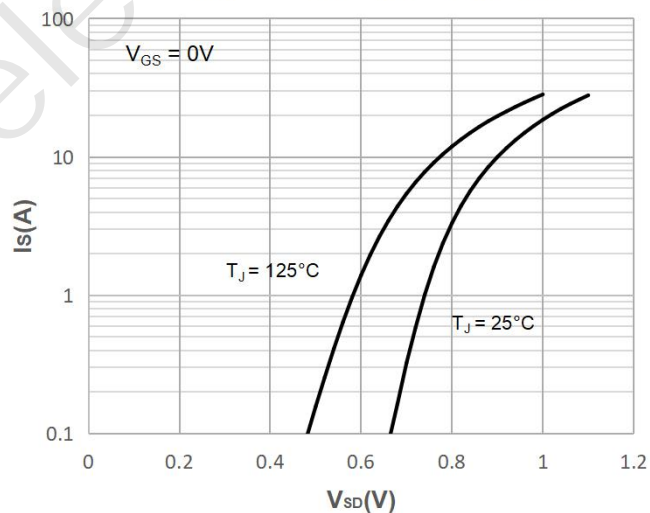


Figure 5: Gate Charge Characteristics

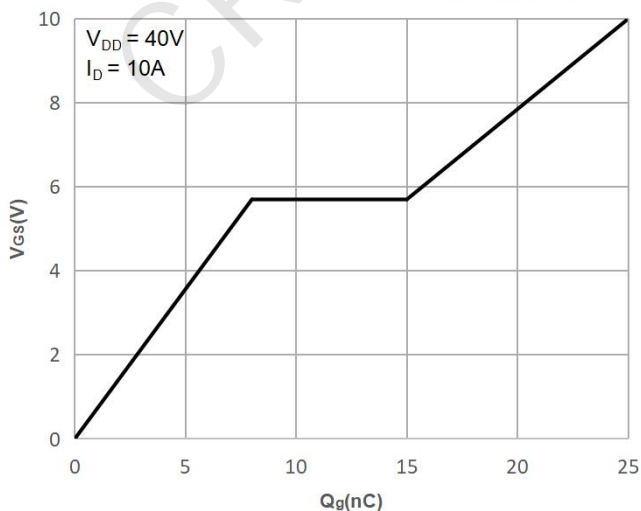
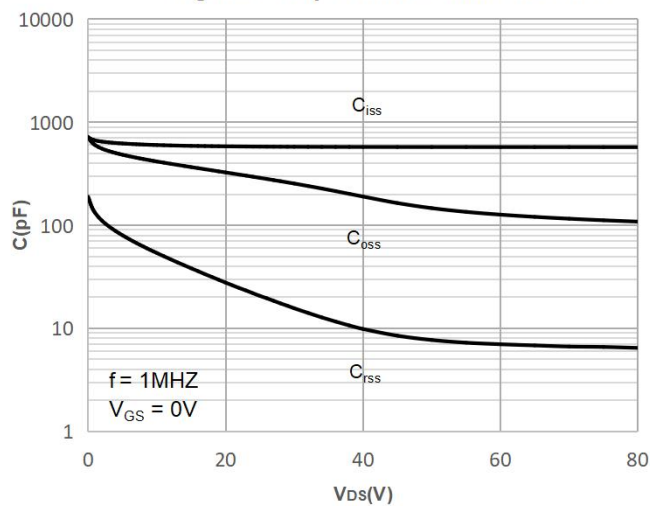


Figure 6: Capacitance Characteristics



Typical Performance Characteristics

Figure 7: Normalized Breakdown voltage vs. Junction Temperature

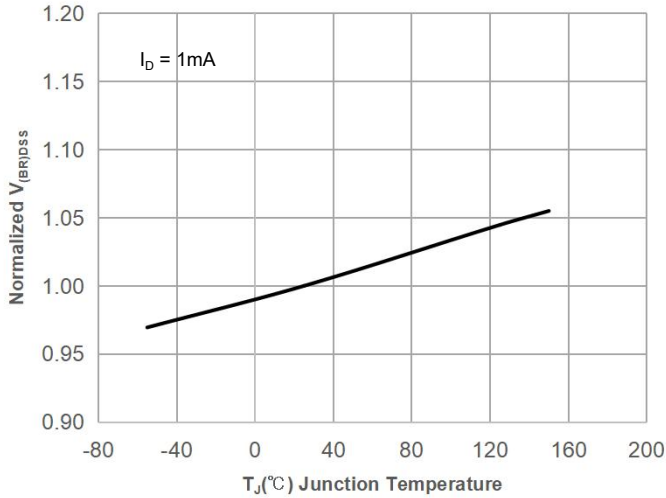


Figure 8: Normalized on Resistance vs. Junction Temperature

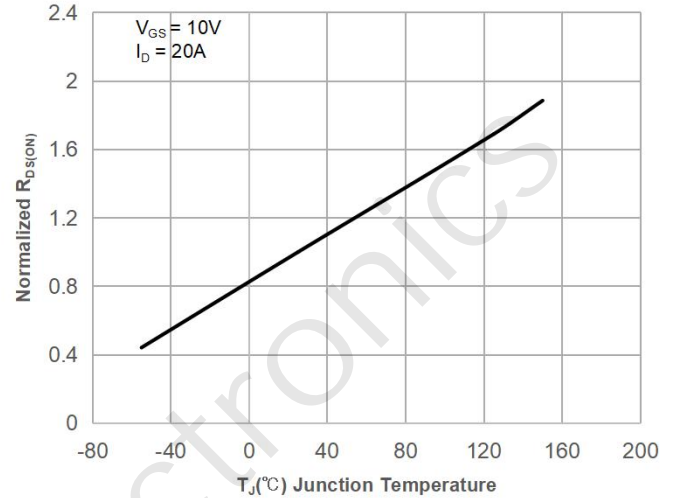


Figure 9: Maximum Safe Operating Area

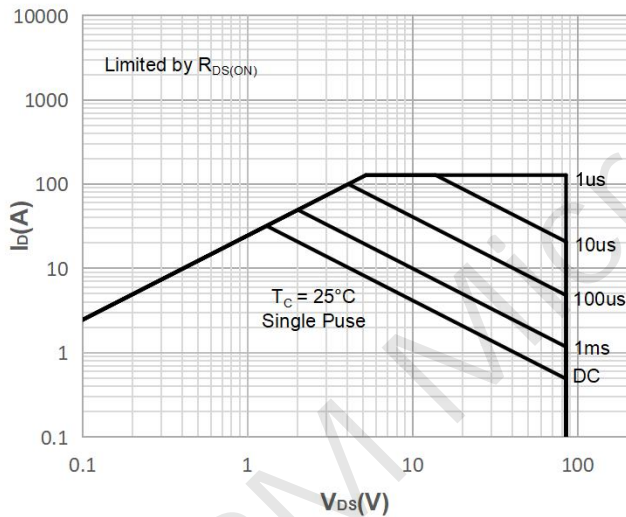


Figure 10: Maximum Continuous Drian Current vs. Case Temperature

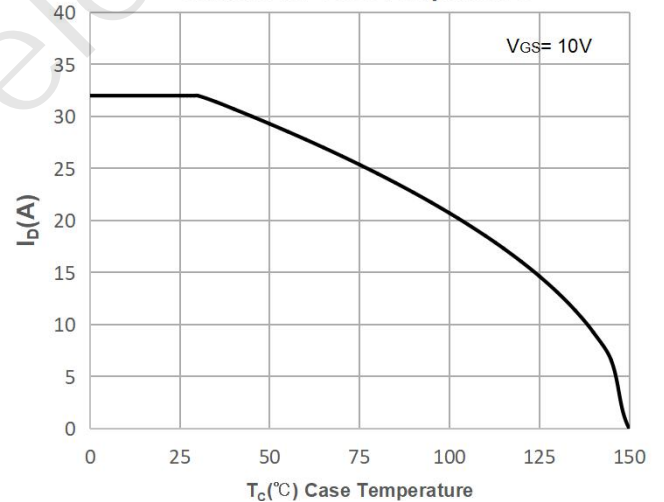


Figure 11: Normalized Maximum Transient Thermal Impedance

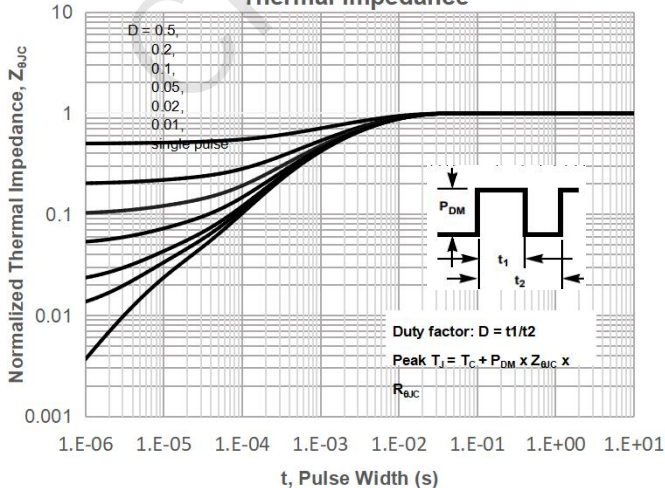
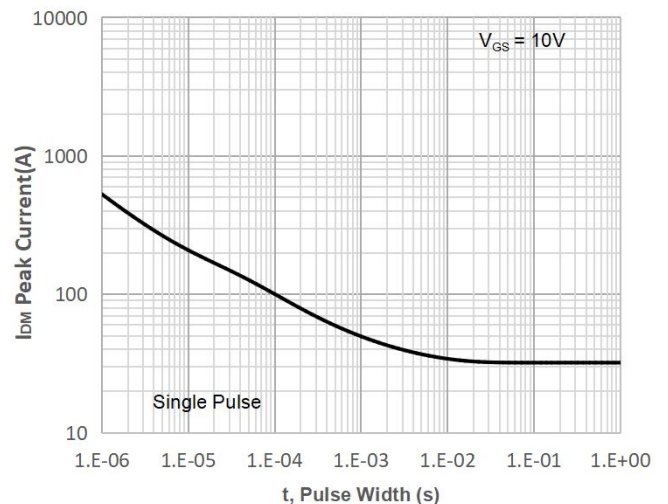


Figure 12: Peak Current Capacity



Test Circuit

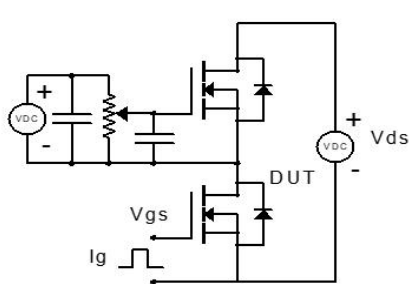


Figure 1: Gate Charge Test Circuit & Waveform

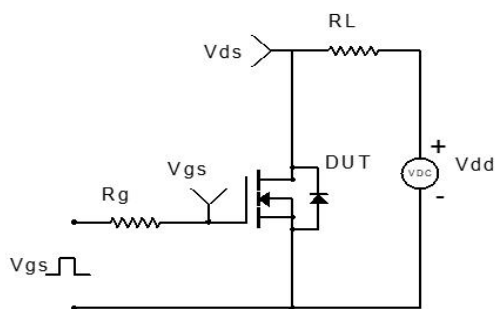


Figure 2: Resistive Switching Test Circuit & Waveform

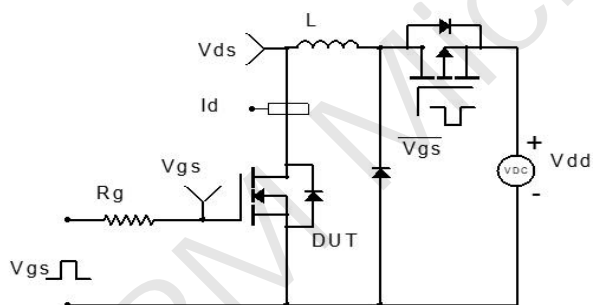


Figure 3: Unclamped Inductive Switching Test Circuit & Waveform

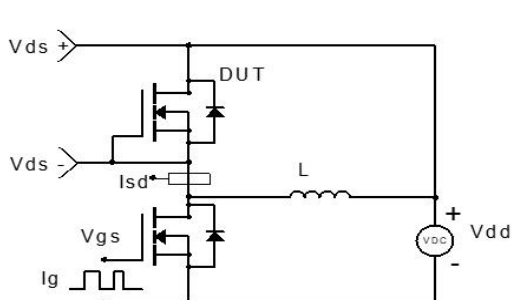
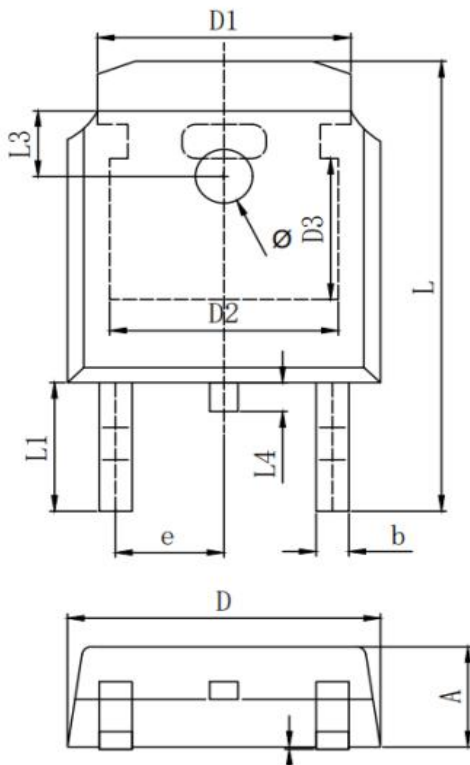


Figure 4: Diode Recovery Test Circuit & Waveform

Package Mechanical Data(TO-252-3L)




| SYMBOL | MILLIMETER | | |
|---------|------------|--------|--------|
| | MIN | Typ. | MAX |
| A | 2.200 | 2.300 | 2.400 |
| A1 | 0.000 | | 0.127 |
| b | 0.640 | 0.690 | 0.740 |
| c (电镀后) | 0.460 | 0.520 | 0.580 |
| D | 6.500 | 6.600 | 6.700 |
| D1 | 5.334 REF | | |
| D2 | 4.826 REF | | |
| D3 | 3.166 REF | | |
| E | 6.000 | 6.100 | 6.200 |
| e | 2.286 TYP | | |
| h | 0.000 | 0.100 | 0.200 |
| L | 9.900 | 10.100 | 10.300 |
| L1 | 2.888 REF | | |
| L2 | 1.400 | 1.550 | 1.700 |
| L3 | 1.600 REF | | |
| L4 | 0.600 | 0.800 | 1.000 |
| Φ | 1.100 | 1.200 | 1.300 |
| θ | 0° | | 8° |
| θ 1 | 9° TYP | | |
| θ 2 | 9° TYP | | |

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