祥瑞微 CRM Microelectronics

CRMGGH1006A

N-Channel 100V, 5.8mΩ Typ. Power MOSFET

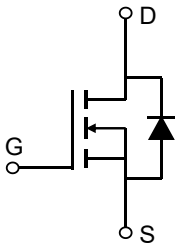
Description

Features

- 100V, 102A

  $R_{DS(ON)}$ Typ = 5.8mΩ @ V_{GS} = 10V

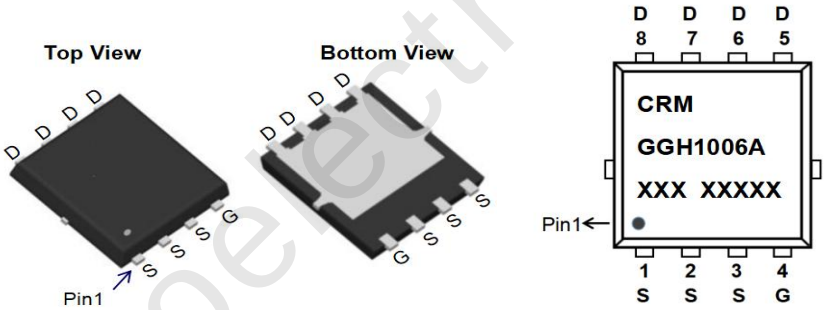
  Advanced Split Gate Trench Technology
- Excellent $R_{DS(ON)}$ and Low Gate Charge
- 100% UIS TESTED!
- 100% ΔVds TESTED!



Schematic Diagram

Application

- Load Switch
- PWM Application
- Power Management



Marking and Pin Assignment

Package Marking and Ordering Information

Device	Marking	Package	Outline	Reel Size	Reel (pcs)	Per Carton (pcs)
CRMGGH1006A	CRMGGH1006A	PDFN5x6-8L	TAPING	13"	5000	50000

Absolute Maximum Ratings (@ T_J = 25°C unless otherwise specified)

Symbol	Parameter		Value	Units
V_{DS}	Drain-to-Source Voltage		100	V
V_{GS}	Gate-to-Source Voltage		±20	V
I_D	Continuous Drain Current	T_C = 25°C	102	A
		T_C = 100°C	61.2	A
I_{DM}	Pulsed Drain Current (1)		408	A
E_{AS}	Single Pulsed Avalanche Energy (2)		150	mJ
P_D	Power Dissipation	T_C = 25°C	132	W
$R_{\theta JC}$	Thermal Resistance, Junction to Case		0.95	°C/W
T_J, T_{STG}	Junction & Storage Temperature Range		-55 to 150	°C

Electrical Characteristics (T_J = 25°C unless otherwise specified)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
Off Characteristics						
$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	I_D = 250μA, V_{GS} = 0V	100	-	-	V
I_{DSS}	Zero Gate Voltage Drain Current	V_{DS} = 100V, V_{GS} = 0V	-	-	1.0	μA
I_{GSS}	Gate-Body Leakage Current	V_{DS} = 0V, V_{GS} = ±20V	-	-	±100	nA
On Characteristics						
$V_{GS(th)}$	Gate Threshold Voltage	V_{DS} = V_{GS}, I_D = 250μA	2.4	3	3.6	V
$R_{DS(ON)}$	Static Drain-Source ON-Resistance(3)	V_{GS} = 10V, I_D = 30A	-	5.8	7.5	mΩ
Dynamic Characteristics						
C_{iss}	Input Capacitance	V_{GS} = 0V, V_{DS} = 50V, f = 1MHz	-	1603	-	pF
C_{oss}	Output Capacitance		-	606	-	pF
C_{rss}	Reverse Transfer Capacitance		-	13	-	pF
Q_g	Total Gate Charge	V_{GS} = 0 to 10V V_{DS} = 50V, I_D = 25A	-	25	-	nC
Q_{gs}	Gate Source Charge		-	12	-	nC
Q_{gd}	Gate Drain("Miller") Charge		-	5	-	nC
Switching Characteristics						
$t_{d(on)}$	Turn-On DelayTime	V_{GS} = 10V, V_{DD} = 50V I_D= 25A, R_{GEN} = 3Ω	-	12	-	ns
t_r	Turn-On Rise Time		-	70	-	ns
$t_{d(off)}$	Turn-Off DelayTime		-	35	-	ns
t_f	Turn-Off Fall Time		-	15	-	ns
Drain-Source Diode Characteristics and Max Ratings						
I_S	Maximum Continuous Drain to Source Diode Forward Current		-	-	102	A
I_{SM}	Maximum Pulsed Drain to Source Diode Forward Current		-	-	408	A
V_{SD}	Drain to Source Diode Forward Voltage	V_{GS} = 0V, I_S = 30A	-	-	1.2	V
trr	Body Diode Reverse Recovery Time	I_F = 25A, di/dt = 100A/us	-	65	-	ns
Qrr	Body Diode Reverse Recovery Charge		-	90	-	nC

Notes:

1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature.
2. E_{AS} condition: Starting T_J=25°C, V_{DD}=50V, V_G=10V, R_G=25ohm, L=0.5mH, I_{AS}=24.5A
3. Pulse Test: Pulse Width≤300μs, Duty Cycle≤0.5%.

Test Circuit

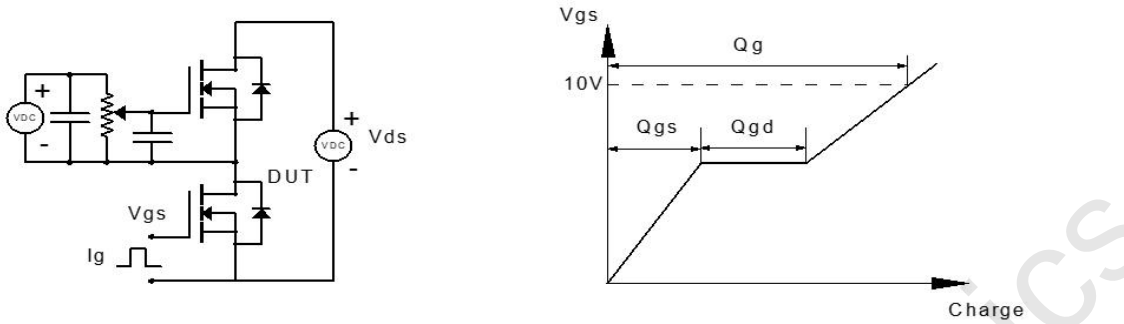


Figure 1: Gate Charge Test Circuit & Waveform

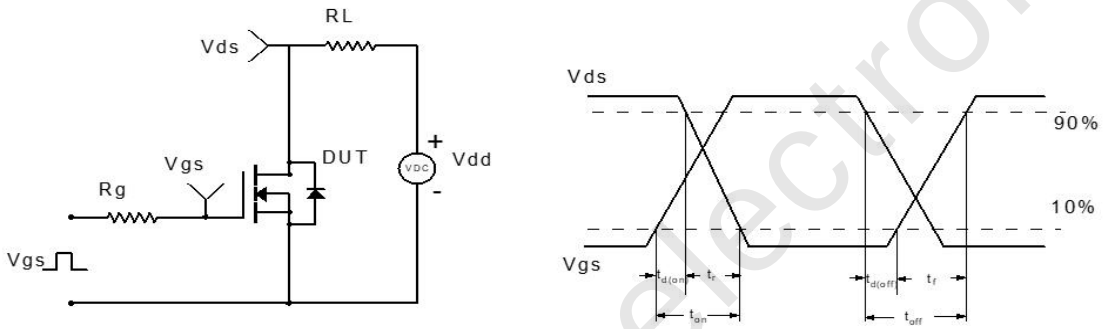


Figure 2: Resistive Switching Test Circuit & Waveform

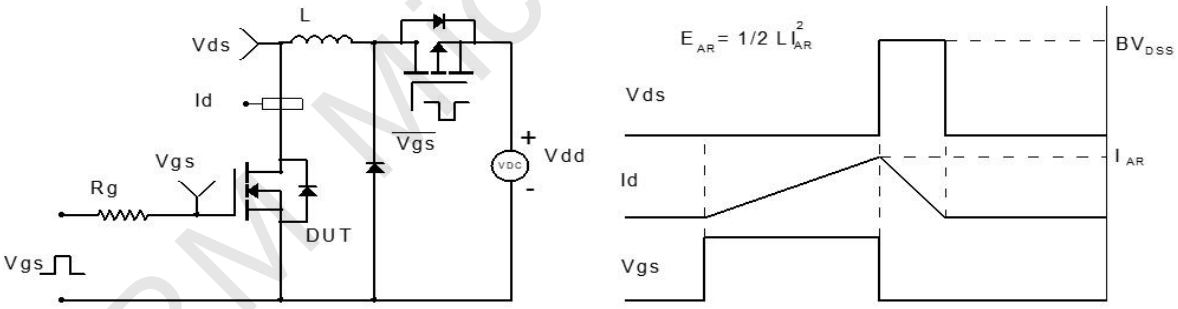


Figure 3: Unclamped Inductive Switching Test Circuit& Waveform

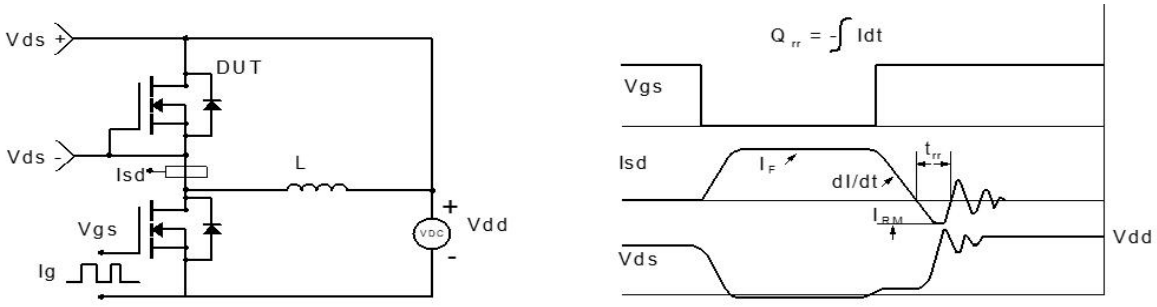
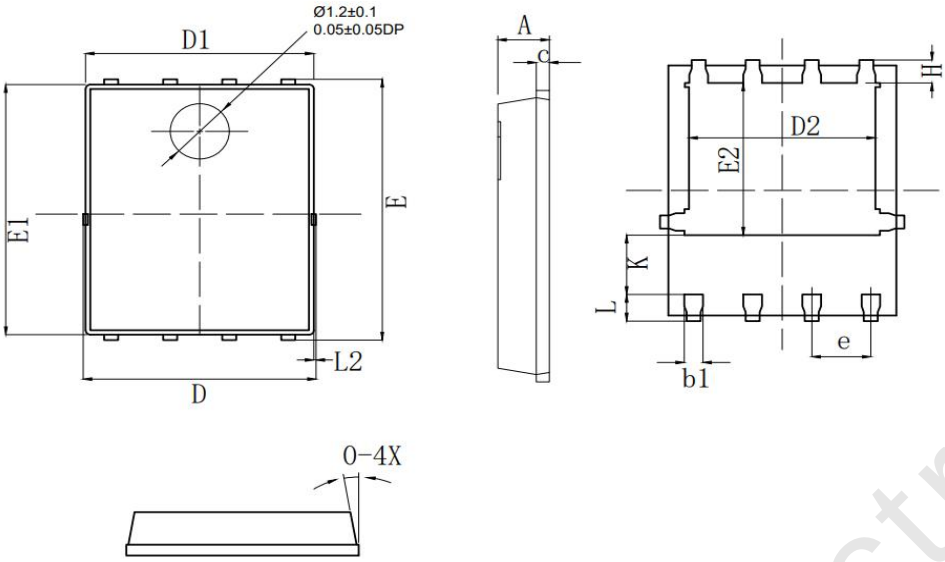


Figure 4: Diode Recovery Test Circuit & Waveform

Package Mechanical Data(PDFN5x6-8L)



SYMBOL	MILLIMETER		
	MIN	NOM	MAX
A	0.90	1.00	1.10
b	0.25	0.30	0.35
b1	0.30	0.40	0.45
c	0.22	0.25	0.28
D	——	——	5.30
D1	4.90	5.05	5.20
D2		3.90REF	
E	6.00	6.15	6.30
E1	5.70	5.85	6.00
E2		3.50REF	
e	1.10	1.27	1.40
H	0.51	0.61	0.71
K	1.10	——	——
L	0.51	0.61	0.71
L2	——	——	0.10
Φ	8°	——	12°

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