CRMKGL0604A

N-Channel 60V, 3.8mΩ Typ. Power MOSFET

Description

Features

• 60V, 91A

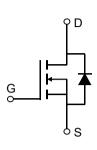
$$R_{DS(ON)}$$
 Typ = 3.8m Ω @ V_{GS} = 10 V

$$R_{DS(ON)}$$
 Typ = 5m Ω @ V_{GS} = 4.5V

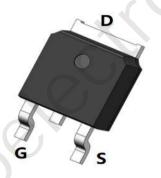
- Advanced Split Gate Trench Technology
- Excellent R_{DS(ON)} and Low Gate Charge
- 100% UIS TESTED!
- 100% ΔVds TESTED!

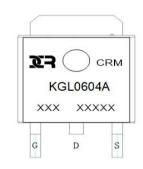
Application

- Load Switch
- PWM Application
- Power Management









Marking and Pin Assignment

Package Marking and Ordering Information

Device	Marking	Package	Outline	Reel Size	Reel (pcs)	Per Carton (pcs)
CRMKGL0604A	CRMKGL0604A	TO-252-3L	TAPING	13"	2500	25000

Absolute Maximum Ratings (@ T_J = 25°C unless otherwise specified)

Symbol	Parameter		Value	Units
V_{DS}	Drain-to-Source Voltage		60	V
V_{GS}	Gate-to-Source Voltage		±20	V
	Continuous Drain Current	T _C = 25°C	91	Α
I _D	Continuous Drain Current	T _C = 100°C	54.6	А
I_{DM}	Pulsed Drain Current (1)		364	Α
E _{AS}	Single Pulsed Avalanche Energy (2)		127	mJ
P_{D}	Power Dissipation	T _C = 25°C	69.4	W
$R_{ heta JC}$	Thermal Resistance, Junction to Case		1.8	°C/W
T_J,T_STG	Junction & Storage Temperature Range		-55 to 150	°C

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Electrical Characteristics (T_J = 25°C unless otherwise specified)

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
Off Chara	acteristics					
V _{(BR)DSS}	Drain-Source Breakdown Voltage	$I_D = 250 \mu A, V_{GS} = 0 V$	60	-	-	V
I _{DSS}	Zero Gate Voltage Drain Current	$V_{DS} = 60V, V_{GS} = 0V$	-	-	1.0	μА
I _{GSS}	Gate-Body Leakage Current	$V_{DS} = 0V, V_{GS} = \pm 20V$	-	-	±100	nA
On Chara	acteristics				6	
$V_{\text{GS(th)}}$	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	1.2	1.7	2.5	V
Б	Static Drain-Source ON-Resistance ⁽³⁾	$V_{GS} = 10V, I_D = 20A$	-	3.8	4.9	mΩ
$R_{DS(ON)}$	Static Drain-Source ON-Resistance	$V_{GS} = 4.5V, I_{D} = 10A$	-	5	6.5	mΩ
Dynamic	Characteristics					
C_{iss}	Input Capacitance		-	2000	-	pF
C_{oss}	Output Capacitance	$V_{GS} = 0V, V_{DS} = 30V,$ f = 1MHz	X-\	660	-	pF
C_{rss}	Reverse Transfer Capacitance			28	-	pF
Q_g	Total Gate Charge		U -	35	-	nC
Q_gs	Gate Source Charge	$V_{GS} = 0 \text{ to } 10V$ $V_{DS} = 30V, I_D = 20A$	-	10	-	nC
Q_gd	Gate Drain("Miller") Charge	VDS = 00 V, 1D = 20/1	-	7	-	nC
Switchin	g Characteristics					
$t_{d(on)}$	Turn-On DelayTime	.()	-	12	-	ns
t_r	Turn-On Rise Time	$V_{GS} = 10V, V_{DD} = 30V$	-	34	-	ns
$t_{\text{d(off)}}$	Turn-Off DelayTime	I_{D} = 20A, R_{GEN} = 4.5 Ω	-	25	-	ns
t_{f}	Turn-Off Fall Time		-	30	-	ns
Drain-So	urce Diode Characteristics and M	lax Ratings				
I _S	Maximum Continuous Drain to Source Di	ode Forward Current	-	-	91	Α
I _{SM}	Maximum Pulsed Drain to Source Diode	Forward Current	-	-	364	Α
V_{SD}	Drain to Source Diode Forward Voltage	$V_{GS} = 0V, I_{S} = 20A$	-	-	1.2	V
trr	Body Diode Reverse Recovery Time	1 - 204 di/dt - 4004/:	-	38	-	ns
Qrr	Body Diode Reverse Recovery Charge	$I_F = 20A$, di/dt = 100A/us	-	23	-	nC

Notes:

^{1.} Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature.

^{2.} E_{AS} condition: Starting T_J =25°C, V_{DD} =30V, V_G =10V, R_G =25ohm, L=0.5mH, I_{AS} =22.5A

^{3.} Pulse Test: Pulse Width≤300µs, Duty Cycle≤0.5%.

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Test Circuit

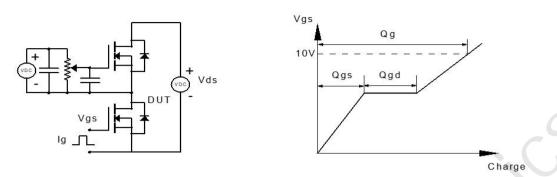


Figure 1: Gate Charge Test Circuit & Waveform

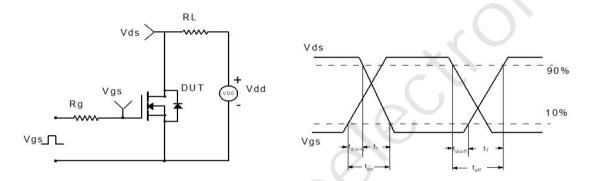


Figure 2: Resistive Switching Test Circuit & Waveform

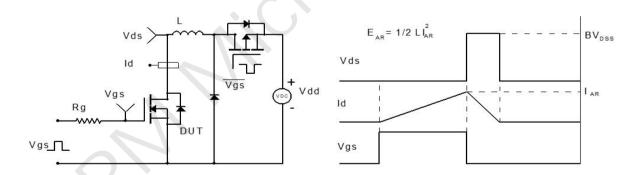


Figure 3: Unclamped Inductive Switching Test Circuit& Waveform

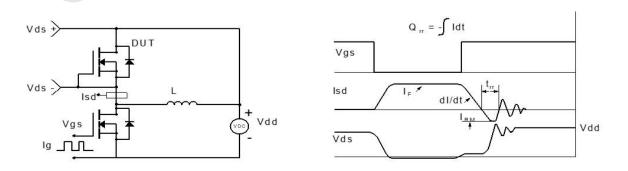
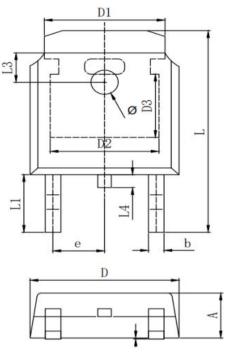


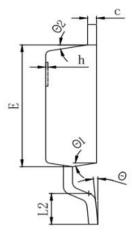
Figure 4: Diode Recovery Test Circuit & Waveform

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Package Mechanical Data(TO-252-3L)





SYMBOL	MILLIMETER				
SIMBOL	MIN	Тур.	MAX		
A	2.200	2.300	2. 400		
A1	0.000		0. 127		
b	0.640	0.690	0.740		
(电镀后)	0.460	0.520	0.580		
D	6.500	6.600	6. 700		
D1	5. 334 REF				
D2	4. 826 REF				
D3	3. 166 REF				
Е	6.000	6. 100	6. 200		
e	2. 286 TYP				
h	0.000	0.100	0. 200		
L	9.900	10.100	10.300		
L1	2. 888 REF				
L2	1.400	1.550	1.700		
L3	1.600 REF				
L4	0.600	0.800	1.000		
ф	1. 100	1.200	1. 300		
θ	0°		8°		
θ 1	9° TYP				
θ2	9° TYP				

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