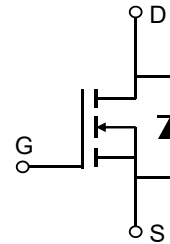


Description

Features

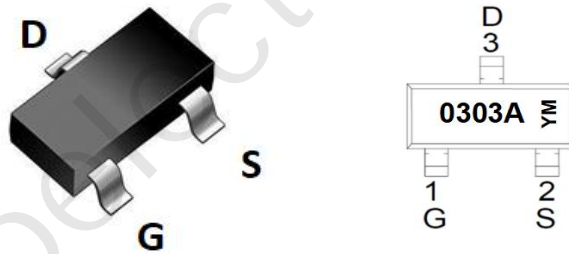
- 30V, 10A
 $R_{DS(ON)}$ Typ = 8.7mΩ @ $V_{GS} = 10V$
 $R_{DS(ON)}$ Typ = 11.7mΩ @ $V_{GS} = 4.5V$
- Advanced Split Gate Trench Technology
- Excellent $R_{DS(ON)}$ and Low Gate Charge
- Lead Free



Schematic Diagram

Application

- Load Switch
- PWM Application
- Power Management



Marking and Pin Assignment

Package Marking and Ordering Information

Device	Marking	Package	Outline	Reel Size	Reel (pcs)	Per Carton (pcs)
CRMJGL0303A	0303A	SOT-23-3L	TAPING	7"	3000	120000

Absolute Maximum Ratings (@ $T_J = 25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter	Value	Units	
V _{DS}	Drain-to-Source Voltage	30	V	
V _{GS}	Gate-to-Source Voltage	±20	V	
I _D	Continuous Drain Current	T _A = 25°C	10	A
		T _A = 100°C	6	A
I _{DM}	Pulsed Drain Current ⁽¹⁾	40	A	
P _D	Power Dissipation	T _A = 25°C	2	W
R _{θJA}	Thermal Resistance, Junction to Ambient ⁽²⁾	60	°C/W	
T _J , T _{STG}	Junction & Storage Temperature Range	-55 to 150	°C	

Electrical Characteristics ($T_J = 25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
Off Characteristics						
V _{(BR)DSS}	Drain-Source Breakdown Voltage	I _D = 250μA, V _{GS} = 0V	30	-	-	V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = 30V, V _{GS} = 0V	-	-	1.0	μA
I _{GSS}	Gate-Body Leakage Current	V _{DS} = 0V, V _{GS} = ±20V	-	-	±100	nA
On Characteristics						
V _{GS(th)}	Gate Threshold Voltage	V _{DS} = V _{GS} , I _D = 250μA	1.2	1.8	2.4	V
R _{DS(ON)}	Static Drain-Source ON-Resistance ⁽³⁾	V _{GS} = 10V, I _D = 5A	-	8.7	11.3	mΩ
		V _{GS} = 4.5V, I _D = 3A	-	11.7	15.2	mΩ
Dynamic Characteristics						
C _{iss}	Input Capacitance	V _{GS} = 0V, V _{DS} = 15V, f = 1MHz	-	920	-	pF
C _{oss}	Output Capacitance		-	793	-	pF
C _{rss}	Reverse Transfer Capacitance		-	47	-	pF
Q _g	Total Gate Charge	V _{GS} = 0 to 10V V _{DS} = 15V, I _D = 5A	-	16	-	nC
Q _{gs}	Gate Source Charge		-	3	-	nC
Q _{gd}	Gate Drain("Miller") Charge		-	3.3	-	nC
Switching Characteristics						
t _{d(on)}	Turn-On DelayTime	V _{GS} = 10V, V _{DD} = 15V I _D = 5A, R _{GEN} = 3Ω	-	6.3	-	ns
t _r	Turn-On Rise Time		-	3.2	-	ns
t _{d(off)}	Turn-Off DelayTime		-	18	-	ns
t _f	Turn-Off Fall Time		-	3.6	-	ns
Drain-Source Diode Characteristics and Max Ratings						
I _S	Maximum Continuous Drain to Source Diode Forward Current	V _{GS} = 0V, I _S = 5A	-	-	10	A
I _{SM}	Maximum Pulsed Drain to Source Diode Forward Current		-	-	40	A
V _{SD}	Drain to Source Diode Forward Voltage		-	-	1.2	V
trr	Body Diode Reverse Recovery Time		-	27	-	ns
Qrr	Body Diode Reverse Recovery Charge		I _F = 5A, di/dt = 100A/us	-	11	-

- Notes:
1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature.
 2. $R_{\theta JA}$ is measured with the device mounted on a 1inch² pad of 2oz copper FR4 PCB
 3. Pulse Test: Pulse Width $\leq 300\mu\text{s}$, Duty Cycle $\leq 0.5\%$.

Test Circuit

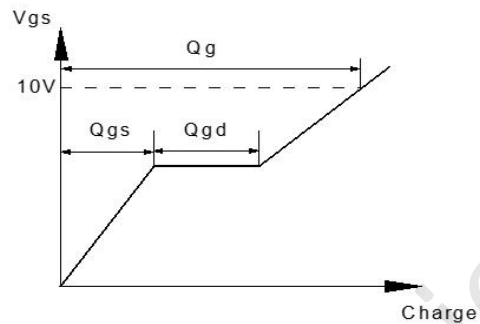
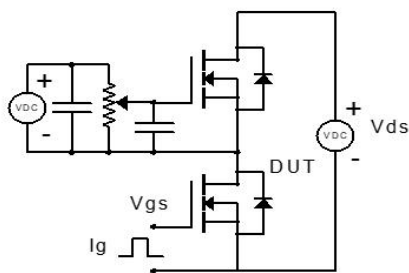


Figure 1: Gate Charge Test Circuit & Waveform



Figure 2: Resistive Switching Test Circuit & Waveform

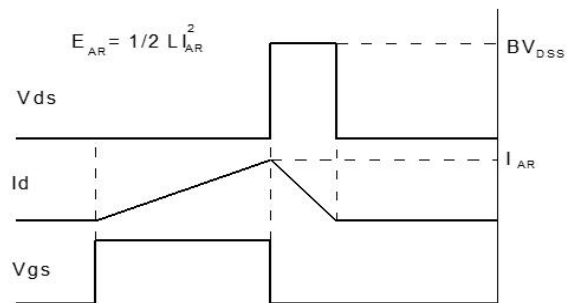
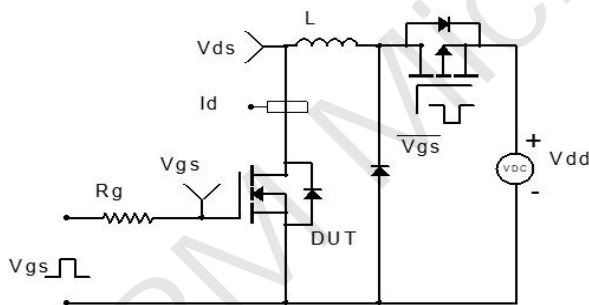
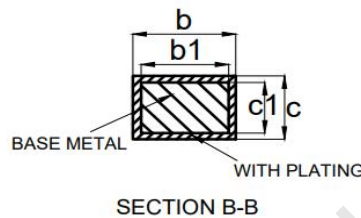
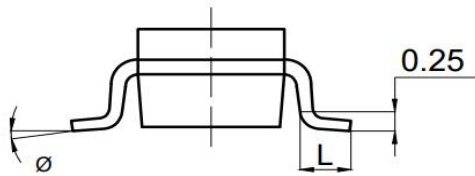
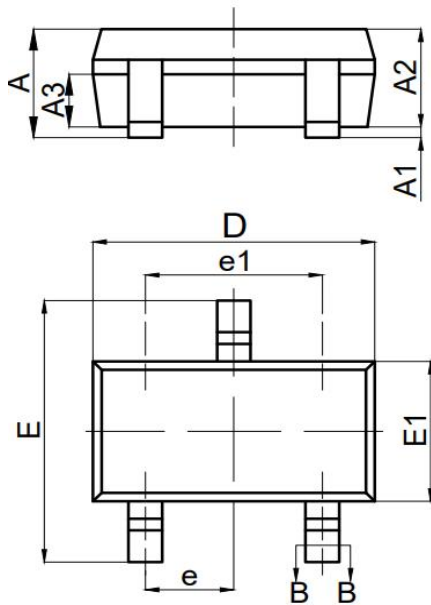


Figure 3: Unclamped Inductive Switching Test Circuit & Waveform



Figure 4: Diode Recovery Test Circuit & Waveform

Package Mechanical Data(SOT-23-3L)




SYMBOL	MILLIMETER		
	MIN	NOM	MAX
A	—	—	1.25
A1	0.04	—	0.10
A2	1.00	1.10	1.20
A3	0.55	0.65	0.75
b	0.30	—	0.40
b1	0.37	0.40	0.43
c	0.11	—	0.21
c1	0.10	0.13	0.16
D	2.72	2.92	3.12
E	2.60	2.80	3.00
E1	1.40	1.60	1.80
e	0.95BSC		
e1	1.90BSC		
L	0.30	—	0.60
Ø	0	—	8°

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