CRMGGL1002A

N-Channel 100V, 2.3mΩ Typ. Power MOSFET

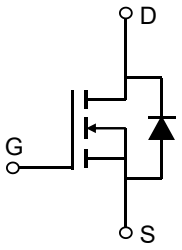
Description

Features

- 100V, 165A

  $R_{DS(ON)}$ Typ = 2.3mΩ @ V_{GS} = 10V

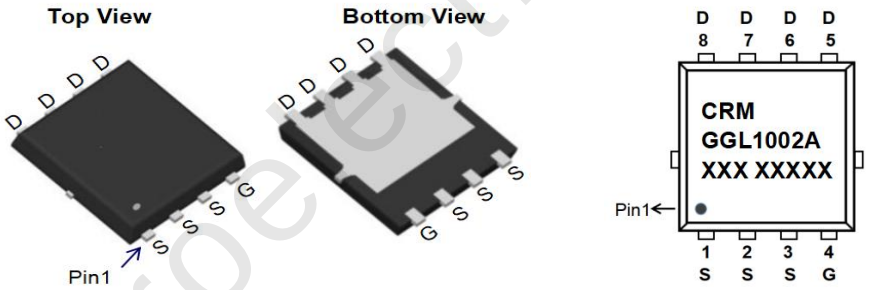
  $R_{DS(ON)}$ Typ = 3mΩ @ V_{GS} = 4.5V
- Advanced Split Gate Trench Technology
- Excellent $R_{DS(ON)}$ and Low Gate Charge
- 100% UIS TESTED!
- 100% ΔVds TESTED!



Schematic Diagram

Application

- Load Switch
- PWM Application
- Power Management



Marking and Pin Assignment

Package Marking and Ordering Information

Device	Marking	Package	Outline	Reel Size	Reel (pcs)	Per Carton (pcs)
CRMGGL1002A	CRMGGL1002A	PDFN5x6-8L	TAPING	13"	5000	60000

Absolute Maximum Ratings (@ T_J = 25°C unless otherwise specified)

Symbol	Parameter		Value	Units
V_{DS}	Drain-to-Source Voltage		100	V
V_{GS}	Gate-to-Source Voltage		±20	V
I_D	Continuous Drain Current	T_C = 25°C	165	A
		T_C = 100°C	99	A
I_{DM}	Pulsed Drain Current (1)		660	A
E_{AS}	Single Pulsed Avalanche Energy (2)		484	mJ
P_D	Power Dissipation	T_C = 25°C	167	W
$R_{\theta JC}$	Thermal Resistance, Junction to Case		0.75	°C/W
T_J, T_{STG}	Junction & Storage Temperature Range		-55 to 150	°C

Electrical Characteristics (T_J = 25°C unless otherwise specified)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
Off Characteristics						
$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	I_D = 250μA, V_{GS} = 0V	100	-	-	V
I_{DSS}	Zero Gate Voltage Drain Current	V_{DS} = 100V, V_{GS} = 0V	-	-	1.0	μA
I_{GSS}	Gate-Body Leakage Current	V_{DS} = 0V, V_{GS} = ±20V	-	-	±100	nA
On Characteristics						
$V_{GS(th)}$	Gate Threshold Voltage	V_{DS} = V_{GS}, I_D = 250μA	1	1.5	2	V
$R_{DS(ON)}$	Static Drain-Source ON-Resistance(3)	V_{GS} = 10V, I_D = 30A	-	2.3	3	mΩ
		V_{GS} = 4.5V, I_D = 25A	-	3	3.9	mΩ
Dynamic Characteristics						
C_{iss}	Input Capacitance	V_{GS} = 0V, V_{DS} = 50V, f = 100KHz	-	4962	-	pF
C_{oss}	Output Capacitance		-	1509	-	pF
C_{rss}	Reverse Transfer Capacitance		-	39	-	pF
Q_g	Total Gate Charge	V_{GS} = 0 to 10V V_{DS} = 50V, I_D = 30A	-	35	-	nC
Q_{gs}	Gate Source Charge		-	12.5	-	nC
Q_{gd}	Gate Drain("Miller") Charge		-	11	-	nC
Switching Characteristics						
$t_{d(on)}$	Turn-On DelayTime	V_{GS} = 10V, V_{DD} = 50V I_D= 30A, R_{GEN} = 3Ω	-	10	-	ns
t_r	Turn-On Rise Time		-	9	-	ns
$t_{d(off)}$	Turn-Off DelayTime		-	38	-	ns
t_f	Turn-Off Fall Time		-	15	-	ns
Drain-Source Diode Characteristics and Max Ratings						
I_S	Maximum Continuous Drain to Source Diode Forward Current		-	-	165	A
I_{SM}	Maximum Pulsed Drain to Source Diode Forward Current		-	-	660	A
V_{SD}	Drain to Source Diode Forward Voltage	V_{GS} = 0V, I_S = 30A	-	-	1.2	V
trr	Body Diode Reverse Recovery Time	I_F = 30A, di/dt = 100A/us	-	39	-	ns
Qrr	Body Diode Reverse Recovery Charge		-	48	-	nC

Notes:

1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature.
2. E_{AS} condition: Starting T_J=25°C, V_{DD}=50V, V_G=10V, R_G=25ohm, L=0.5mH, I_{AS}=44A
3. Pulse Test: Pulse Width≤300μs, Duty Cycle≤0.5%.

Test Circuit

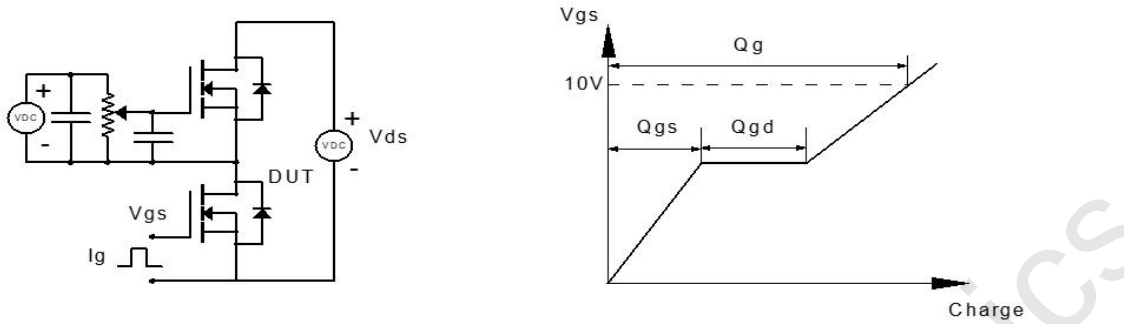


Figure 1: Gate Charge Test Circuit & Waveform

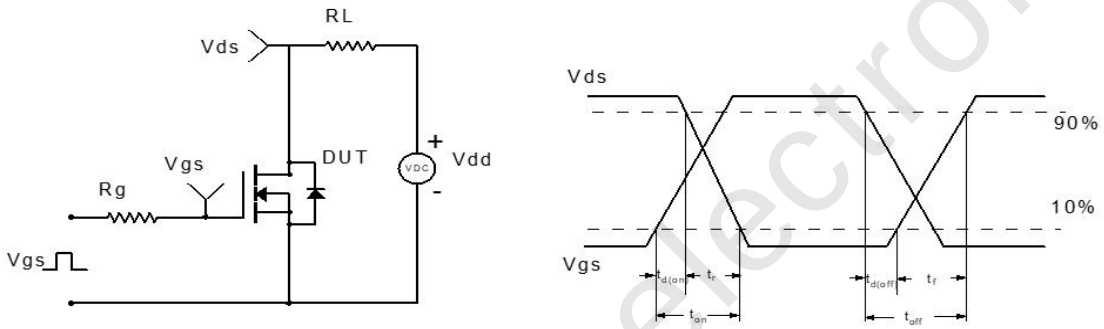


Figure 2: Resistive Switching Test Circuit & Waveform

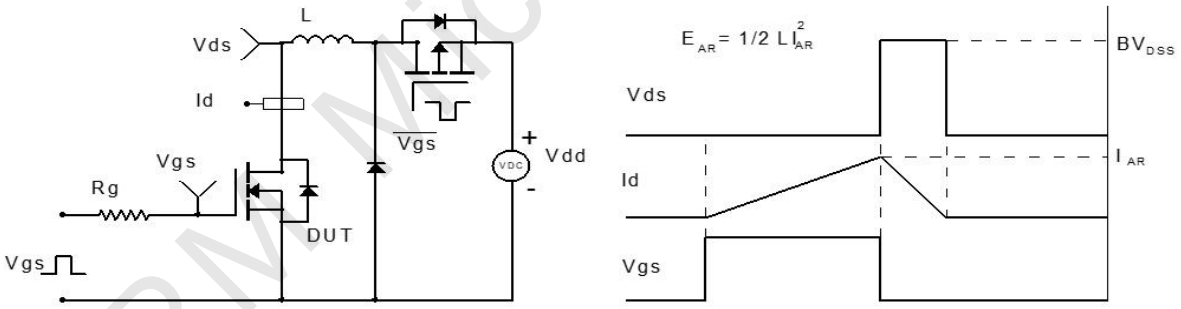


Figure 3: Unclamped Inductive Switching Test Circuit& Waveform

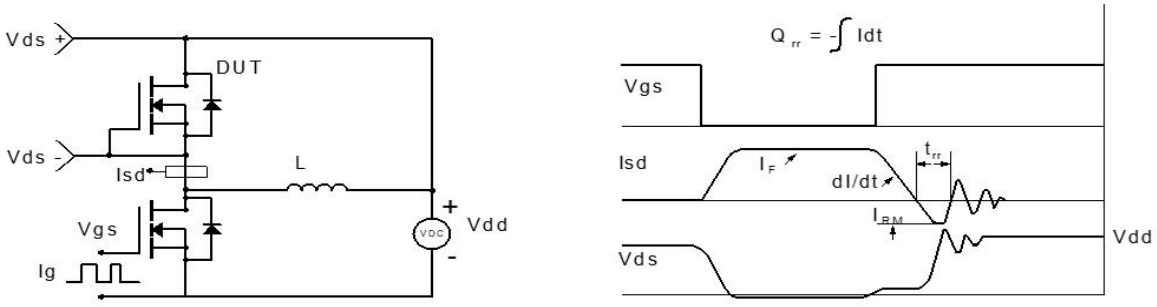
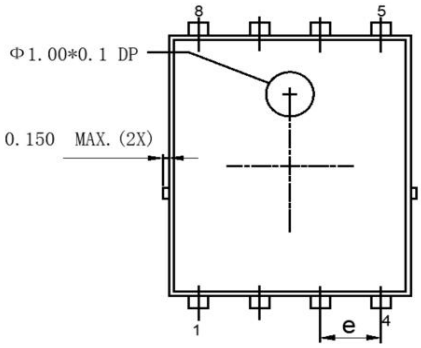
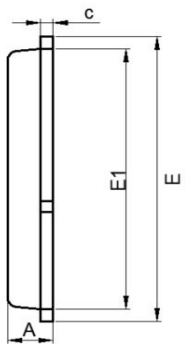


Figure 4: Diode Recovery Test Circuit & Waveform

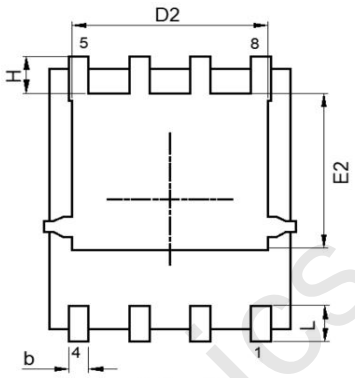
Package Mechanical Data(PDFN5x6-8L)



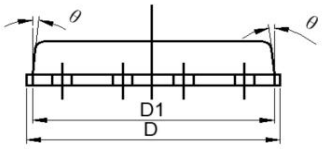
Top View



Side View



Bottom View



Front View

DIM.	MILLIMETER		
	MIN.	NOM.	MAX.
A	0.90	1.00	1.10
b	0.31	0.41	0.51
c	0.21	0.25	0.34
D	5.05	5.20	5.40
D1	4.95	5.05	5.15
D2	4.00	4.10	4.20
E	6.30	6.40	6.50
E1	5.75	5.85	5.95
E2	3.43	3.53	3.63
e	1.27BSC		
H	0.73	0.83	0.93
L	0.61	0.71	0.81
θ	0°	--	12°

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