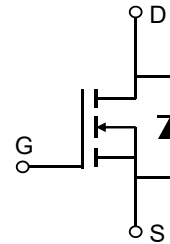


Description

Features

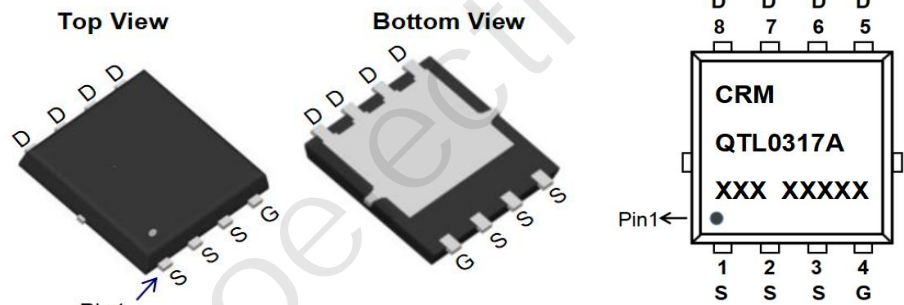
- 30V, 16A
- $R_{DS(ON)}$ Typ = 13.7mΩ @ $V_{GS} = 10V$
- $R_{DS(ON)}$ Typ = 23mΩ @ $V_{GS} = 4.5V$
- Advanced Trench Technology
- Excellent $R_{DS(ON)}$ and Low Gate Charge
- Lead Free
- 100% UIS TESTED!
- 100% ΔV_{DS} TESTED!



Schematic Diagram

Application

- Load Switch
- PWM Application
- Power Management



Marking and Pin Assignment

Package Marking and Ordering Information

Device	Marking	Package	Outline	Reel Size	Reel (pcs)	Per Carton (pcs)
CRMQTL0317A	CRMQTL0317A	PDFN3.3x3.3-8L	TAPING	13"	5000	50000

Absolute Maximum Ratings (@ $T_J = 25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter	Value	Units	
V _{DS}	Drain-to-Source Voltage	30	V	
V _{GS}	Gate-to-Source Voltage	±20	V	
I _D	Continuous Drain Current	T _C = 25°C	16	A
		T _C = 100°C	9.6	A
I _{DM}	Pulsed Drain Current ⁽¹⁾	64	A	
E _{AS}	Single Pulsed Avalanche Energy ⁽²⁾	12	mJ	
P _D	Power Dissipation	T _C = 25°C	7.4	W
R _{θJC}	Thermal Resistance, Junction to Case	17	°C/W	
T _J , T _{STG}	Junction & Storage Temperature Range	-55 to 150	°C	

Electrical Characteristics ($T_J = 25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
--------	-----------	------------	------	------	------	------

Off Characteristics

$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	$I_D = 250\mu\text{A}$, $V_{GS} = 0\text{V}$	30	-	-	V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS} = 30\text{V}$, $V_{GS} = 0\text{V}$	-	-	1.0	μA
I_{GSS}	Gate-Body Leakage Current	$V_{DS} = 0\text{V}$, $V_{GS} = \pm 20\text{V}$	-	-	± 100	nA

On Characteristics

$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS} = V_{GS}$, $I_D = 250\mu\text{A}$	1	1.5	2	V
$R_{DS(ON)}$	Static Drain-Source ON-Resistance ⁽³⁾	$V_{GS} = 10\text{V}$, $I_D = 2.9\text{A}$	-	13.7	17.8	mΩ
		$V_{GS} = 4.5\text{V}$, $I_D = 2\text{A}$	-	23	29.9	mΩ

Dynamic Characteristics

C_{iss}	Input Capacitance	$V_{GS} = 0\text{V}$, $V_{DS} = 15\text{V}$, $f = 1\text{MHz}$	-	510	-	pF
C_{oss}	Output Capacitance		-	61	-	pF
C_{rss}	Reverse Transfer Capacitance		-	51	-	pF
Q_g	Total Gate Charge	$V_{GS} = 0$ to 10V $V_{DS} = 15\text{V}$, $I_D = 3\text{A}$	-	10	-	nC
Q_{gs}	Gate Source Charge		-	2	-	nC
Q_{gd}	Gate Drain("Miller") Charge		-	2	-	nC

Switching Characteristics

$t_{d(on)}$	Turn-On DelayTime	$V_{GS} = 10\text{V}$, $V_{DD} = 15\text{V}$ $I_D = 3\text{A}$, $R_{GEN} = 3\Omega$	-	4	-	ns
t_r	Turn-On Rise Time		-	6	-	ns
$t_{d(off)}$	Turn-Off DelayTime		-	12	-	ns
t_f	Turn-Off Fall Time		-	3	-	ns

Drain-Source Diode Characteristics and Max Ratings

I_S	Maximum Continuous Drain to Source Diode Forward Current	$V_{GS} = 0\text{V}$, $I_S = 2.9\text{A}$	-	-	16	A
I_{SM}	Maximum Pulsed Drain to Source Diode Forward Current		-	-	64	A
V_{SD}	Drain to Source Diode Forward Voltage		-	-	1.2	V
t_{rr}	Body Diode Reverse Recovery Time		-	8	-	ns
Q_{rr}	Body Diode Reverse Recovery Charge		-	2	-	nC

- Notes:
1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature.
 2. E_{AS} condition: Starting $T_J = 25^\circ\text{C}$, $V_{DD} = 15\text{V}$, $V_G = 10\text{V}$, $R_G = 25\Omega$, $L = 0.5\text{mH}$, $I_{AS} = 7\text{A}$
 3. Pulse Test: Pulse Width $\leq 300\mu\text{s}$, Duty Cycle $\leq 0.5\%$.

Test Circuit

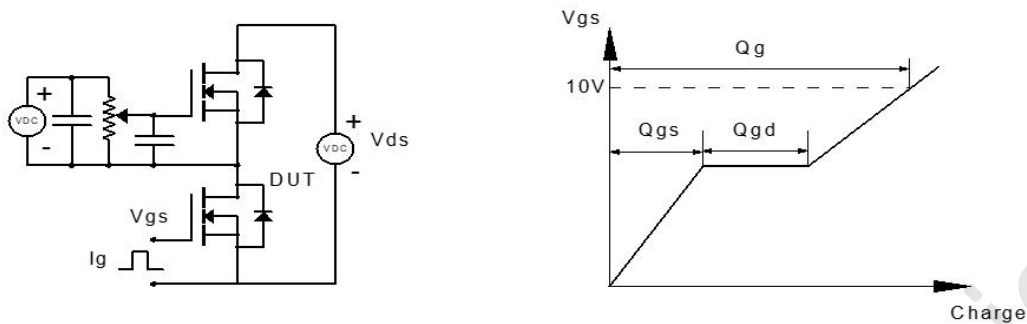


Figure 1: Gate Charge Test Circuit & Waveform

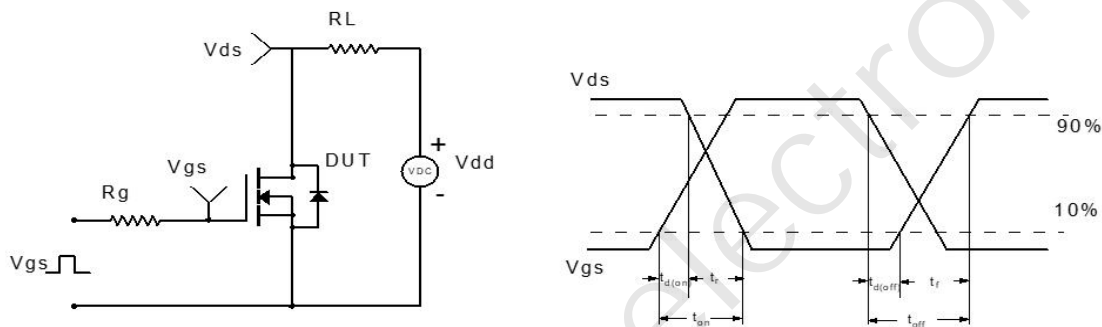


Figure 2: Resistive Switching Test Circuit & Waveform

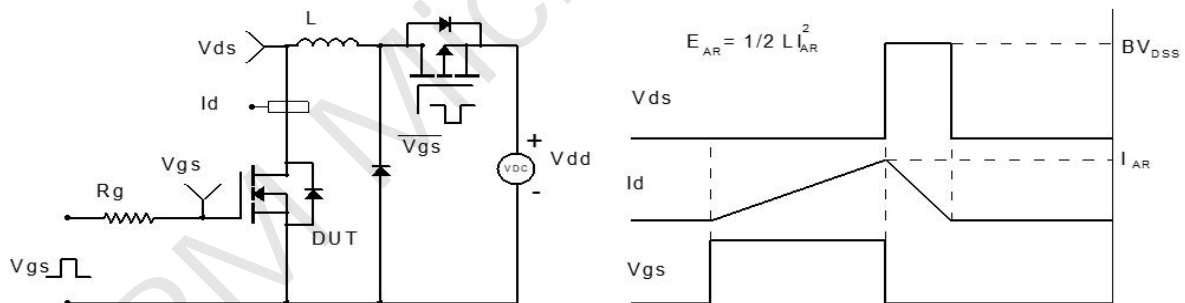


Figure 3: Unclamped Inductive Switching Test Circuit & Waveform

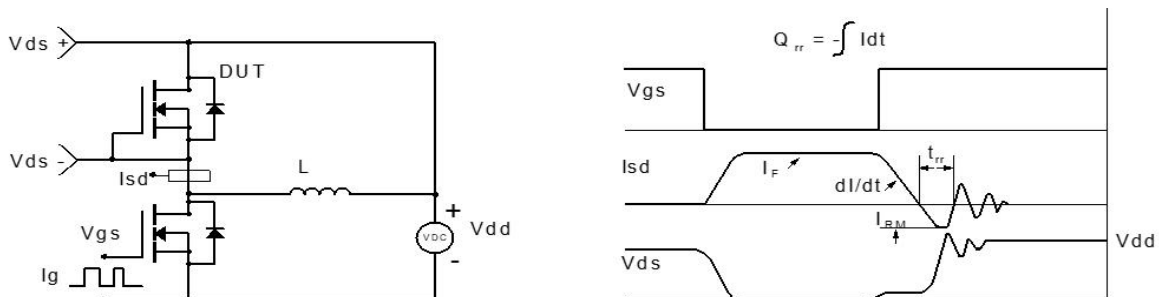
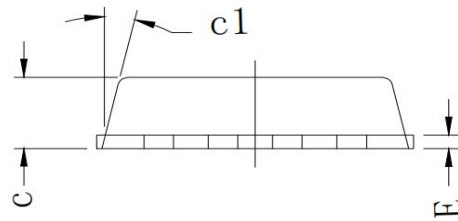
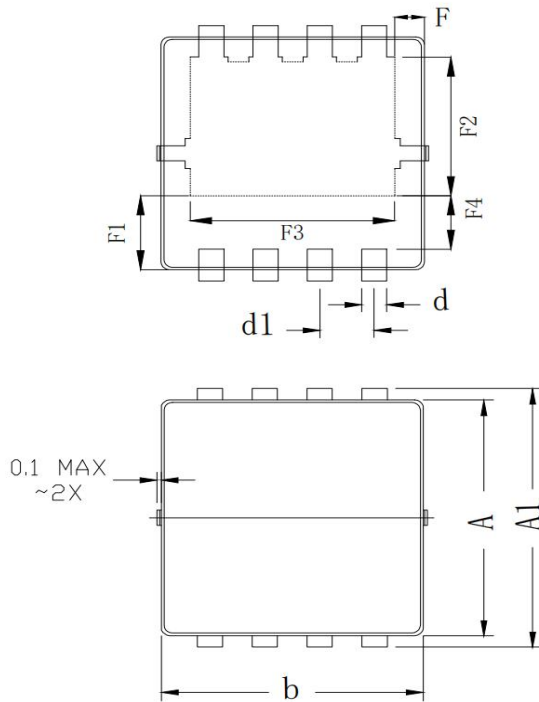


Figure 4: Diode Recovery Test Circuit & Waveform

Package Mechanical Data(PDFN3.3x3.3-8L)




COMMON DIMENSION (MM)			
PKG	PDFN 3.3×3.3-8L		
SYMBOL	MIN	TYP	MAX
A	3.070	3.100	3.130
A1	3.300	3.400	3.500
b	3.070	3.100	3.130
c	0.770	0.800	0.830
c1	-	13°	-
d	0.275	0.300	0.325
d1	0.625	0.650	0.675
E	0.144	0.152	0.160
F	0.300	0.325	0.350
F1	0.960	0.985	1.010
F2	1.775	1.800	1.825
F3	2.425	2.450	2.475
F4	0.660	0.685	0.710

Important Notice

The information presented in datasheets is for reference only. CRM reserves the right to make changes at any time to any products or information herein, without notice.

Customers are responsible for the design and applications, including compliance with all laws, regulations and safety requirements or standards.

“Typical” parameters which provided in datasheets can vary in different applications and actual performance may vary over time. Customers are responsible for doing all necessary testing to minimize the risks associated with their applications and products.

 is a registered trademark of Wuxi CRM Microelectronics Co. , Ltd.

Copyright ©2023 CRM Microelectronics Co. , Ltd. All rights reserved.

Contact information

For more information, please visit: <http://www.crm-semi.tech>

For sales information, please send an email to: sales@crm-semi.com