

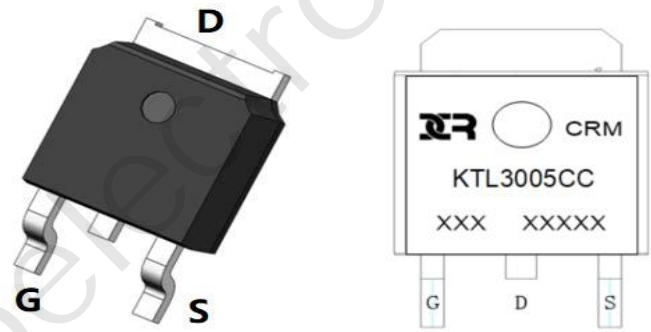
### Description

### Features

- 30V, 85A
- $R_{DS(ON)}$  Typ = 4.5mΩ @  $V_{GS} = 10V$
- $R_{DS(ON)}$  Typ = 5.6mΩ @  $V_{GS} = 4.5V$
- Advanced Trench Technology
- Excellent  $R_{DS(ON)}$  and Low Gate Charge
- 100% UIS TESTED!
- 100%  $\Delta V_{ds}$  TESTED!



Schematic Diagram



Marking and Pin Assignment

### Application

- Load Switch
- PWM Application
- Power Management

### Package Marking and Ordering Information

Device	Marking	Package	Outline	Reel Size	Reel (pcs)	Per Carton (pcs)
CRMKTL3005CC	CRMKTL3005CC	TO-252-3L	TAPING	13"	2500	25000

### Absolute Maximum Ratings (@ $T_J = 25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter	Value	Units
$V_{DS}$	Drain-to-Source Voltage	30	V
$V_{GS}$	Gate-to-Source Voltage	±20	V
$I_D$	Continuous Drain Current	$T_C = 25^\circ\text{C}$	85
		$T_C = 100^\circ\text{C}$	51
$I_{DM}$	Pulsed Drain Current <sup>(1)</sup>	340	A
$E_{AS}$	Single Pulsed Avalanche Energy <sup>(2)</sup>	77	mJ
$P_D$	Power Dissipation	$T_C = 25^\circ\text{C}$	72.7
$R_{\theta JC}$	Thermal Resistance, Junction to Case	1.72	°C/W
$T_J, T_{STG}$	Junction & Storage Temperature Range	-55 to 150	°C

### Electrical Characteristics ( $T_J = 25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
--------	-----------	------------	------	------	------	------

#### Off Characteristics

$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	$I_D = 250\mu\text{A}, V_{GS} = 0\text{V}$	30	-	-	V
$I_{DSS}$	Zero Gate Voltage Drain Current	$V_{DS} = 30\text{V}, V_{GS} = 0\text{V}$	-	-	1.0	$\mu\text{A}$
$I_{GSS}$	Gate-Body Leakage Current	$V_{DS} = 0\text{V}, V_{GS} = \pm 20\text{V}$	-	-	$\pm 100$	nA

#### On Characteristics

$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_D = 250\mu\text{A}$	1	1.5	2	V
$R_{DS(ON)}$	Static Drain-Source ON-Resistance <sup>(3)</sup>	$V_{GS} = 10\text{V}, I_D = 20\text{A}$	-	4.5	5.9	mΩ
		$V_{GS} = 4.5\text{V}, I_D = 15\text{A}$	-	5.6	7.3	mΩ

#### Dynamic Characteristics

$C_{iss}$	Input Capacitance	$V_{GS} = 0\text{V}, V_{DS} = 15\text{V},$ $f = 1\text{MHz}$	-	1900	-	pF
$C_{oss}$	Output Capacitance		-	197	-	pF
$C_{rss}$	Reverse Transfer Capacitance		-	165	-	pF
$Q_g$	Total Gate Charge	$V_{GS} = 0 \text{ to } 10\text{V}$ $V_{DS} = 15\text{V}, I_D = 30\text{A}$	-	35	-	nC
$Q_{gs}$	Gate Source Charge		-	7	-	nC
$Q_{gd}$	Gate Drain("Miller") Charge		-	8	-	nC

#### Switching Characteristics

$t_{d(on)}$	Turn-On DelayTime	$V_{GS} = 10\text{V}, V_{DD} = 15\text{V}$ $I_D = 30\text{A}, R_{GEN} = 3\Omega$	-	9	-	ns
$t_r$	Turn-On Rise Time		-	18	-	ns
$t_{d(off)}$	Turn-Off DelayTime		-	28	-	ns
$t_f$	Turn-Off Fall Time		-	10	-	ns

#### Drain-Source Diode Characteristics and Max Ratings

$I_S$	Maximum Continuous Drain to Source Diode Forward Current		-	-	85	A
$I_{SM}$	Maximum Pulsed Drain to Source Diode Forward Current		-	-	340	A
$V_{SD}$	Drain to Source Diode Forward Voltage	$V_{GS} = 0\text{V}, I_S = 20\text{A}$	-	-	1.2	V

- Notes:
1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature.
  2.  $E_{AS}$  condition: Starting  $T_J = 25^\circ\text{C}$ ,  $V_{DD} = 15\text{V}$ ,  $V_G = 10\text{V}$ ,  $R_G = 25\Omega$ ,  $L = 0.5\text{mH}$ ,  $I_{AS} = 17.5\text{A}$
  4. Pulse Test: Pulse Width  $\leq 300\mu\text{s}$ , Duty Cycle  $\leq 0.5\%$ .

## Test Circuit



Figure 1: Gate Charge Test Circuit & Waveform



Figure 2: Resistive Switching Test Circuit & Waveform

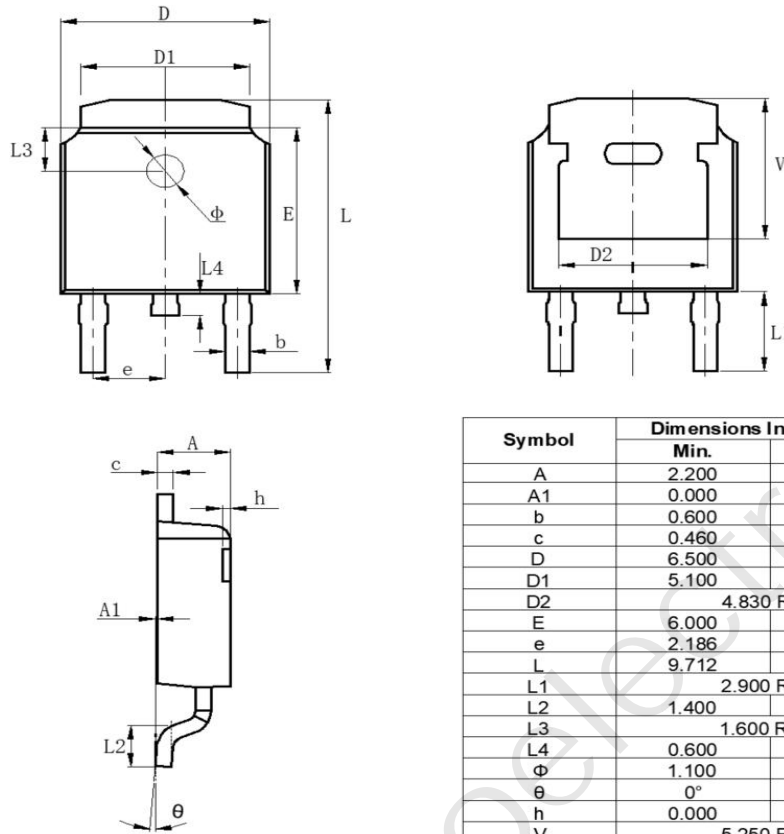


Figure 3: Unclamped Inductive Switching Test Circuit & Waveform



Figure 4: Diode Recovery Test Circuit & Waveform

### Package Mechanical Data(TO-252-3L)




Symbol	Dimensions In Millimeters	
	Min.	Max.
A	2.200	2.400
A1	0.000	0.127
b	0.600	0.860
c	0.460	0.580
D	6.500	6.700
D1	5.100	5.460
D2	4.830 REF.	
E	6.000	6.300
e	2.186	2.386
L	9.712	10.312
L1	2.900 REF.	
L2	1.400	1.700
L3	1.600 REF.	
L4	0.600	1.000
Φ	1.100	1.300
θ	0°	8°
h	0.000	0.300
V	5.250 REF.	

### Important Notice

The information presented in datasheets is for reference only. CRM reserves the right to make changes at any time to any products or information herein, without notice.

Customers are responsible for the design and applications, including compliance with all laws, regulations and safety requirements or standards.

“Typical” parameters which provided in datasheets can vary in different applications and actual performance may vary over time. Customers are responsible for doing all necessary testing to minimize the risks associated with their applications and products.

 is a registered trademark of Wuxi CRM Microelectronics Co. , Ltd.

Copyright ©2023 CRM Microelectronics Co. , Ltd. All rights reserved.

### Contact information

For more information, please visit: <http://www.crm-semi.tech>

For sales information, please send an email to: [sales@crm-semi.com](mailto:sales@crm-semi.com)