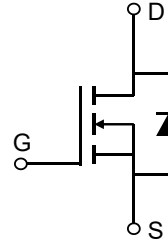


### Description

#### Features

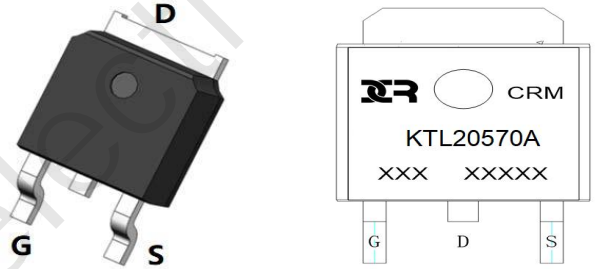
- 200V, 5A  
 $R_{DS(ON)}$  Typ = 466mΩ @  $V_{GS} = 10V$
- Advanced Trench Technology
- Excellent  $R_{DS(ON)}$  and Low Gate Charge
- 100% UIS TESTED!
- 100%  $\Delta V_{ds}$  TESTED!



Schematic Diagram

#### Application

- Load Switch
- PWM Application
- Power Management



Marking and Pin Assignment

#### Package Marking and Ordering Information

Device	Marking	Package	Outline	Reel Size	Reel (pcs)	Per Carton (pcs)
CRMKTL20570A	CRMKTL20570A	TO-252-3L	TAPING	13"	2500	25000

#### Absolute Maximum Ratings (@ $T_J = 25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter	Value	Units
$V_{DS}$	Drain-to-Source Voltage	200	V
$V_{GS}$	Gate-to-Source Voltage	±20	V
$I_D$	Continuous Drain Current	$T_C = 25^\circ\text{C}$	5
		$T_C = 100^\circ\text{C}$	3
$I_{DM}$	Pulsed Drain Current <sup>(1)</sup>	20	A
$E_{AS}$	Single Pulsed Avalanche Energy <sup>(2)</sup>	20	mJ
$P_D$	Power Dissipation	$T_C = 25^\circ\text{C}$	35
$R_{\theta JC}$	Thermal Resistance, Junction to Case	3.6	°C/W
$T_J, T_{STG}$	Junction & Storage Temperature Range	-55 to 150	°C

## Electrical Characteristics (T<sub>J</sub> = 25°C unless otherwise specified)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
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### Off Characteristics

V <sub>(BR)DSS</sub>	Drain-Source Breakdown Voltage	I <sub>D</sub> = 250μA, V <sub>GS</sub> = 0V	200	-	-	V
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	V <sub>DS</sub> = 200V, V <sub>GS</sub> = 0V	-	-	1.0	μA
I <sub>GSS</sub>	Gate-Body Leakage Current	V <sub>DS</sub> = 0V, V <sub>GS</sub> = ±20V	-	-	±100	nA

### On Characteristics

V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250μA	1.2	1.8	2.4	V
R <sub>DS(ON)</sub>	Static Drain-Source ON-Resistance <sup>(3)</sup>	V <sub>GS</sub> = 10V, I <sub>D</sub> = 3A	-	466	560	mΩ

### Dynamic Characteristics

C <sub>iss</sub>	Input Capacitance	V <sub>GS</sub> = 0V, V <sub>DS</sub> = 25V, f = 1MHz	-	625	-	pF
C <sub>oss</sub>	Output Capacitance		-	32	-	pF
C <sub>rss</sub>	Reverse Transfer Capacitance		-	23	-	pF
Q <sub>g</sub>	Total Gate Charge	V <sub>GS</sub> = 0 to 10V V <sub>DS</sub> = 100V, I <sub>D</sub> = 1A	-	12	-	nC
Q <sub>gs</sub>	Gate Source Charge		-	2.5	-	nC
Q <sub>gd</sub>	Gate Drain("Miller") Charge		-	3.8	-	nC

### Switching Characteristics

t <sub>d(on)</sub>	Turn-On DelayTime	V <sub>GS</sub> = 10V, V <sub>DD</sub> = 100V I <sub>D</sub> = 1A, R <sub>GEN</sub> = 2.5Ω	-	10	-	ns
t <sub>r</sub>	Turn-On Rise Time		-	12	-	ns
t <sub>d(off)</sub>	Turn-Off DelayTime		-	15	-	ns
t <sub>f</sub>	Turn-Off Fall Time		-	15	-	ns

### Drain-Source Diode Characteristics and Max Ratings

I <sub>S</sub>	Maximum Continuous Drain to Source Diode Forward Current	V <sub>GS</sub> = 0V, I <sub>S</sub> = 3A	-	-	5	A
I <sub>SM</sub>	Maximum Pulsed Drain to Source Diode Forward Current		-	-	20	A
V <sub>SD</sub>	Drain to Source Diode Forward Voltage		-	-	1.2	V
t <sub>rr</sub>	Body Diode Reverse Recovery Time		-	50	-	ns
Q <sub>rr</sub>	Body Diode Reverse Recovery Charge		-	98	-	nC

Notes:

1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature.
2. E<sub>AS</sub> condition: Starting T<sub>J</sub>=25°C, V<sub>DD</sub>=50V, V<sub>G</sub>=10V, R<sub>G</sub>=25ohm, L=10mH, I<sub>AS</sub>=2A
3. Pulse Test: Pulse Width≤300μs, Duty Cycle≤0.5%.

## Test Circuit



Figure 1: Gate Charge Test Circuit & Waveform



Figure 2: Resistive Switching Test Circuit & Waveform

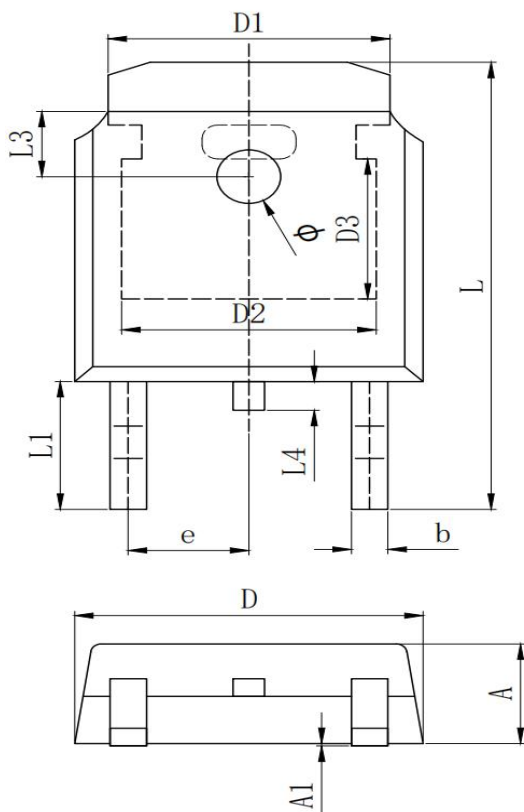


Figure 3: Unclamped Inductive Switching Test Circuit & Waveform



Figure 4: Diode Recovery Test Circuit & Waveform

### Package Mechanical Data(TO-252-3L)




SYMBOL	MILLIMETER		
	MIN	Typ.	MAX
A	2.200	2.300	2.400
A1	0.000		0.127
b	0.640	0.690	0.740
c (电镀后)	0.460	0.520	0.580
D	6.500	6.600	6.700
D1	5.334 REF		
D2	4.826 REF		
D3	3.166 REF		
E	6.000	6.100	6.200
e	2.286 TYP		
h	0.000	0.100	0.200
L	9.900	10.100	10.300
L1	2.888 REF		
L2	1.400	1.550	1.700
L3	1.600 REF		
L4	0.600	0.800	1.000
φ	1.100	1.200	1.300
θ	0°		8°
θ 1	9° TYP		
θ 2	9° TYP		

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