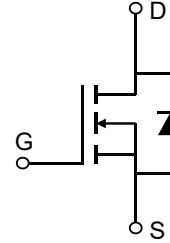


### Description

#### Features

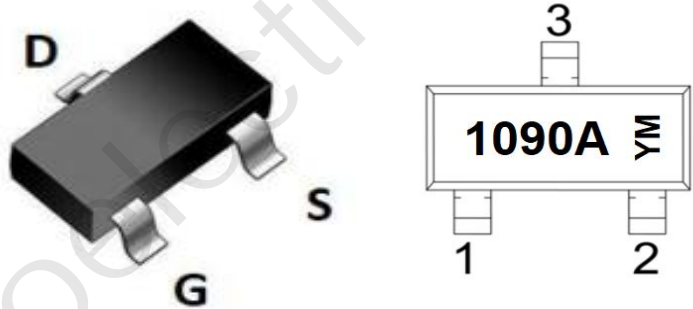
- 100V, 3.8A  
 $R_{DS(ON)} \text{ Typ} = 75\text{m}\Omega @ V_{GS} = 10\text{V}$   
 $R_{DS(ON)} \text{ Typ} = 83\text{m}\Omega @ V_{GS} = 4.5\text{V}$
- Advanced Trench Technology
- Excellent  $R_{DS(ON)}$  and Low Gate Charge
- Lead Free



Schematic Diagram

#### Application

- Load Switch
- PWM Application
- Power Management



Marking and Pin Assignment

#### Package Marking and Ordering Information

Device	Marking	Package	Outline	Reel Size	Reel (pcs)	Per Carton (pcs)
CRMJTL1090A	1090A	SOT-23-3L	TAPING	7"	3000	120000

#### Absolute Maximum Ratings (@ $T_J = 25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter	Value	Units
$V_{DS}$	Drain-to-Source Voltage	100	V
$V_{GS}$	Gate-to-Source Voltage	$\pm 20$	V
$I_D$	Continuous Drain Current	$T_A = 25^\circ\text{C}$	3.8
		$T_A = 100^\circ\text{C}$	2.3
$I_{DM}$	Pulsed Drain Current <sup>(1)</sup>	15	A
$P_D$	Power Dissipation	$T_A = 25^\circ\text{C}$	3.125
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient <sup>(2)</sup>	40	$^\circ\text{C/W}$
$T_J, T_{STG}$	Junction & Storage Temperature Range	-55 to 150	$^\circ\text{C}$

### Electrical Characteristics ( $T_J = 25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
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#### Off Characteristics

$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	$I_D = 250\mu\text{A}$ , $V_{GS} = 0\text{V}$	100	-	-	V
$I_{DSS}$	Zero Gate Voltage Drain Current	$V_{DS} = 100\text{V}$ , $V_{GS} = 0\text{V}$	-	-	1.0	$\mu\text{A}$
$I_{GSS}$	Gate-Body Leakage Current	$V_{DS} = 0\text{V}$ , $V_{GS} = \pm 20\text{V}$	-	-	$\pm 100$	nA

#### On Characteristics

$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS} = V_{GS}$ , $I_D = 250\mu\text{A}$	1.2	2	2.4	V
$R_{DS(ON)}$	Static Drain-Source ON-Resistance <sup>(3)</sup>	$V_{GS} = 10\text{V}$ , $I_D = 3.8\text{A}$	-	75	98	mΩ
		$V_{GS} = 4.5\text{V}$ , $I_D = 3\text{A}$	-	83	108	mΩ

#### Dynamic Characteristics

$C_{iss}$	Input Capacitance	$V_{GS} = 0\text{V}$ , $V_{DS} = 25\text{V}$ , $f = 1\text{MHz}$	-	1084	-	pF
$C_{oss}$	Output Capacitance		-	42	-	pF
$C_{rss}$	Reverse Transfer Capacitance		-	38	-	pF
$Q_g$	Total Gate Charge	$V_{GS} = 0$ to $10\text{V}$ $V_{DS} = 50\text{V}$ , $I_D = 3.8\text{A}$	-	22	-	nC
$Q_{gs}$	Gate Source Charge		-	3	-	nC
$Q_{gd}$	Gate Drain("Miller") Charge		-	6.1	-	nC

#### Switching Characteristics

$t_{d(on)}$	Turn-On DelayTime	$V_{GS} = 10\text{V}$ , $V_{DD} = 50\text{V}$ $I_D = 3.8\text{A}$ , $R_{GEN} = 3\Omega$	-	14	-	ns
$t_r$	Turn-On Rise Time		-	5.5	-	ns
$t_{d(off)}$	Turn-Off DelayTime		-	28	-	ns
$t_f$	Turn-Off Fall Time		-	5.2	-	ns

#### Drain-Source Diode Characteristics and Max Ratings

$I_S$	Maximum Continuous Drain to Source Diode Forward Current	$V_{GS} = 0\text{V}$ , $I_S = 3.8\text{A}$	-	-	3.8	A
$I_{SM}$	Maximum Pulsed Drain to Source Diode Forward Current		-	-	15.2	A
$V_{SD}$	Drain to Source Diode Forward Voltage		-	-	1.2	V
$t_{rr}$	Body Diode Reverse Recovery Time		-	30	-	ns
$Q_{rr}$	Body Diode Reverse Recovery Charge		-	42	-	nC

- Notes:
1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature.
  2.  $R_{\theta JA}$  is measured with the device mounted on a 1inch<sup>2</sup> pad of 2oz copper FR4 PCB
  3. Pulse Test: Pulse Width  $\leq 300\mu\text{s}$ , Duty Cycle  $\leq 0.5\%$ .

## Typical Performance Characteristics

Figure 1: Output Characteristics

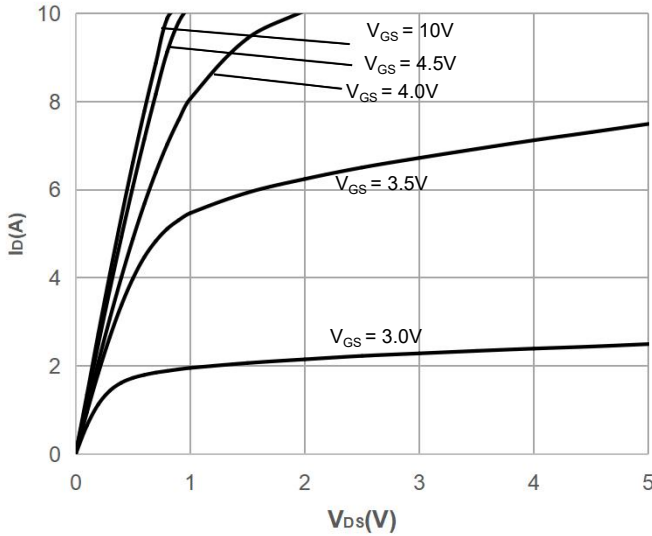


Figure 2: Typical Transfer Characteristics

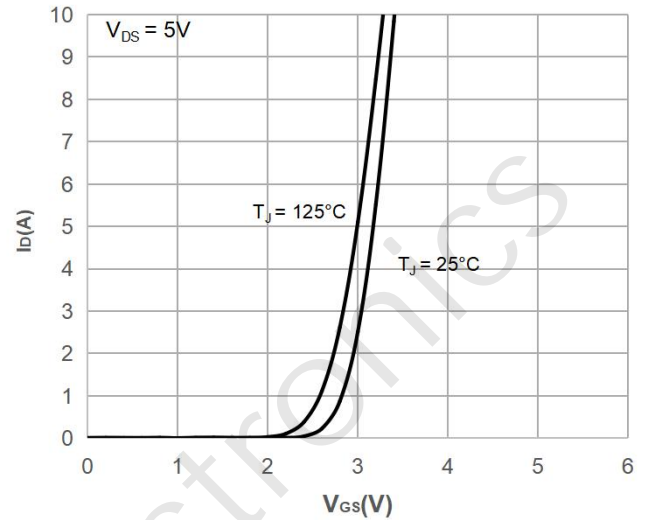


Figure 3: On-resistance vs. Drain Current

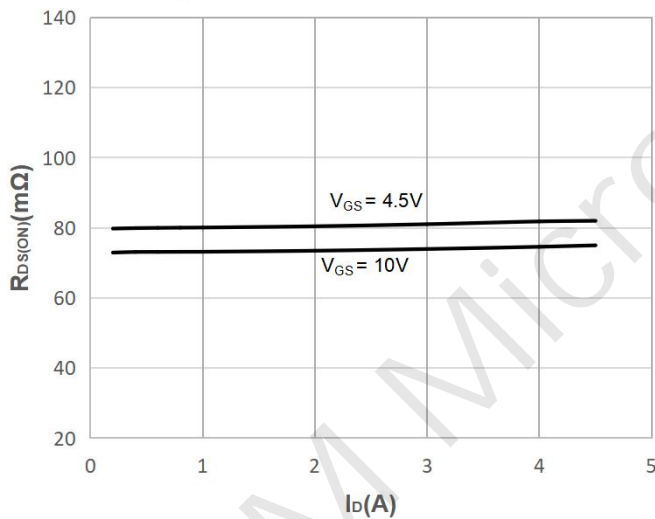


Figure 4: Body Diode Characteristics

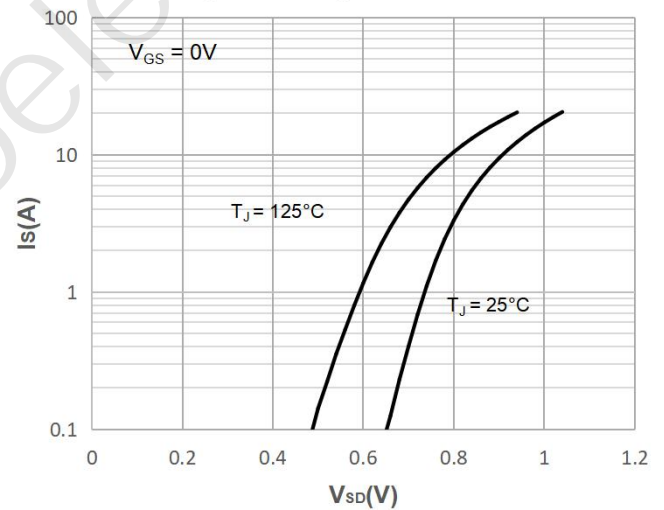


Figure 5: Gate Charge Characteristics

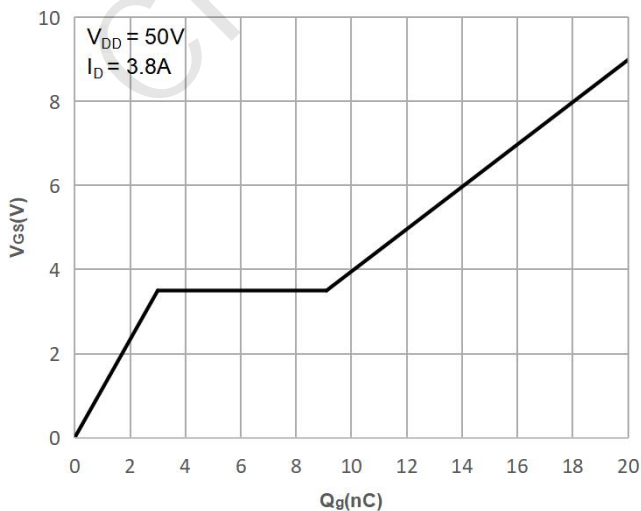
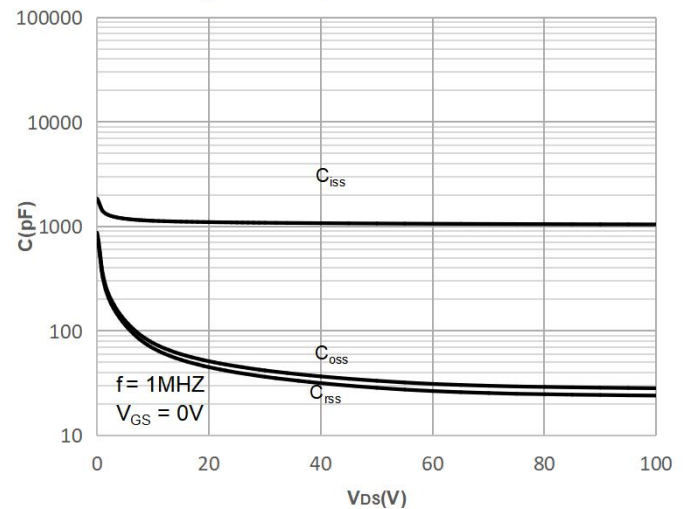


Figure 6: Capacitance Characteristics



## Typical Performance Characteristics

Figure 7: Normalized Breakdown voltage vs. Junction Temperature

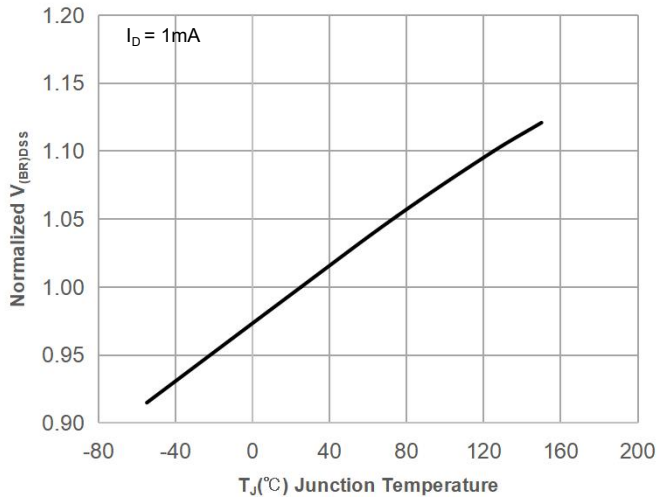


Figure 8: Normalized on Resistance vs. Junction Temperature

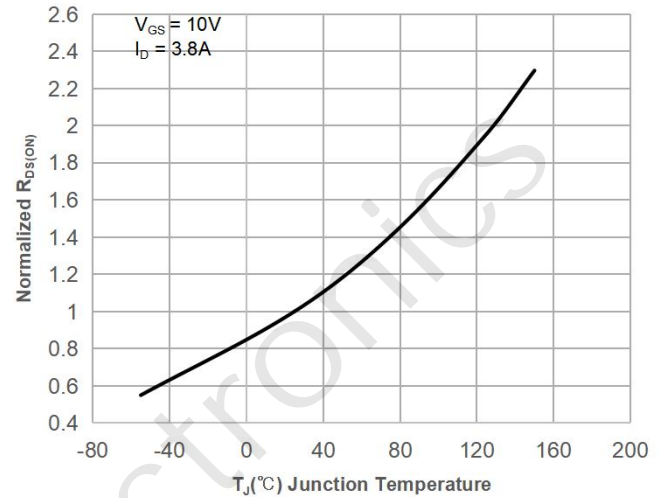


Figure 9: Maximum Safe Operating Area

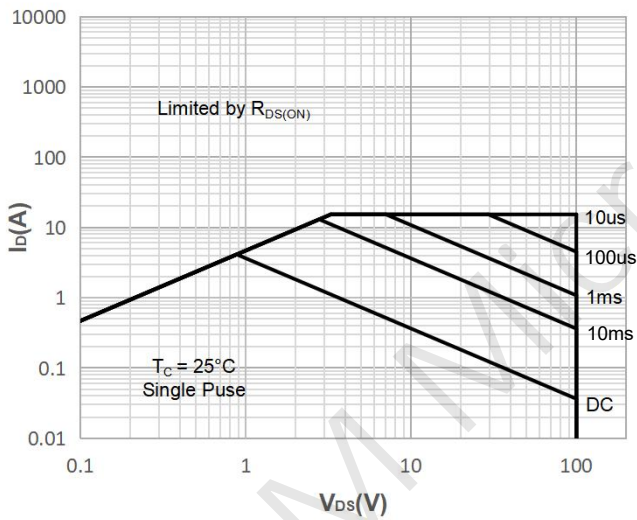


Figure 10: Maximum Continuous Drain Current vs. Case Temperature

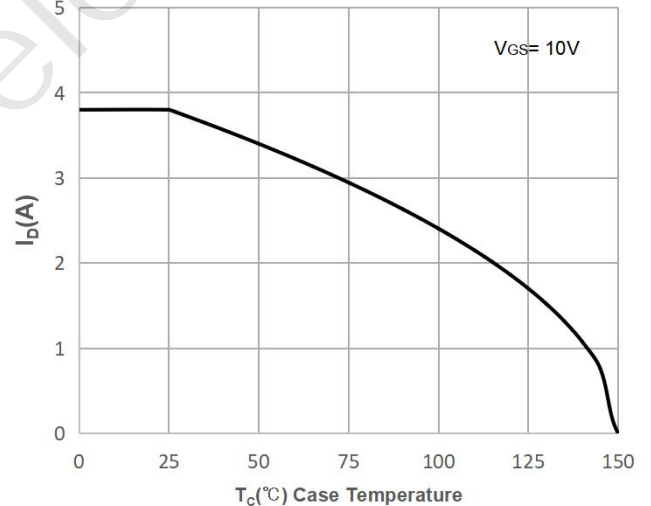


Figure 11: Normalized Maximum Transient Thermal Impedance

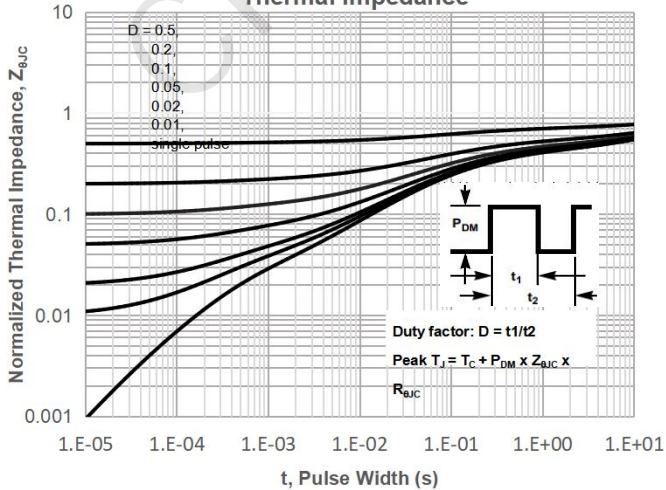
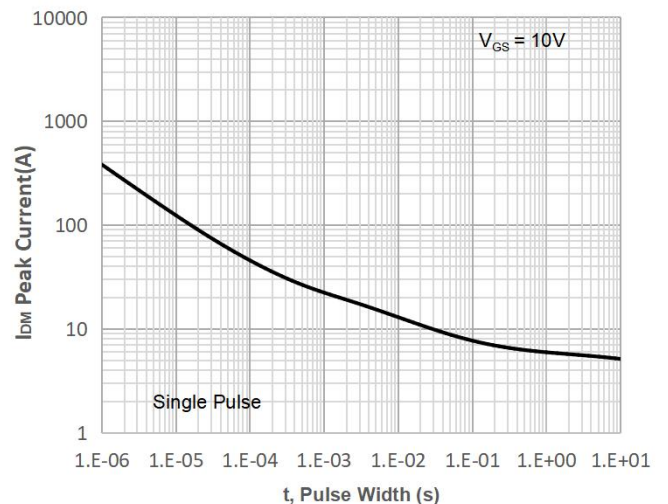


Figure 12: Peak Current Capacity



## Test Circuit

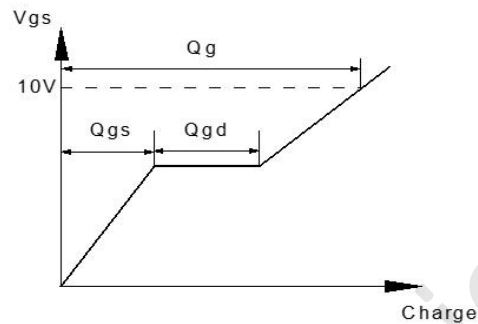


Figure 1: Gate Charge Test Circuit & Waveform

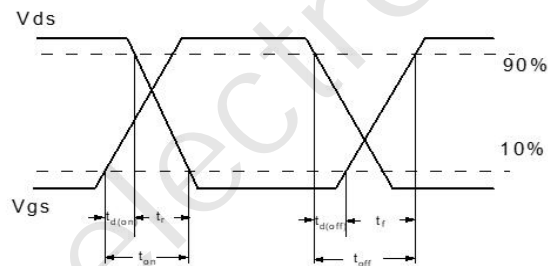
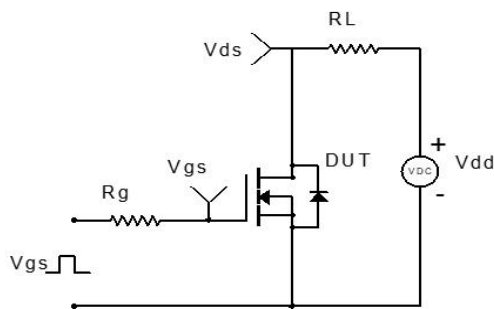


Figure 2: Resistive Switching Test Circuit & Waveform

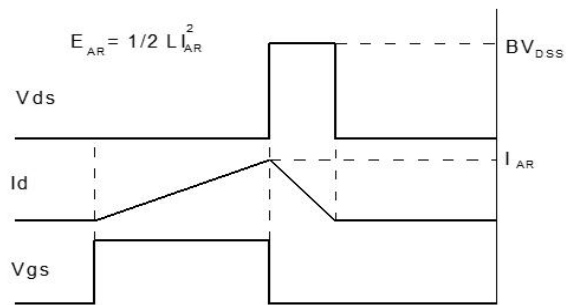
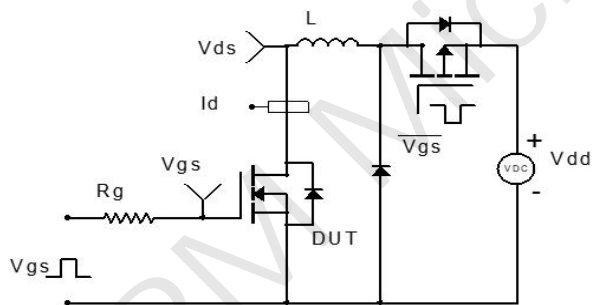


Figure 3: Unclamped Inductive Switching Test Circuit & Waveform

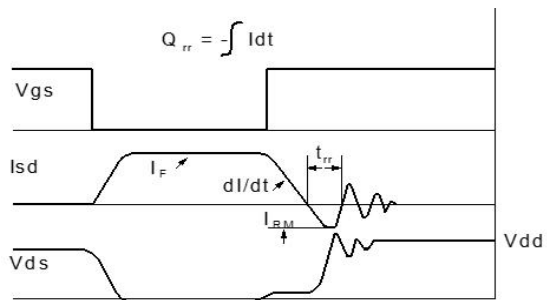
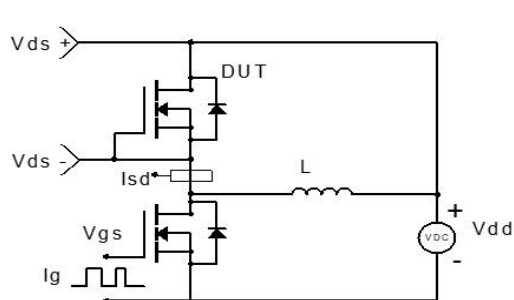
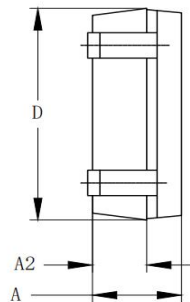
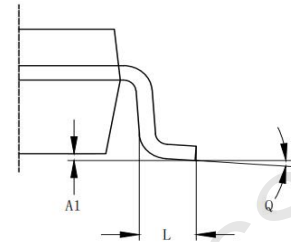
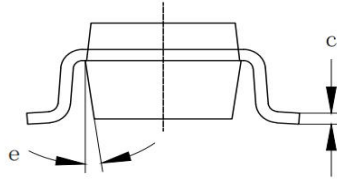
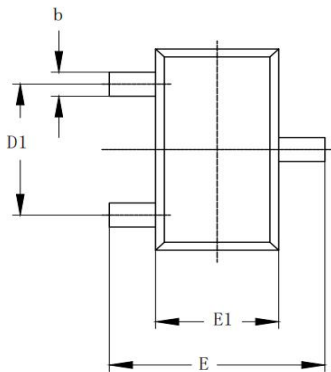


Figure 4: Diode Recovery Test Circuit & Waveform

### Package Mechanical Data(SOT-23-3L)




PKG SYMBOL	COMMON DIMENSION(MM)		
	SOT-23-3L		
	MIN	TYP	MAX
A	1.080	1.100	1.120
A1	0.010	0.060	0.150
A2	0.640	0.670	0.700
b	0.325	0.350	0.375
e	0.125	0.135	0.150
D	2.920	2.930	2.980
D1	1.875	1.900	1.925
E	2.650	2.800	2.950
E1	1.580	1.600	1.670
L	0.300	0.450	0.600
e	8°		
Q	0°	4°	8°

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