CRMITL20570A

N-Channel 200V,478mΩ Typ. Power MOSFET

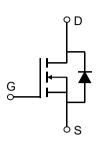
Description

Features

• 200V, 5A

 $R_{DS(ON)}$ Typ = 478m Ω @ V_{GS} = 10V Advanced Trench Technology

- Excellent R_{DS(ON)} and Low Gate Charge
- 100% UIS TESTED!
- 100% ΔVds TESTED!





Application

- Load Switch
- PWM Application
- Power Management



Marking and Pin Assignment

Package Marking and Ordering Information

Device	Marking	Package	Outline	TUBE(pcs)	Inner Box (pcs)	Per Carton (pcs)
CRMITL20570A	CRMITL20570A	TO-251-3L	TUBE	72	4320	21600

Absolute Maximum Ratings (@ T_J = 25°C unless otherwise specified)

Symbol	Parameter		Value	Units
V_{DS}	Drain-to-Source Voltage		200	V
V_{GS}	Gate-to-Source Voltage		±20	V
I _D	Continuous Drain Current	T _C = 25°C	5	Α
I _D		T _C = 100°C	3	Α
I _{DM}	Pulsed Drain Current (1)		20	Α
E _{AS}	Single Pulsed Avalanche Energy (2)		26.5	mJ
P_{D}	Power Dissipation	T _C = 25°C	35	W
$R_{ heta JC}$	Thermal Resistance, Junction to Case		3.6	°C/W
T_J,T_STG	Junction & Storage Temperature Range		-55 to 150	°C

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Electrical Characteristics (T_J = 25°C unless otherwise specified)

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
Off Chara	acteristics					
V _{(BR)DSS}	Drain-Source Breakdown Voltage	$I_D = 250 \mu A, V_{GS} = 0 V$	200	-	-	V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = 200V, V _{GS} = 0V	-	-	1.0	μΑ
I _{GSS}	Gate-Body Leakage Current	$V_{DS} = 0V, V_{GS} = \pm 20V$	-	-	±100	nA
On Chara	acteristics				6	
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	1.2	1.8	2.4	V
R _{DS(ON)}	Static Drain-Source ON-Resistance ⁽³⁾	$V_{GS} = 10V, I_D = 3A$	-	478	573	mΩ
Dynamic	Characteristics					
C _{iss}	Input Capacitance		- /	625	-	pF
C_{oss}	Output Capacitance	$V_{GS} = 0V, V_{DS} = 25V,$ f = 1MHz	-	32	-	pF
C_{rss}	Reverse Transfer Capacitance	I - IIVIMZ	X -	23	-	pF
Q_g	Total Gate Charge		7	12	-	nC
Q_gs	Gate Source Charge	$V_{GS} = 0$ to 10V	U .	2.5	-	nC
$Q_{\sf gd}$	Gate Drain("Miller") Charge	$V_{DS} = 100V, I_{D} = 1A$	-	3.8	-	nC
Switchin	g Characteristics					
t _{d(on)}	Turn-On DelayTime		-	10	-	ns
t _r	Turn-On Rise Time	$V_{GS} = 10V, V_{DD} = 100V$	-	12	-	ns
$t_{\text{d(off)}}$	Turn-Off DelayTime	I_{D} = 1A, R_{GEN} = 2.5 Ω	-	15	-	ns
t _f	Turn-Off Fall Time		-	15	-	ns
Drain-So	urce Diode Characteristics and M	Max Ratings				
Is	Maximum Continuous Drain to Source Diode Forward Current		-	-	5	Α
I _{SM}	Maximum Pulsed Drain to Source Diode	Forward Current	-	-	20	Α
V_{SD}	Drain to Source Diode Forward Voltage	V _{GS} = 0V, I _S = 3A	-	-	1.2	V
trr	Body Diode Reverse Recovery Time	1 - 40 - 4:/	-	50	-	ns
Qrr	Body Diode Reverse Recovery Charge	$I_F = 1A$, di/dt = 100A/us	-	98	-	nC

Notes:

^{1.} Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature.

^{2.} E_{AS} condition: Starting T_J =25°C, V_{DD} =50V, V_G =10V, R_G =25ohm, L=10mH, I_{AS} =2.3A

^{3.} Pulse Test: Pulse Width≤300µs, Duty Cycle≤0.5%.

Test Circuit

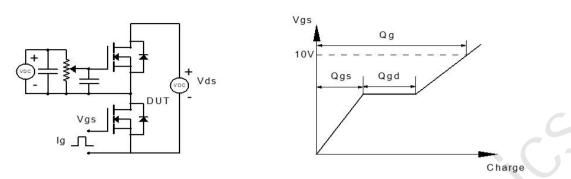


Figure 1: Gate Charge Test Circuit & Waveform

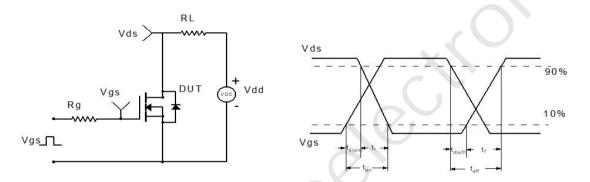


Figure 2: Resistive Switching Test Circuit & Waveform

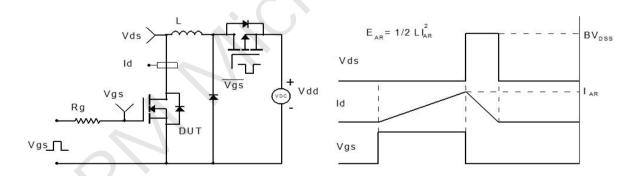


Figure 3: Unclamped Inductive Switching Test Circuit& Waveform

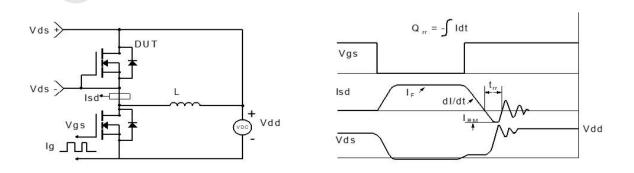
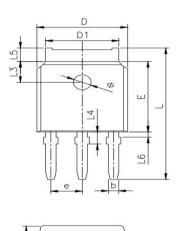


Figure 4: Diode Recovery Test Circuit & Waveform

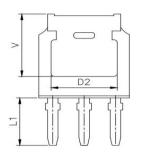
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Package Mechanical Data(TO-251-3L)







SYMBOL	MILLIMETER				
STIVIDOL	MIN	NOM	MAX		
Α	2.20	2.30	2.40		
b	0.66	0.76	0.86		
С	0.46	0.51	0.58		
D	6.50	6.60	6.70		
D1	5.10	5.33	5.46		
D2	4.	4.83 REF.			
E	6.00	6.10	6.20		
е	2.19	2.29	2.39		
L	11.02	11.22	11.42		
L1	4.	10 REF.			
L2	0.	.508BSC			
L3	1.80 REF.				
L4	0.95	1.05	1.15		
L5	0.90	12—11—1	1.25		
L6	0.15	1-1-1	0.75		
Ф	1.10		1.30		
V	5.40 REF				

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