

# **CRMKTL3005A** N-Channel 30V, 3.7mΩ Typ. Power MOSFET

### Description

### **Features**

• 30V, 100A

 $R_{DS(ON)}$  Typ = 3.7m $\Omega$  @ V<sub>GS</sub> = 10V

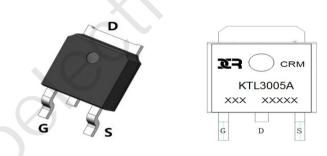
 $R_{DS(ON)}$  Typ = 6.0m $\Omega$  @ V<sub>GS</sub> = 4.5V

- Advanced Trench Technology
- Excellent R<sub>DS(ON)</sub> and Low Gate Charge
- 100% UIS TESTED!
- 100% ΔVds TESTED!

# Application

- Load Switch
- PWM Application
- Power Management

# R G G S Chematic Diagram



#### Marking and Pin Assignment

#### Package Marking and Ordering Information

Device	Marking	Package	Outline	Reel Size	Reel (pcs)	Per Carton (pcs)
CRMKTL3005A	CRMKTL3005A	TO-252-3L	TAPING	13"	2500	25000

#### Absolute Maximum Ratings (@ T<sub>J</sub> = 25°C unless otherwise specified)

Symbol	Parameter		Value	Units
V <sub>DS</sub>	Drain-to-Source Voltage		30	V
V <sub>GS</sub>	Gate-to-Source Voltage		±20	V
	Continuous Drain Current	Drain Current $T_c = 25^{\circ}C$ 100	100	А
I <sub>D</sub>		$T_{\rm C}$ = 100°C	60	А
I <sub>DM</sub>	Pulsed Drain Current <sup>(1)</sup>		400	А
E <sub>AS</sub>	Single Pulsed Avalanche Energy <sup>(2)</sup>		90	mJ
P <sub>D</sub>	Power Dissipation	$T_c = 25^{\circ}C$	78	W
$R_{ extsf{ heta}JC}$	Thermal Resistance, Junction to Case		1.6	°C/W
T <sub>J</sub> , T <sub>stg</sub>	Junction & Storage Temperature Range		-55 to 150	°C



#### **Electrical Characteristics** (T<sub>J</sub> = 25°C unless otherwise specified)

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
Off Chara	acteristics					
V <sub>(BR)DSS</sub>	Drain-Source Breakdown Voltage	I <sub>D</sub> = 250μA, V <sub>GS</sub> = 0V	30	-	-	V
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	V <sub>DS</sub> = 30V, V <sub>GS</sub> = 0V	-	-	1.0	μΑ
I <sub>GSS</sub>	Gate-Body Leakage Current	$V_{DS} = 0V, V_{GS} = \pm 20V$	-	-	±100	nA
On Chara	acteristics				6	
V <sub>GS(th)</sub>	Gate Threshold Voltage	$V_{DS}$ = $V_{GS}$ , $I_D$ = 250 $\mu$ A	1	1.5	2.5	V
R <sub>DS(ON)</sub>	Static Drain-Source ON-Resistance <sup>(3)</sup>	V <sub>GS</sub> = 10V, I <sub>D</sub> = 20A	-	3.7	4.8	mΩ
		V <sub>GS</sub> = 4.5V, I <sub>D</sub> = 10A	-	6.0	7.2	mΩ
Dynamic	Characteristics					
C <sub>iss</sub>	Input Capacitance		-	2070	-	pF
C <sub>oss</sub>	Output Capacitance	V <sub>GS</sub> = 0V, V <sub>DS</sub> = 15V, f = 1MHz	X-\	228	-	pF
C <sub>rss</sub>	Reverse Transfer Capacitance	1 - 1101112		202	-	pF
Q <sub>g</sub>	Total Gate Charge	0	<u> </u>	37	-	nC
$Q_{gs}$	Gate Source Charge	$V_{GS} = 0 \text{ to } 10V$ $V_{DS} = 15V, I_{D} = 30A$	-	8	-	nC
$Q_{gd}$	Gate Drain("Miller") Charge	$v_{\rm DS} = 10$ V, $v_{\rm D} = 00$ A	-	9	-	nC
Switchin	g Characteristics					
t <sub>d(on)</sub>	Turn-On DelayTime		-	7.8	-	ns
t <sub>r</sub>	Turn-On Rise Time	V <sub>GS</sub> = 10V, V <sub>DD</sub> = 15V	-	13	-	ns
$t_{d(off)}$	Turn-Off DelayTime	$I_D$ = 30A, $R_{GEN}$ = 3 $\Omega$	-	31	-	ns
t <sub>f</sub>	Turn-Off Fall Time		-	10	-	ns
Drain-So	urce Diode Characteristics and I	Max Ratings				
I <sub>S</sub>	Maximum Continuous Drain to Source Diode Forward Current		-	-	100	А
I <sub>SM</sub>	Maximum Pulsed Drain to Source Diode Forward Current		-	-	400	А
$V_{SD}$	Drain to Source Diode Forward Voltage	V <sub>GS</sub> = 0V, I <sub>S</sub> = 30A	-	-	1.2	V
Notes:	1. Repetitive Rating: Pulse Width Limited by Maxir	mum Junction Temperature.				

2.  $E_{AS}$  condition: Starting T<sub>J</sub>=25°C, V<sub>DD</sub>=15V, V<sub>G</sub>=10V, R<sub>G</sub>=25ohm, L=0.5mH, I<sub>AS</sub>=19A

3. Pulse Test: Pulse Width≤300µs, Duty Cycle≤0.5%.



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### **Test Circuit**

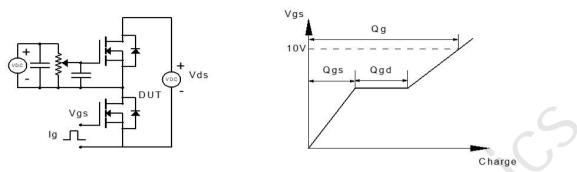
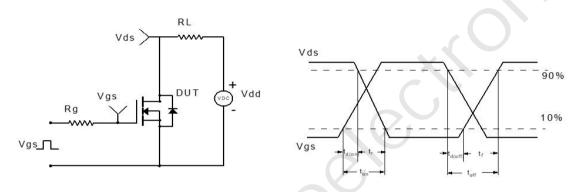
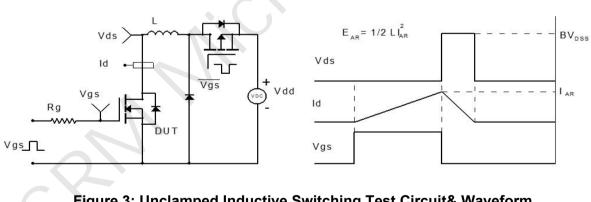


Figure 1: Gate Charge Test Circuit & Waveform



#### Figure 2: Resistive Switching Test Circuit & Waveform





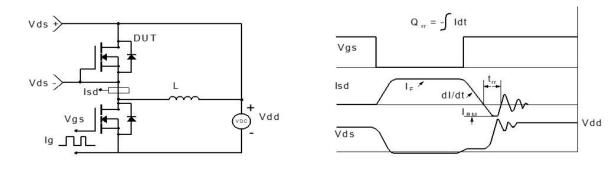
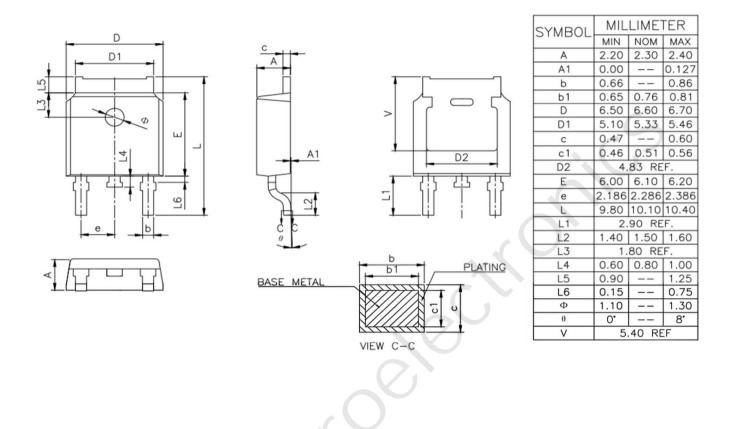


Figure 4: Diode Recovery Test Circuit & Waveform



## Package Mechanical Data(TO-252-3L)



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### **Contact information**

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