CRMGTL0626A

N-Channel 60V, 25mΩ Typ. Power MOSFET

Description

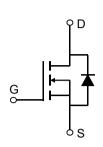
Features

• 60V, 20A

$$R_{DS(ON)}$$
 Typ = 25m Ω @ V_{GS} = 10V

$$R_{DS(ON)}$$
 Typ = 31m Ω @ V_{GS} = 4.5 V

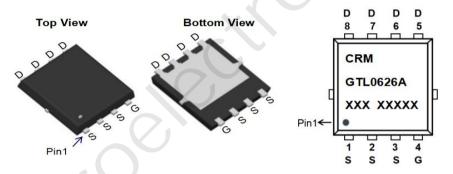
- Advanced Trench Technology
- Excellent R_{DS(ON)} and Low Gate Charge
- 100% UIS TESTED!
- 100% ΔVds TESTED!





Application

- Load Switch
- PWM Application
- Power Management



Marking and Pin Assignment

Package Marking and Ordering Information

Device	Marking	Package	Outline	Reel Size	Reel (pcs)	Per Carton (pcs)
CRMGTL0626A	CRMGTL0626A	PDFN5x6-8L	TAPING	13"	5000	50000

Absolute Maximum Ratings (@ T_J = 25°C unless otherwise specified)

Symbol	Parameter		Value	Units
V_{DS}	Drain-to-Source Voltage		60	V
V_{GS}	Gate-to-Source Voltage		±20	V
	Continuous Drain Current	T _C = 25°C	20	Α
I _D	Continuous Drain Current	T _C = 100°C	12	Α
I _{DM}	Pulsed Drain Current (1)		80	Α
E _{AS}	Single Pulsed Avalanche Energy (2)		25	mJ
P_{D}	Power Dissipation	T _C = 25°C	22	W
$R_{ heta JC}$	Thermal Resistance, Junction to Case		5.6	°C/W
T_{J}, T_{STG}	Junction & Storage Temperature Range		-55 to 150	°C



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Electrical Characteristics (T_J = 25°C unless otherwise specified)

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Uni
Off Chara	acteristics					
$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	$I_D = 250 \mu A, V_{GS} = 0 V$	60	-	-	V
I _{DSS}	Zero Gate Voltage Drain Current	$V_{DS} = 60V, V_{GS} = 0V$	-	-	1.0	μА
I _{GSS}	Gate-Body Leakage Current	$V_{DS} = 0V, V_{GS} = \pm 20V$	-	-	±100	nA
On Chara	acteristics				6	
$V_{\text{GS(th)}}$	Gate Threshold Voltage	$V_{DS} = V_{GS}$, $I_D = 250 \mu A$	1.1	1.6	2.2	V
D	Static Drain-Source ON-Resistance ⁽³⁾	$V_{GS} = 10V, I_D = 10A$	-	25	32	mΩ
$R_{DS(ON)}$		$V_{GS} = 4.5V, I_D = 5A$	-	31	40	mΩ
Dynamic	Characteristics					
C _{iss}	Input Capacitance		-	860	-	pF
C_{oss}	Output Capacitance	$V_{GS} = 0V, V_{DS} = 25V,$ f = 1MHz	X-\	62	-	pF
C_{rss}	Reverse Transfer Capacitance	1 – 1101112		51	-	pF
Q_g	Total Gate Charge		U -	20.3	-	nC
Q_gs	Gate Source Charge	$V_{GS} = 0 \text{ to } 10V$ $V_{DS} = 30V, I_{D} = 10A$	-	3.7	-	nC
Q_{gd}	Gate Drain("Miller") Charge	V _{DS} = 30 V, I _D = 10A	-	5.3	-	nC
Switchin	g Characteristics					
$t_{d(on)}$	Turn-On DelayTime	.()	-	6	-	ns
t _r	Turn-On Rise Time	$V_{GS} = 10V, V_{DD} = 30V$	-	6	-	ns
$t_{\text{d(off)}}$	Turn-Off DelayTime	$I_D = 5A$, $R_{GEN} = 1.8\Omega$	-	19	-	ns
$t_{\rm f}$	Turn-Off Fall Time		-	3	-	ns
Drain-So	urce Diode Characteristics and M	Max Ratings				
I _S	Maximum Continuous Drain to Source Diode Forward Current		-	-	20	Α
I _{SM}	Maximum Pulsed Drain to Source Diode	Forward Current	-	-	80	Α
V _{SD}	Drain to Source Diode Forward Voltage	V _{GS} = 0V, I _S = 10A	-	-	1.2	V
trr	Body Diode Reverse Recovery Time	1 - 50 -1:/-14 - 4000/-	-	13	-	ns
Qrr	Body Diode Reverse Recovery Charge	$I_F = 5A$, di/dt = 100A/us	-	9	-	nC

Notes:

^{1.} Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature.

^{2.} E_{AS} condition: Starting T_J =25°C, V_{DD} =30V, V_G =10V, R_G =25ohm, L=0.5mH, I_{AS} =10A

^{3.} Pulse Test: Pulse Width≤300µs, Duty Cycle≤0.5%.

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Test Circuit

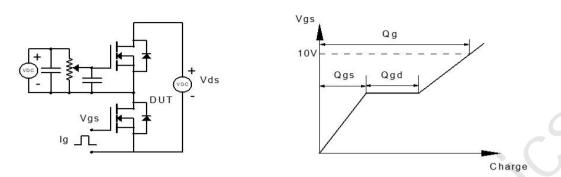


Figure 1: Gate Charge Test Circuit & Waveform

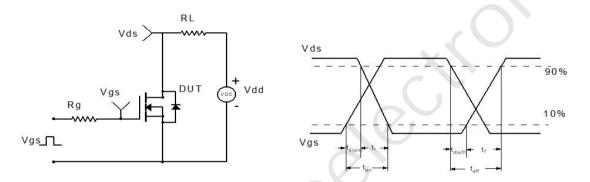


Figure 2: Resistive Switching Test Circuit & Waveform

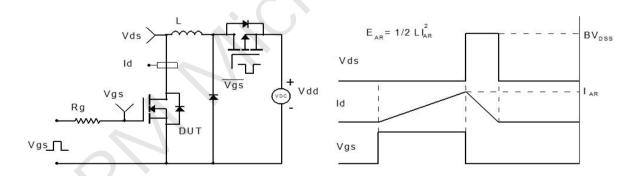


Figure 3: Unclamped Inductive Switching Test Circuit& Waveform

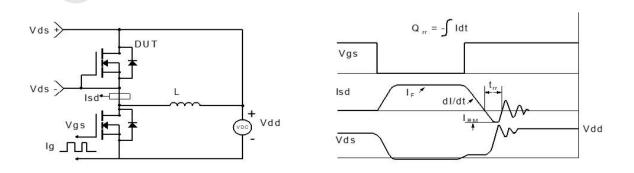
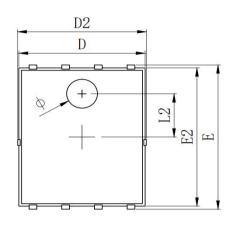


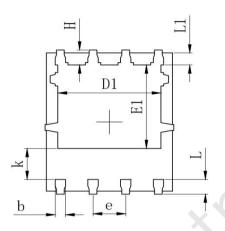
Figure 4: Diode Recovery Test Circuit & Waveform

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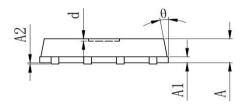
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Package Mechanical Data(PDFN5x6-8L)





SYMBOL	MILLIMETER				
SYMBUL	MIN	Typ.	MAX		
Α	0.900	1.000	1. 100		
A1	0.254 REF.				
A2	0~0.05				
D	4. 824	4. 900	4. 976		
D1	3. 910	4. 010	4. 110		
D2	4. 924	5. 000	5. 076		
Е	5. 924	6. 000	6. 076		
E1	3. 375	3. 475	3. 575		
E2	5. 674	5. 750	5. 826		
b	0. 350	0. 400	0. 450		
е	1.270 TYP.				
L	0. 534	0.610	0. 686		
L1	0. 424	0. 500	0. 576		
L2	1.800 REF.				
k	1. 190	1. 290	1. 390		
Н	0. 549	0.625	0. 701		
θ	8°	10°	12°		
ф	1.100	1.200	1.300		
d			0. 100		



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Contact information

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