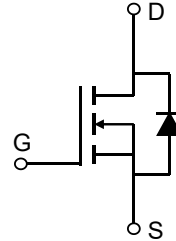


## Description

### Features

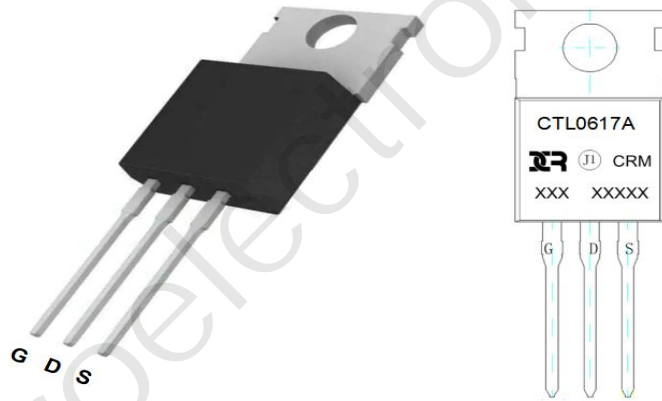
- 60V, 50A
- $R_{DS(ON)}$  Typ = 11.3mΩ @  $V_{GS} = 10V$
- $R_{DS(ON)}$  Typ = 13.7mΩ @  $V_{GS} = 4.5V$
- Advanced Trench Technology
- Excellent  $R_{DS(ON)}$  and Low Gate Charge
- 100% UIS TESTED!
- 100%  $\Delta V_{ds}$  TESTED!



Schematic Diagram

### Application

- Load Switch
- PWM Application
- Power Management



Marking and Pin Assignment

### Package Marking and Ordering Information

Device	Marking	Package	Outline	TUBE(pcs)	Inner Box (pcs)	Per Carton (pcs)
CRMCTL0617A	CRMCTL0617A	TO-220C-3L	TUBE	50	1000	5000

### Absolute Maximum Ratings (@ $T_J = 25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter	Value	Units	
V <sub>DS</sub>	Drain-to-Source Voltage	60	V	
V <sub>GS</sub>	Gate-to-Source Voltage	±20	V	
I <sub>D</sub>	Continuous Drain Current	T <sub>C</sub> = 25°C	50	A
		T <sub>C</sub> = 100°C	30	A
I <sub>DM</sub>	Pulsed Drain Current <sup>(1)</sup>	200	A	
E <sub>AS</sub>	Single Pulsed Avalanche Energy <sup>(2)</sup>	72	mJ	
P <sub>D</sub>	Power Dissipation	T <sub>C</sub> = 25°C	75	W
R <sub>θJC</sub>	Thermal Resistance, Junction to Case	1.67	°C/W	
T <sub>J</sub> , T <sub>STG</sub>	Junction & Storage Temperature Range	-55 to 150	°C	

### Electrical Characteristics ( $T_J = 25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
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#### Off Characteristics

$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	$I_D = 250\mu\text{A}$ , $V_{GS} = 0\text{V}$	60	-	-	V
$I_{DSS}$	Zero Gate Voltage Drain Current	$V_{DS} = 60\text{V}$ , $V_{GS} = 0\text{V}$	-	-	1.0	$\mu\text{A}$
$I_{GSS}$	Gate-Body Leakage Current	$V_{DS} = 0\text{V}$ , $V_{GS} = \pm 20\text{V}$	-	-	$\pm 100$	nA

#### On Characteristics

$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS} = V_{GS}$ , $I_D = 250\mu\text{A}$	1	1.5	2	V
$R_{DS(ON)}$	Static Drain-Source ON-Resistance <sup>(4)</sup>	$V_{GS} = 10\text{V}$ , $I_D = 30\text{A}$	-	11.3	14.7	mΩ
		$V_{GS} = 4.5\text{V}$ , $I_D = 20\text{A}$	-	13.7	18	mΩ

#### Dynamic Characteristics

$C_{iss}$	Input Capacitance	$V_{GS} = 0\text{V}$ , $V_{DS} = 25\text{V}$ , $f = 1\text{MHz}$	-	1967	-	pF
$C_{oss}$	Output Capacitance		-	136	-	pF
$C_{rss}$	Reverse Transfer Capacitance		-	117	-	pF
$Q_g$	Total Gate Charge	$V_{GS} = 0$ to $10\text{V}$ $V_{DS} = 30\text{V}$ , $I_D = 30\text{A}$	-	45	-	nC
$Q_{gs}$	Gate Source Charge		-	8	-	nC
$Q_{gd}$	Gate Drain("Miller") Charge		-	11	-	nC

#### Switching Characteristics

$t_{d(on)}$	Turn-On DelayTime	$V_{GS} = 10\text{V}$ , $V_{DD} = 30\text{V}$ $I_D = 30\text{A}$ , $R_{GEN} = 1.8\Omega$	-	11	-	ns
$t_r$	Turn-On Rise Time		-	79	-	ns
$t_{d(off)}$	Turn-Off DelayTime		-	33	-	ns
$t_f$	Turn-Off Fall Time		-	107	-	ns

#### Drain-Source Diode Characteristics and Max Ratings

$I_S$	Maximum Continuous Drain to Source Diode Forward Current	$V_{GS} = 0\text{V}$ , $I_S = 30\text{A}$	-	-	50	A
$I_{SM}$	Maximum Pulsed Drain to Source Diode Forward Current		-	-	200	A
$V_{SD}$	Drain to Source Diode Forward Voltage		-	-	1.2	V
$t_{rr}$	Body Diode Reverse Recovery Time		-	14	-	ns
$Q_{rr}$	Body Diode Reverse Recovery Charge		-	10	-	nC

- Notes:
1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature.
  2.  $E_{AS}$  condition: Starting  $T_J = 25^\circ\text{C}$ ,  $V_{DD} = 30\text{V}$ ,  $V_G = 10\text{V}$ ,  $R_G = 25\text{ohm}$ ,  $L = 0.5\text{mH}$ ,  $I_{AS} = 17\text{A}$
  3. Pulse Test: Pulse Width  $\leq 300\mu\text{s}$ , Duty Cycle  $\leq 0.5\%$ .

## Typical Performance Characteristics

Figure 1: Output Characteristics

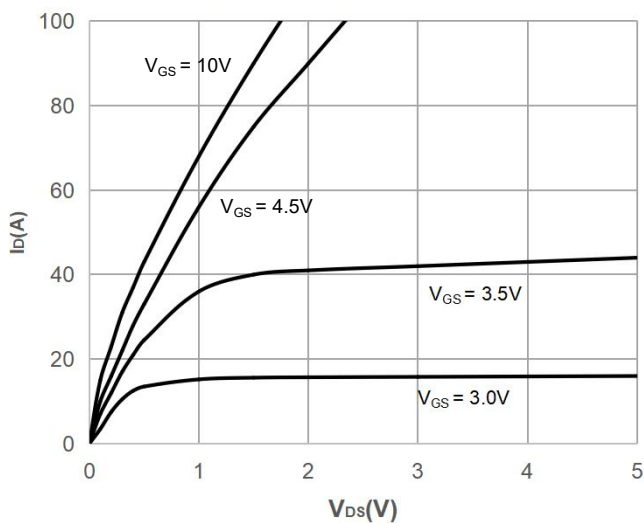


Figure 2: Typical Transfer Characteristics

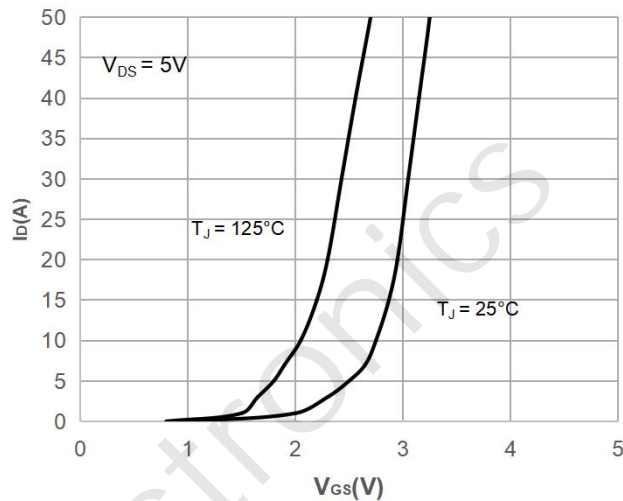


Figure 3: On-resistance vs. Drain Current

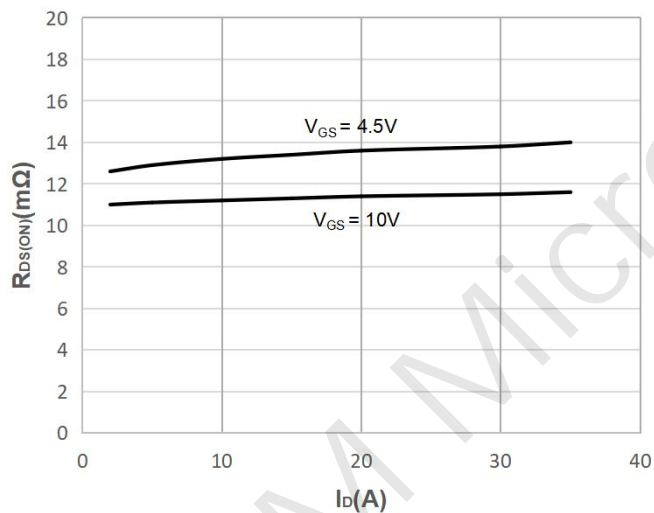


Figure 4: Body Diode Characteristics

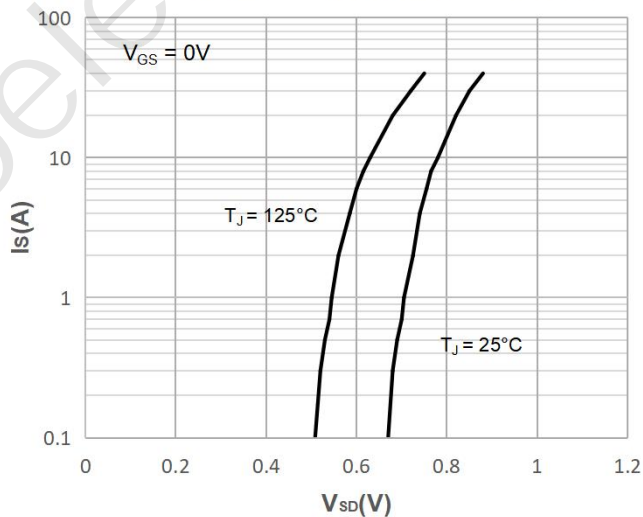


Figure 5: Gate Charge Characteristics

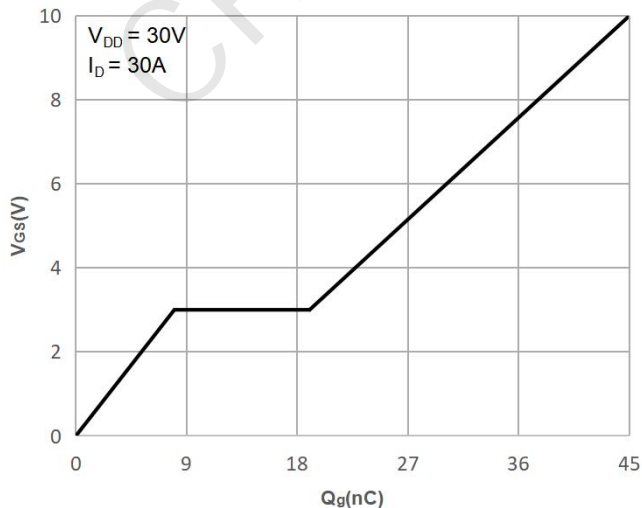
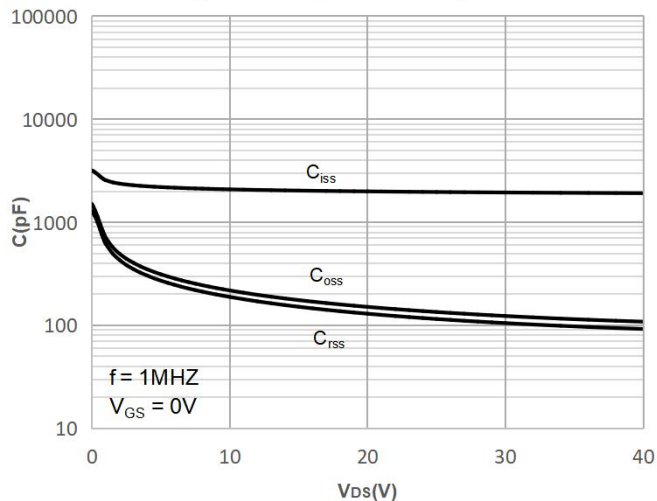


Figure 6: Capacitance Characteristics



## Typical Performance Characteristics

Figure 7: Normalized Breakdown voltage vs. Junction Temperature

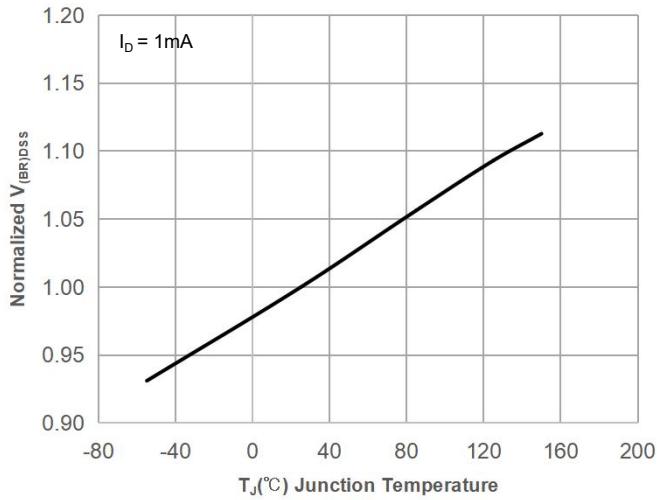


Figure 8: Normalized on Resistance vs. Junction Temperature

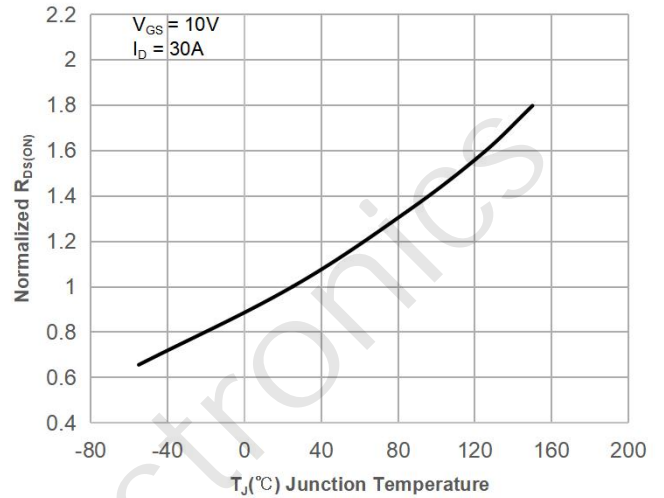


Figure 9: Maximum Safe Operating Area

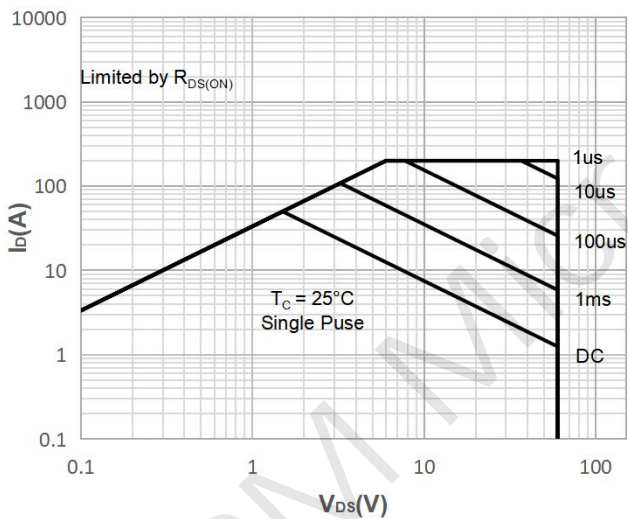


Figure 10: Maximum Continuous Drain Current vs. Case Temperature

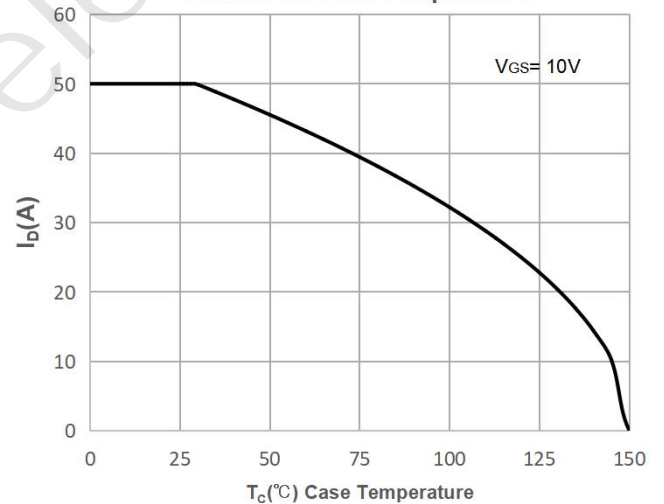


Figure 11: Normalized Maximum Transient Thermal Impedance

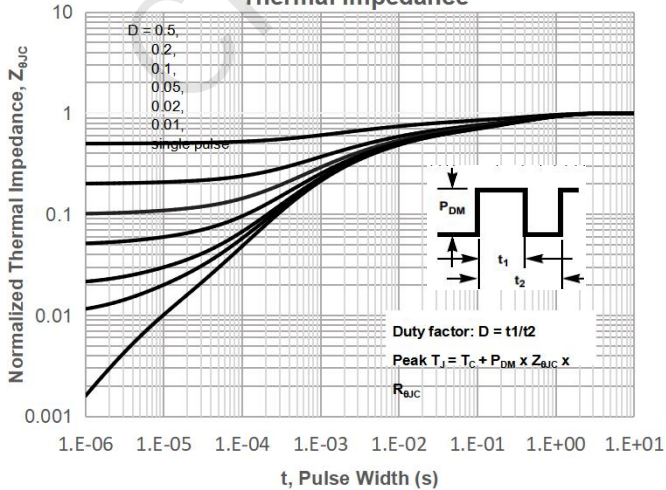
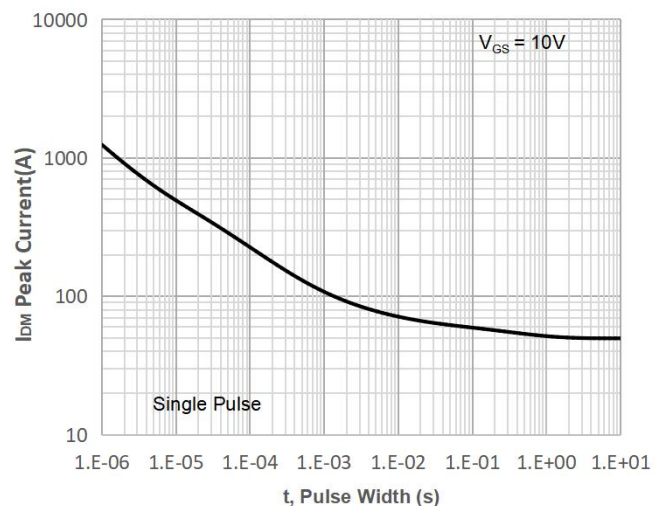


Figure 12: Peak Current Capacity



## Test Circuit

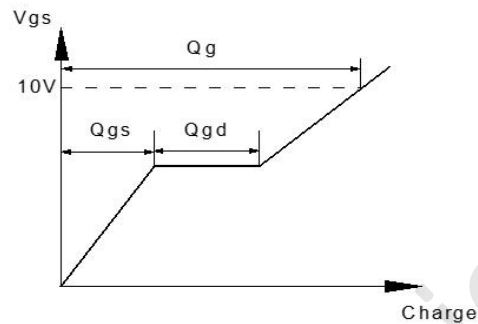


Figure 1: Gate Charge Test Circuit & Waveform

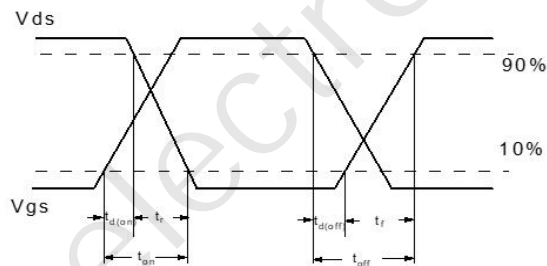
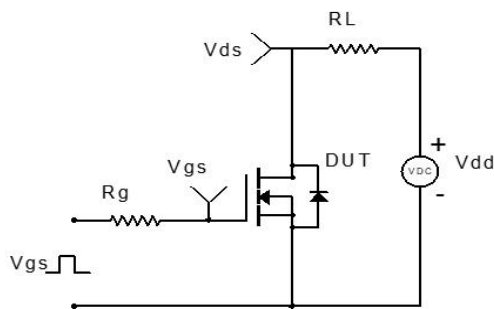


Figure 2: Resistive Switching Test Circuit & Waveform

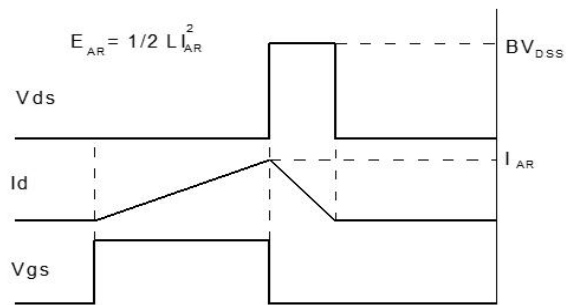
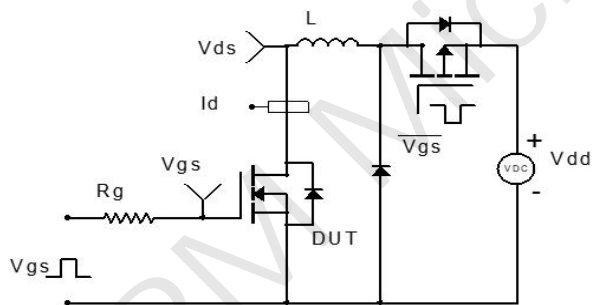


Figure 3: Unclamped Inductive Switching Test Circuit & Waveform

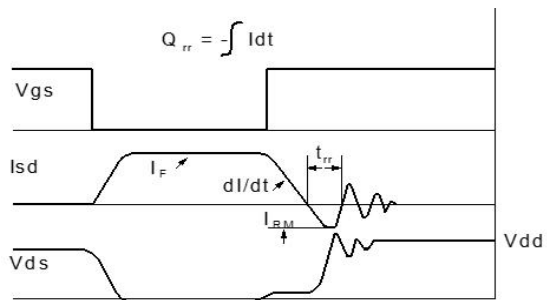
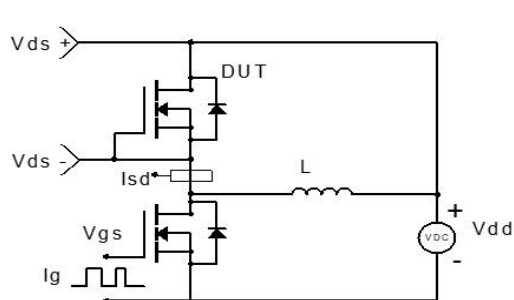
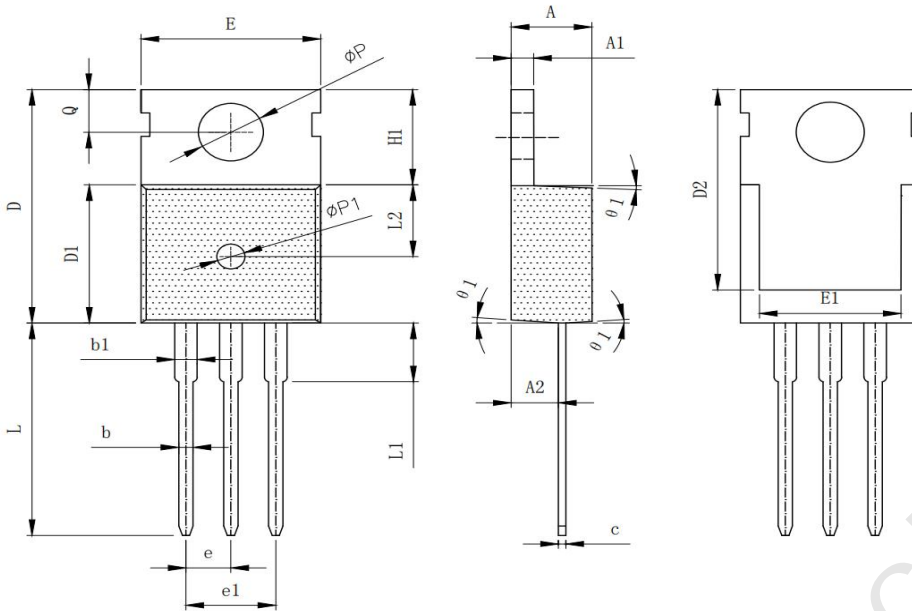


Figure 4: Diode Recovery Test Circuit & Waveform

### Package Mechanical Data(TO-220C-3L)



SYMBOL	MILLIMETER		
	MIN	NOM	MAX
A	4.40	4.50	4.60
A1	1.25	1.30	1.35
A2	2.30	2.40	2.50
b	0.70	0.80	0.90
b1	1.25	1.35	1.45
c	0.40	0.50	0.60
D	15.50	15.80	16.10
D1	9.10	9.20	9.30
D2	12.73	12.83	12.93
E	9.70	9.90	10.20
E1	7.60	8.00	8.40
e	2.54 (BSC)		
e1	5.08 (BSC)		
H1	6.30	6.50	6.80
L	12.75	13.08	13.50
L1	—	—	3.10
L2	4.30	4.60	4.90
ϕP	3.50	3.60	3.70
ϕP1	1.40	1.50	1.60
Q	2.70	—	2.90
θ1	2°	4°	6°

NOTES: 1. PKG SURFACE IS MATTE Ra1.2~1.4;  
 OTHERS IS POLISHED Ra0.15;

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