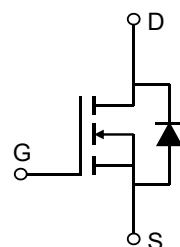


## Description

### Features

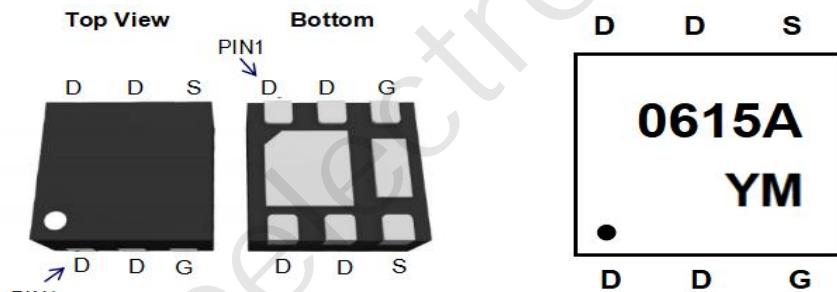
- 60V, 15A
- $R_{DS(ON)}$  Typ = 15.5mΩ @  $V_{GS}$  = 10V
- $R_{DS(ON)}$  Typ = 22mΩ @  $V_{GS}$  = 4.5V
- Advanced Split Gate Trench Technology
- Excellent  $R_{DS(ON)}$  and Low Gate Charge
- Lead Free



Schematic Diagram

### Application

- Load Switch
- PWM Application
- Power Management



Marking and Pin Assignment

### Package Marking and Ordering Information

Device	Marking	Package	Outline	Reel Size	Reel (pcs)	Per Carton (pcs)
CRMVGL0615A	0615A	DFN2020-6L	TAPING	7"	3000	120000

### Absolute Maximum Ratings (@ $T_J$ = 25°C unless otherwise specified)

Symbol	Parameter	Value	Units
$V_{DS}$	Drain-to-Source Voltage	60	V
$V_{GS}$	Gate-to-Source Voltage	$\pm 20$	V
$I_D$	Continuous Drain Current $T_C = 25^\circ\text{C}$	15	A
		$T_C = 100^\circ\text{C}$	A
$I_{DM}$	Pulsed Drain Current <sup>(1)</sup>	60	A
$P_D$	Power Dissipation $T_C = 25^\circ\text{C}$	8.6	W
$R_{θJC}$	Thermal Resistance, Junction to Case	14.5	°C/W
$T_J, T_{STG}$	Junction & Storage Temperature Range	-55 to 150	°C

### Electrical Characteristics ( $T_J = 25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
<b>Off Characteristics</b>						
$V_{(\text{BR})\text{DSS}}$	Drain-Source Breakdown Voltage	$I_D = 250\mu\text{A}, V_{GS} = 0\text{V}$	60	-	-	V
$I_{\text{DSS}}$	Zero Gate Voltage Drain Current	$V_{DS} = 60\text{V}, V_{GS} = 0\text{V}$	-	-	1.0	$\mu\text{A}$
$I_{\text{GSS}}$	Gate-Body Leakage Current	$V_{DS} = 0\text{V}, V_{GS} = \pm 20\text{V}$	-	-	$\pm 100$	nA
<b>On Characteristics</b>						
$V_{GS(\text{th})}$	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_D = 250\mu\text{A}$	1	1.5	2	V
$R_{\text{DS(ON)}}$	Static Drain-Source ON-Resistance <sup>(2)</sup>	$V_{GS} = 10\text{V}, I_D = 5\text{A}$	-	15.5	20	$\text{m}\Omega$
		$V_{GS} = 4.5\text{V}, I_D = 3\text{A}$	-	22	29	$\text{m}\Omega$
<b>Dynamic Characteristics</b>						
$C_{\text{iss}}$	Input Capacitance		-	527	-	pF
$C_{\text{oss}}$	Output Capacitance	$V_{GS} = 0\text{V}, V_{DS} = 30\text{V}, f = 1\text{MHz}$	-	157	-	pF
$C_{\text{rss}}$	Reverse Transfer Capacitance		-	7.5	-	pF
$Q_g$	Total Gate Charge		-	12.1	-	nC
$Q_{gs}$	Gate Source Charge	$V_{GS} = 0 \text{ to } 10\text{V}$	-	1.3	-	nC
$Q_{gd}$	Gate Drain("Miller") Charge	$V_{DS} = 30\text{V}, I_D = 15\text{A}$	-	2.6	-	nC
<b>Switching Characteristics</b>						
$t_{d(on)}$	Turn-On DelayTime		-	3.3	-	ns
$t_r$	Turn-On Rise Time	$V_{GS} = 10\text{V}, V_{DD} = 30\text{V}$	-	4	-	ns
$t_{d(off)}$	Turn-Off DelayTime	$I_D = 15\text{A}, R_{\text{GEN}} = 6\Omega$	-	14	-	ns
$t_f$	Turn-Off Fall Time		-	5.5	-	ns
<b>Drain-Source Diode Characteristics and Max Ratings</b>						
$I_S$	Maximum Continuous Drain to Source Diode Forward Current		-	-	15	A
$I_{\text{SM}}$	Maximum Pulsed Drain to Source Diode Forward Current		-	-	60	A
$V_{SD}$	Drain to Source Diode Forward Voltage	$V_{GS} = 0\text{V}, I_S = 5\text{A}$	-	-	1.2	V
$trr$	Body Diode Reverse Recovery Time		-	20	-	ns
$Qrr$	Body Diode Reverse Recovery Charge	$I_F = 15\text{A}, di/dt = 100\text{A/us}$	-	8.1	-	nC

Notes:

1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature.

2. Pulse Test: Pulse Width  $\leq 300\mu\text{s}$ , Duty Cycle  $\leq 0.5\%$ .

## Typical Performance Characteristics

Figure 1: Output Characteristics

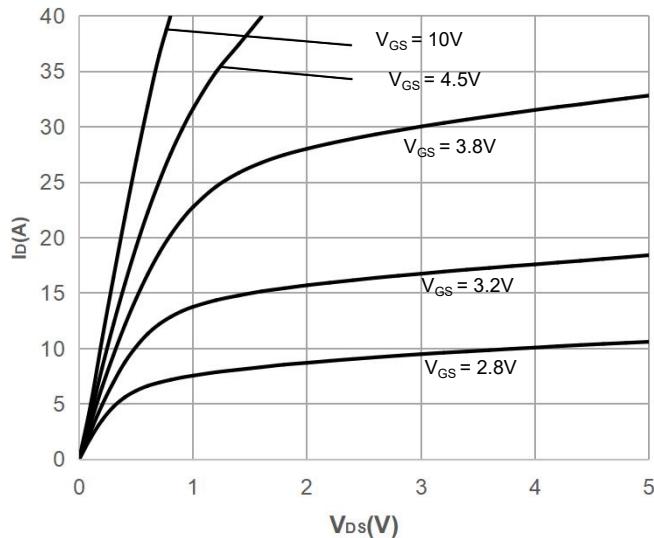


Figure 2: Typical Transfer Characteristics

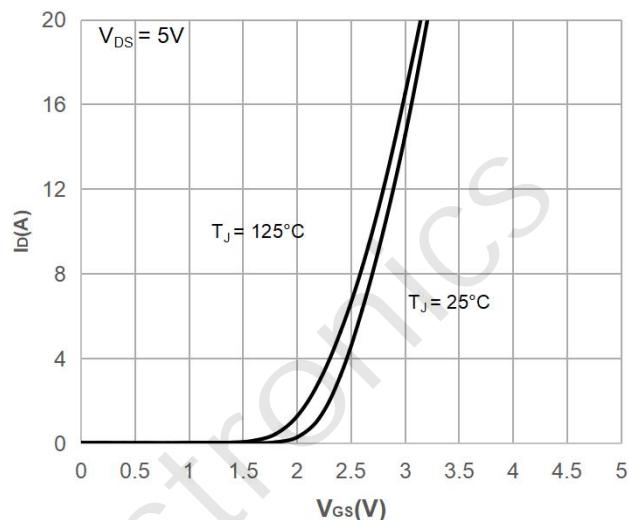


Figure 3: On-resistance vs. Drain Current

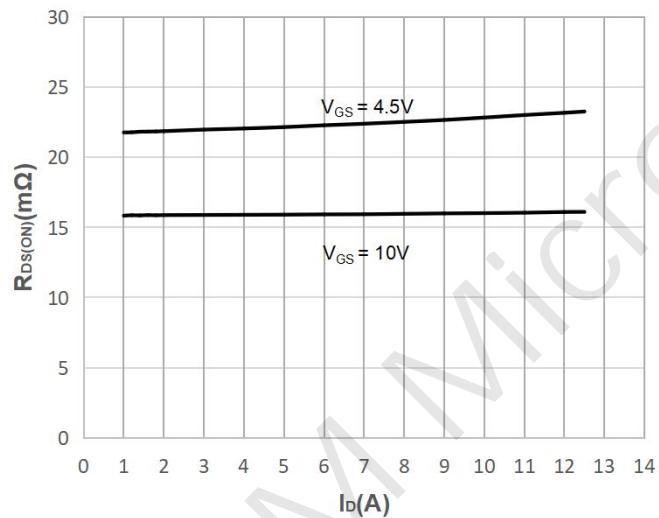


Figure 4: Body Diode Characteristics

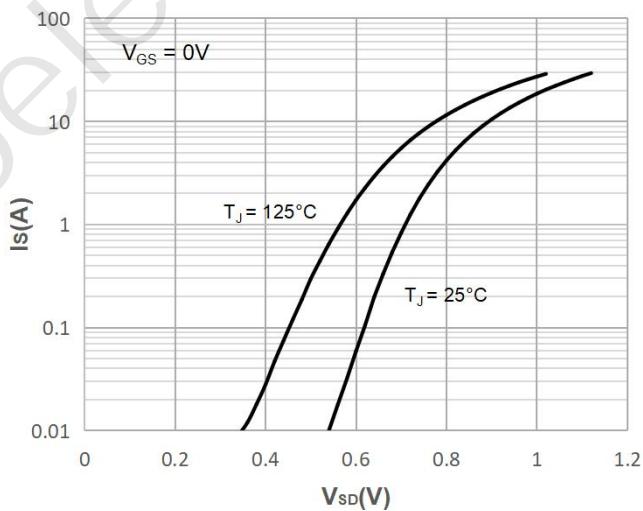


Figure 5: Gate Charge Characteristics

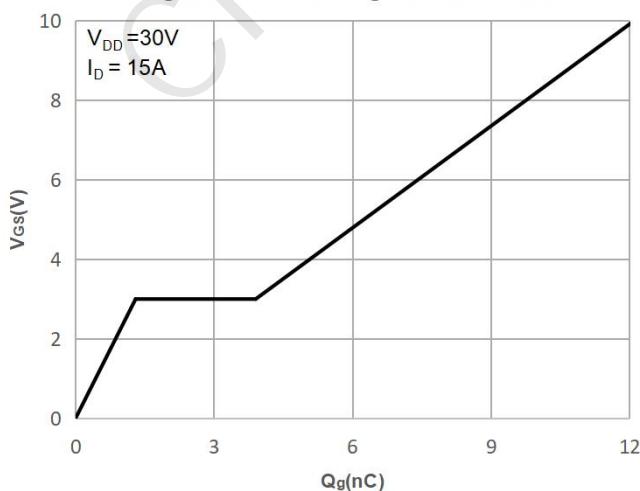
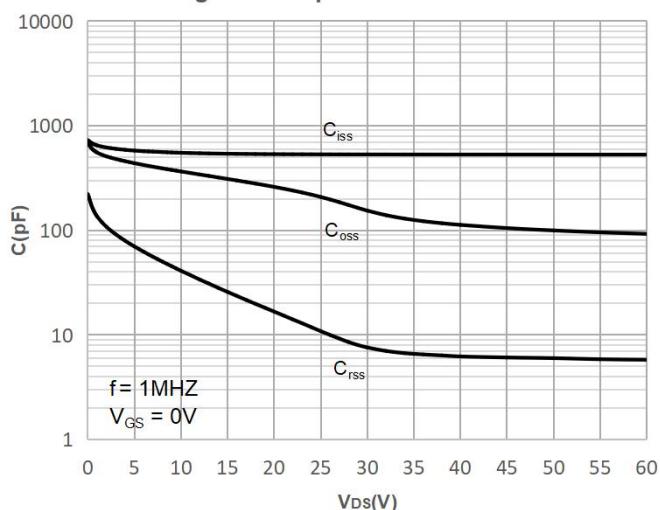
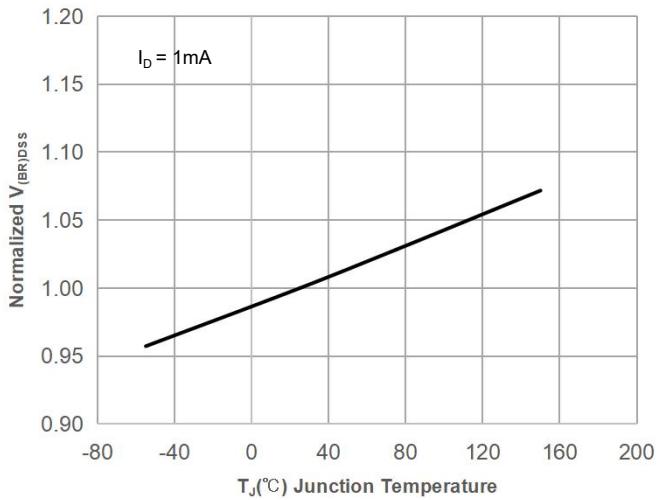


Figure 6: Capacitance Characteristics

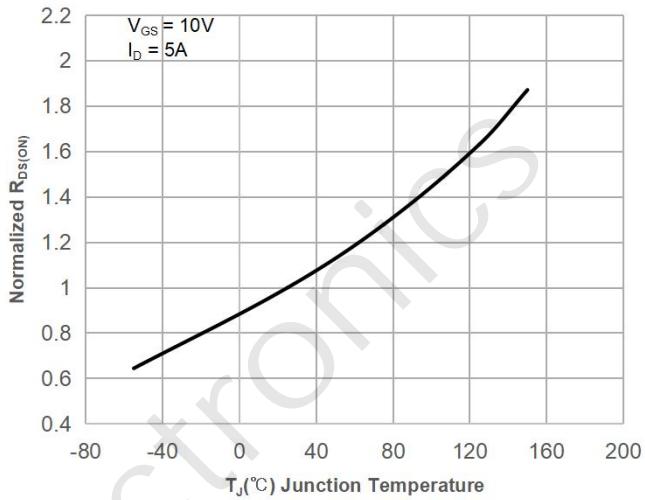


## Typical Performance Characteristics

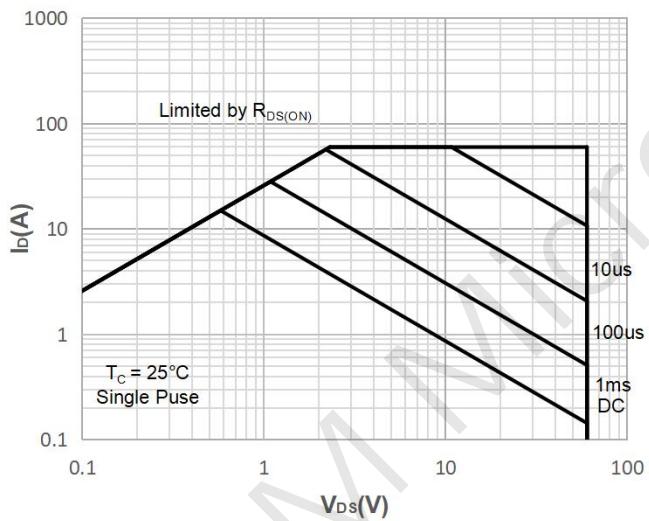
**Figure 7: Normalized Breakdown voltage vs. Junction Temperature**



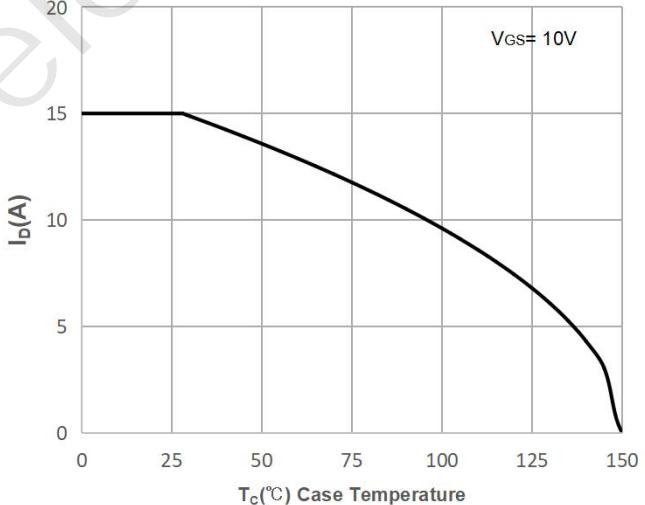
**Figure 8: Normalized on Resistance vs. Junction Temperature**



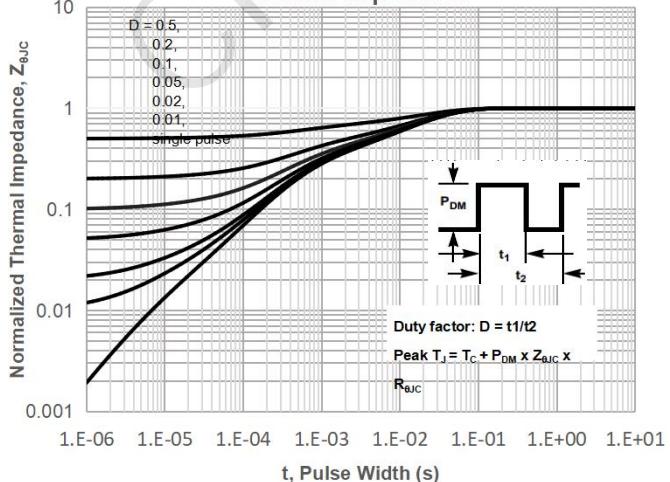
**Figure 9: Maximum Safe Operating Area**



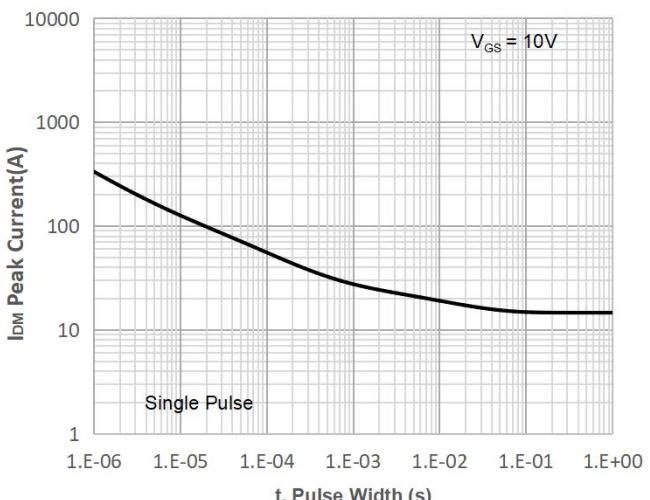
**Figure 10: Maximum Continuous Drain Current vs. Case Temperature**



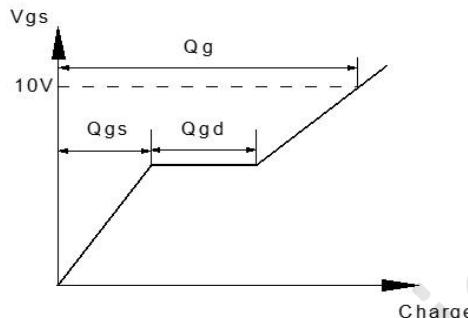
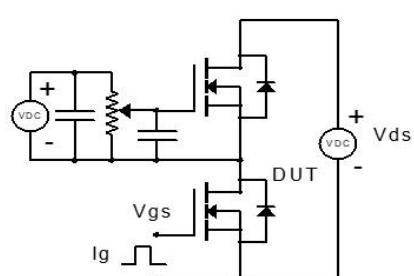
**Figure 11: Normalized Maximum Transient Thermal Impedance**



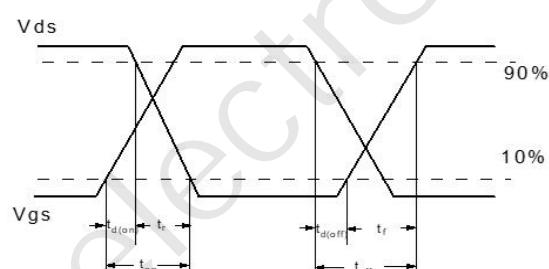
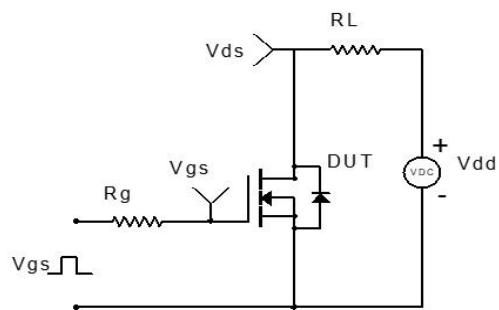
**Figure 12: Peak Current Capacity**



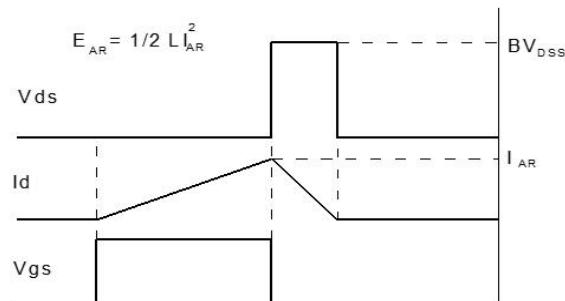
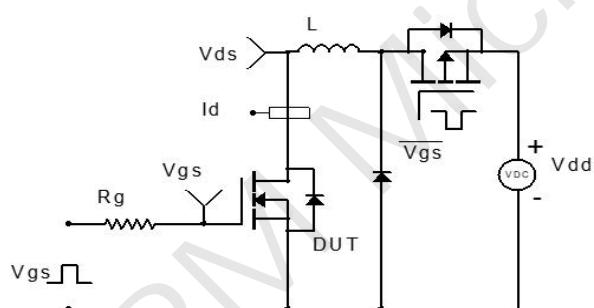
## Test Circuit



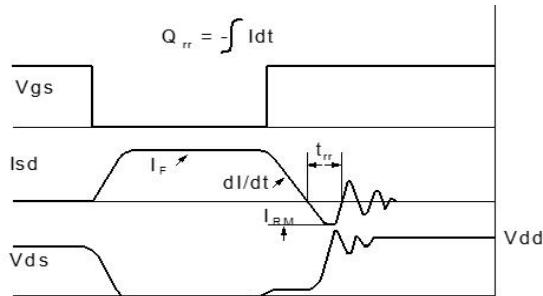
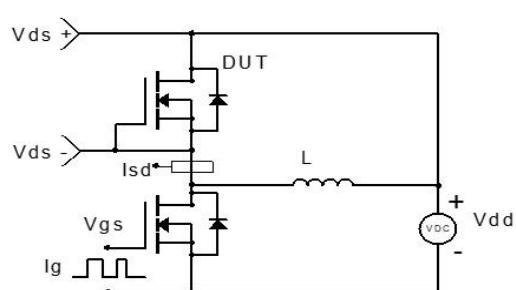
**Figure 1: Gate Charge Test Circuit & Waveform**



**Figure 2: Resistive Switching Test Circuit & Waveform**

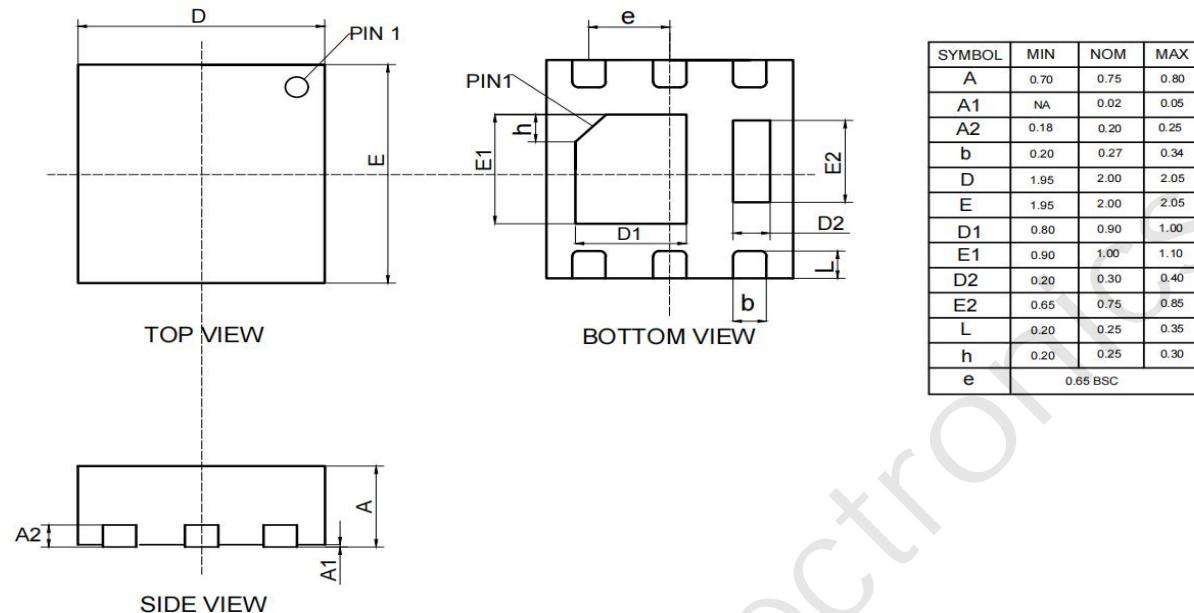


**Figure 3: Unclamped Inductive Switching Test Circuit & Waveform**



**Figure 4: Diode Recovery Test Circuit & Waveform**

## Package Mechanical Data(DFN2020-6L)



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