CRMCP18N20B

N-Channel 200V,132mΩ Typ. Power MOSFET

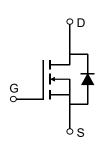
Description

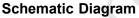
Features

• 200V, 18A

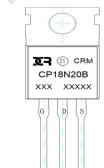
 $R_{DS(ON)}$ Typ =132m Ω @ V_{GS} = 10V

- Fast Switching
- Improved dv/dt Capability
- 100% UIS TESTED!
- 100% ΔVds TESTED!





100



Marking and Pin Assignment

Application

- Switch Mode Power Supply(SMPS)
- Uninterruptible Power Supply(UPS)

Package Marking and Ordering Information

Device	Marking	Package	Outline	TUBE(pcs)	Inner Box (pcs)	Per Carton (pcs)
CRMCP18N20B	CRMCP18N20B	TO-220C-3L	TUBE	50	2000	8000

Absolute Maximum Ratings (@ T_J = 25°C unless otherwise specified)

Symbol	Parameter		Value	Units
V_{DS}	Drain-to-Source Voltage		200	V
V _{GS}	Gate-to-Source Voltage		±20	V
	Continuous Drain Current	T _C = 25°C	18	Α
I _D	Continuous Diam Current	T _C = 100°C	10.8	Α
I _{DM}	Pulsed Drain Current (1)		72	Α
E _{AS}	Single Pulsed Avalanche Energy (2)		169	mJ
P_{D}	Power Dissipation	T _C = 25°C	150	W
$R_{ heta JC}$	Thermal Resistance, Junction to Case		0.83	°C/W
T_{J} , T_{STG}	Junction & Storage Temperature Range		-55 to 150	°C

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Electrical Characteristics (T_J = 25°C unless otherwise specified)

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
Off Chara	acteristics					
$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	$I_D = 250 \mu A, V_{GS} = 0 V$	200	-	-	V
I _{DSS}	Zero Gate Voltage Drain Current	$V_{DS} = 200V, V_{GS} = 0V$	-	-	1.0	μΑ
I _{GSS}	Gate-Body Leakage Current	$V_{DS} = 0V, V_{GS} = \pm 20V$	-	-	±100	nA
On Chara	acteristics				6)
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	2	3	4	V
R _{DS(ON)}	Static Drain-Source ON-Resistance ⁽³⁾	V _{GS} = 10V, I _D = 9A	-	132	172	mΩ
Dynamic	Characteristics					
C _{iss}	Input Capacitance		-	911	-	pF
C_{oss}	Output Capacitance	$V_{GS} = 0V, V_{DS} = 25V,$ f = 1MHz		165	-	pF
C_{rss}	Reverse Transfer Capacitance	1 – 1101112	X-\	87	-	pF
Q_g	Total Gate Charge	(-	60	-	nC
Q_{gs}	Gate Source Charge	$V_{GS} = 0 \text{ to } 10V$ $V_{DS} = 160V, I_{D} = 18A$	U -	4	-	nC
Q_{gd}	Gate Drain("Miller") Charge	V _{DS} - 100V, I _D -10A	-	37	-	nC
Switching	g Characteristics					
t _{d(on)}	Turn-On DelayTime		-	12	-	ns
t _r	Turn-On Rise Time	$V_{GS} = 10V, V_{DD} = 100V$	-	145	-	ns
$t_{\text{d(off)}}$	Turn-Off DelayTime	I_D = 18A, R_{GEN} = 5Ω	-	50	-	ns
t_f	Turn-Off Fall Time		-	15	-	ns
Drain-So	urce Diode Characteristics and M	Max Ratings				
Is	Maximum Continuous Drain to Source D	ode Forward Current	-	-	18	Α
I _{SM}	Maximum Pulsed Drain to Source Diode	Forward Current	-	-	72	Α
V_{SD}	Drain to Source Diode Forward Voltage	$V_{GS} = 0V, I_{S} = 9A$	-	-	1.2	V
trr	Body Diode Reverse Recovery Time	1 - 404 - 11/14 - 4004/	-	200	-	ns
Qrr	Body Diode Reverse Recovery Charge	$I_F = 18A$, di/dt = 100A/us	-	1.2	-	nC
<u>~</u> ;	212, 2.000 No. 1000 N			1.4		

Notes:

^{1.} Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature.

^{2.} E_{AS} condition: Starting T_J =25°C, V_{DD} =50V, V_G =10V, R_G =25ohm, L=0.5mH, I_{AS} =26A

^{3.} Pulse Test: Pulse Width≤300µs, Duty Cycle≤0.5%.

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Test Circuit

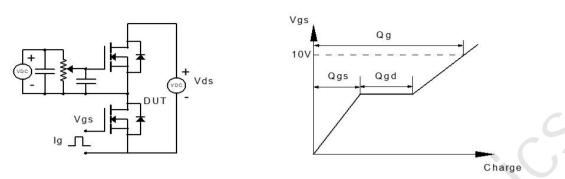


Figure 1: Gate Charge Test Circuit & Waveform

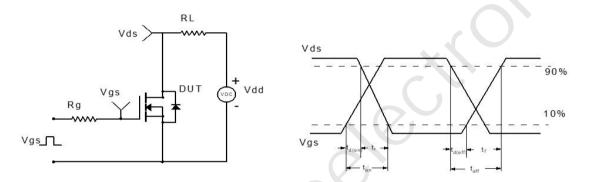


Figure 2: Resistive Switching Test Circuit & Waveform

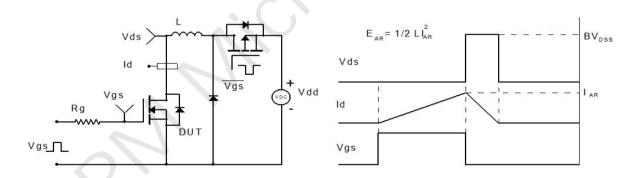


Figure 3: Unclamped Inductive Switching Test Circuit& Waveform

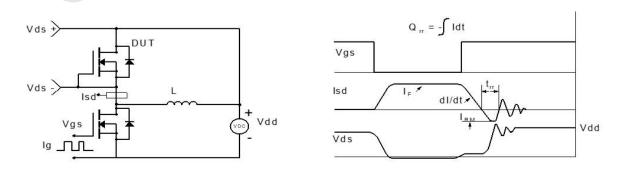
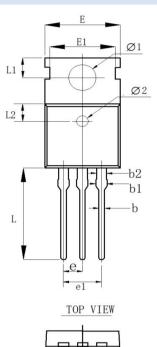


Figure 4: Diode Recovery Test Circuit & Waveform

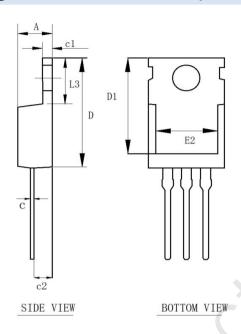
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Package Mechanical Data(TO-220C-3L)



SIDE VIEW



SYMBOL	MIN	NOM	MAX	
A	4. 30	4. 50	4. 70	
b	0.70	0. 80	0.90	
b1			1. 42	
b2	1. 17	1. 27	1. 37	
С	0.40	0. 50	0.60	
c1	1. 25	1. 30	1. 35	
c2	2. 20	2. 40	2.60	
D	15. 45	15. 65	15. 85	
D1	13. 20	13. 40	13.60	
Е	9. 80	10.00	10. 20	
E1	8.60	8. 70	8.80	
E2	7.80	8. 00	8. 20	
e1	4.88	5. 08	5. 28	
L	12.95	13. 15	13. 35	
L1	2.70	2. 80	2.90	
L2	2.40	2. 50	2.60	
L3	6.30	6. 50	6.70	
Ø1	3. 50	3. 60	3.70	
Ø2	1. 35	1. 50	1.65	
е	2, 54BSC			

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