CRMQTL0607A

N-Channel 60V,7mΩ Typ. Power MOSFET

Description

Features

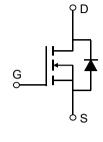
• 60V, 55A

$$R_{DS(ON)}$$
 Typ = 7m Ω @ V_{GS} = 10V
 $R_{DS(ON)}$ Typ = 8.7m Ω @ V_{GS} = 4.5V

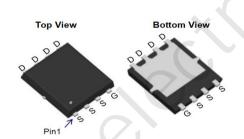
- Advanced Trench Technology
- Excellent R_{DS(ON)} and Low Gate Charge
- Lead Free
- 100% UIS TESTED!
- 100% ΔVds TESTED!

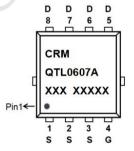
Application

- Load Switch
- PWM Application
- Power Management









Marking and Pin Assignment

Package Marking and Ordering Information

Device	Marking	Package	Outline	Reel Size	Reel (pcs)	Per Carton (pcs)
CRMQTL0607A	CRMQTL0607A	PDFN3.3x3.3-8L	TAPING	13"	5000	60000

Absolute Maximum Ratings (@ T_J = 25°C unless otherwise specified)

Symbol	Parameter		Value	Units
V_{DS}	Drain-to-Source Voltage		60	V
V_{GS}	Gate-to-Source Voltage		±20	V
	Continuous Drain Current	T _C = 25°C	55	А
I _D	Continuous Drain Current	T _C = 100°C	33	А
I _{DM}	Pulsed Drain Current (1)		220	А
E _{AS}	Single Pulsed Avalanche Energy (2)		100	mJ
P_{D}	Power Dissipation	T _C = 25°C	48	W
$R_{ hetaJC}$	Thermal Resistance, Junction to Case		2.6	°C/W
T_J,T_STG	Junction & Storage Temperature Range		-55 to 150	°C

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Electrical Characteristics (T_J = 25°C unless otherwise specified)

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Uni
Off Chara	acteristics					
V _{(BR)DSS}	Drain-Source Breakdown Voltage	$I_D = 250 \mu A, V_{GS} = 0 V$	60	-	-	V
I _{DSS}	Zero Gate Voltage Drain Current	$V_{DS} = 60V, V_{GS} = 0V$	-	-	1.0	μΑ
I _{GSS}	Gate-Body Leakage Current	$V_{DS} = 0V, V_{GS} = \pm 20V$	-	-	±100	nA
On Chara	acteristics				6	
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	1.2	1.6	2.3	V
В	Chatia Dania Conner ON Desister (3)	$V_{GS} = 10V, I_D = 20A$	-	7	9.1	mΩ
$R_{DS(ON)}$	Static Drain-Source ON-Resistance ⁽³⁾	$V_{GS} = 4.5V, I_D = 10A$	-	8.7	11.3	mΩ
Dynamic	Characteristics					
C _{iss}	Input Capacitance		-(3034	-	pF
C_{oss}	Output Capacitance	$V_{GS} = 0V, V_{DS} = 25V,$ f = 1MHz	X - \	198	-	pF
C_{rss}	Reverse Transfer Capacitance		-	167	-	pF
Q_g	Total Gate Charge		J -	77	-	nC
Q_gs	Gate Source Charge	$V_{GS} = 0 \text{ to } 10V$ $V_{DS} = 30V, I_{D} = 30A$	-	14	-	nC
Q_{gd}	Gate Drain("Miller") Charge	VDS = 00 V, 1D = 00 V	-	15	-	nC
Switchin	g Characteristics					
$t_{d(on)}$	Turn-On DelayTime	.r ()	-	13	-	ns
t _r	Turn-On Rise Time	$V_{GS} = 10V, V_{DD} = 30V$	-	77	-	ns
$t_{\text{d(off)}}$	Turn-Off DelayTime	I_{D} = 30A, R_{GEN} = 1.8 Ω	-	50	-	ns
t _f	Turn-Off Fall Time	>		106		ns
Drain-So	urce Diode Characteristics and N	Max Ratings				
I _S	Maximum Continuous Drain to Source Di	ode Forward Current	-	-	55	А
I _{SM}	Maximum Pulsed Drain to Source Diode	Forward Current	-	-	220	А
V_{SD}	Drain to Source Diode Forward Voltage	$V_{GS} = 0V, I_{S} = 20A$	-	-	1.2	V
trr	Body Diode Reverse Recovery Time		-	25	-	ns
Qrr	Body Diode Reverse Recovery Charge	$I_F = 30A$, di/dt = 100A/us	-	30	-	nC

Notes:

^{1.} Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature.

^{2.} E_{AS} condition: Starting T_J =25°C, V_{DD} =30V, V_G =10V, R_G =25ohm, L=0.5mH, I_{AS} =20A

^{3.} Pulse Test: Pulse Width≤300µs, Duty Cycle≤0.5%.

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Test Circuit

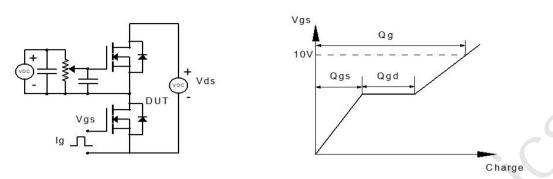


Figure 1: Gate Charge Test Circuit & Waveform

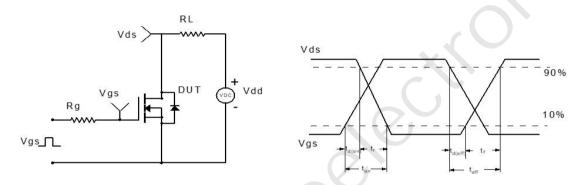


Figure 2: Resistive Switching Test Circuit & Waveform

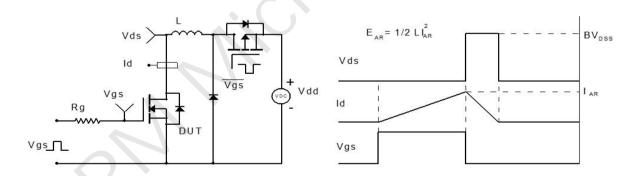


Figure 3: Unclamped Inductive Switching Test Circuit& Waveform

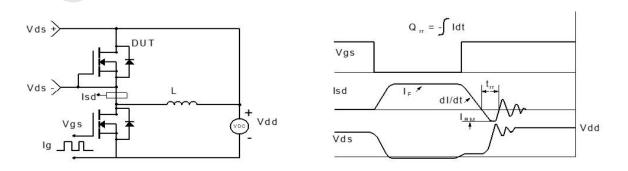
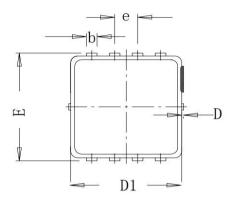


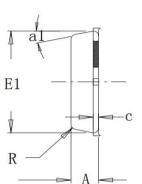
Figure 4: Diode Recovery Test Circuit & Waveform

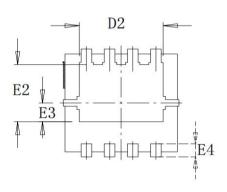
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Package Mechanical Data(PDFN3.3x3.3-8L)







CVAIDOL	MILLIMETER				
SYMBOL	MIN	NOM	MAX		
A	0.75	0.78	0. 81		
* b	0.297	0. 3	0. 35		
С	_	0.152	-		
* D	0.00	0. 05	0.1		
D1	3.12	3. 15	3. 18		
* D2	_	2, 35	-		
* E	3.2	3. 3	3.4		
E1	3.09	3. 12	3. 15		
E2	-	1.75	_		
E3	-	0.575	-		
* E4	_	0.4	_		
R	_	0. 15			
* e	0. 65BSC				
a1°	_	12°			

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Contact information

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