

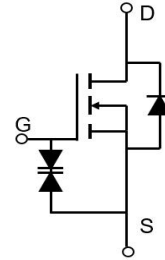
Description

Features

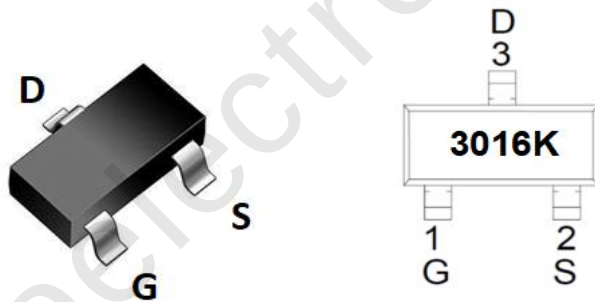
- 30V, 0.6A
- $R_{DS(ON)}$ Typ = 390mΩ @ $V_{GS} = 10V$
- $R_{DS(ON)}$ Typ = 500mΩ @ $V_{GS} = 4.5V$
- $R_{DS(ON)}$ Typ = 1000mΩ @ $V_{GS} = 2.5V$
- Advanced Trench Technology
- Excellent $R_{DS(ON)}$ and Low Gate Charge
- Lead Free
- ESD Protected: 2KV

Application

- Load Switch
- PWM Application
- Power Management



Schematic Diagram



Marking and Pin Assignment

Package Marking and Ordering Information

Device	Marking	Package	Outline	Reel Size	Reel (pcs)	Per Carton (pcs)
CRMLTU3016K	3016K	SOT-23	TAPING	7"	3000	120000

Absolute Maximum Ratings (@ $T_J = 25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter	Value	Units	
V _{DS}	Drain-to-Source Voltage	30	V	
V _{GS}	Gate-to-Source Voltage	±20	V	
I _D	Continuous Drain Current	T _A = 25°C	0.6	A
		T _A = 100°C	0.36	A
I _{DM}	Pulsed Drain Current ⁽¹⁾	2.4	A	
P _D	Power Dissipation	T _A = 25°C	0.35	W
R _{θJA}	Thermal Resistance, Junction to Ambient ⁽²⁾	357	°C/W	
T _J , T _{STG}	Junction & Storage Temperature Range	-55 to 150	°C	

Electrical Characteristics ($T_J = 25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
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Off Characteristics

$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	$I_D = 250\mu\text{A}, V_{GS} = 0\text{V}$	30	-	-	V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS} = 30\text{V}, V_{GS} = 0\text{V}$	-	-	1.0	μA
I_{GSS}	Gate-Body Leakage Current	$V_{DS} = 0\text{V}, V_{GS} = \pm 20\text{V}$	-	-	± 10	μA

On Characteristics

$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_D = 250\mu\text{A}$	0.5	0.85	1.2	V
$R_{DS(ON)}$	Static Drain-Source ON-Resistance ⁽³⁾	$V_{GS} = 10\text{V}, I_D = 0.2\text{A}$	-	390	468	$\text{m}\Omega$
		$V_{GS} = 4.5\text{V}, I_D = 0.15\text{A}$	-	500	600	$\text{m}\Omega$
		$V_{GS} = 2.5\text{V}, I_D = 0.1\text{A}$	-	1000	1200	$\text{m}\Omega$

Dynamic Characteristics

C_{iss}	Input Capacitance	$V_{GS} = 0\text{V}, V_{DS} = 15\text{V},$ $f = 1\text{MHz}$	-	20	-	pF
C_{oss}	Output Capacitance		-	5	-	pF
C_{rss}	Reverse Transfer Capacitance		-	3	-	pF
Q_g	Total Gate Charge	$V_{GS} = 0 \text{ to } 10\text{V}$ $V_{DS} = 15\text{V}, I_D = 0.3\text{A}$	-	1.5	-	nC
Q_{gs}	Gate Source Charge		-	0.2	-	nC
Q_{gd}	Gate Drain("Miller") Charge		-	0.5	-	nC

Switching Characteristics

$t_{d(on)}$	Turn-On DelayTime	$V_{GS} = 10\text{V}, V_{DD} = 15\text{V}$ $I_D = 0.3\text{A}, R_{GEN} = 3\Omega$	-	2	-	ns
t_r	Turn-On Rise Time		-	14	-	ns
$t_{d(off)}$	Turn-Off DelayTime		-	6	-	ns
t_f	Turn-Off Fall Time		-	19	-	ns

Drain-Source Diode Characteristics and Max Ratings

I _S	Maximum Continuous Drain to Source Diode Forward Current	-	-	0.6	A	
I _{SM}	Maximum Pulsed Drain to Source Diode Forward Current	-	-	2.4	A	
V _{SD}	Drain to Source Diode Forward Voltage	V _{GS} = 0V, I _S = 0.2A	-	-	1.2	V

- Notes:
1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature.
 2. $R_{\theta JA}$ is measured with the device mounted on a 1inch² pad of 2oz copper FR4 PCB
 3. Pulse Test: Pulse Width $\leq 300\mu\text{s}$, Duty Cycle $\leq 0.5\%$.

Test Circuit

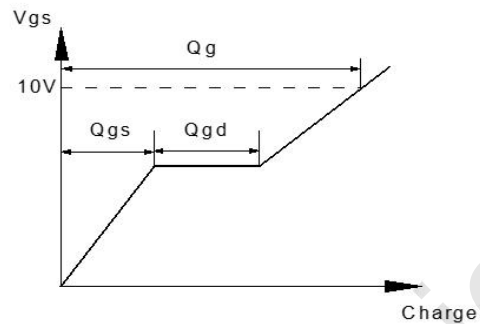
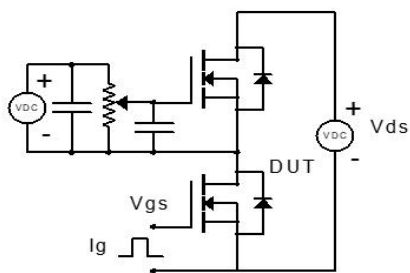


Figure 1: Gate Charge Test Circuit & Waveform

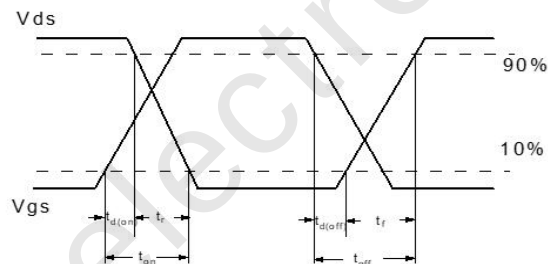
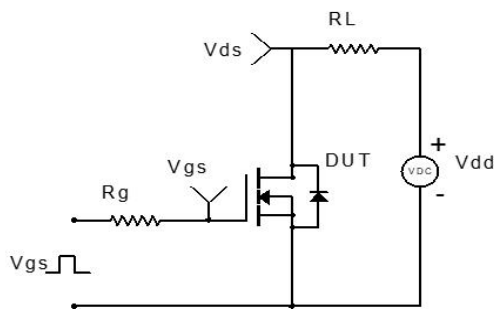


Figure 2: Resistive Switching Test Circuit & Waveform

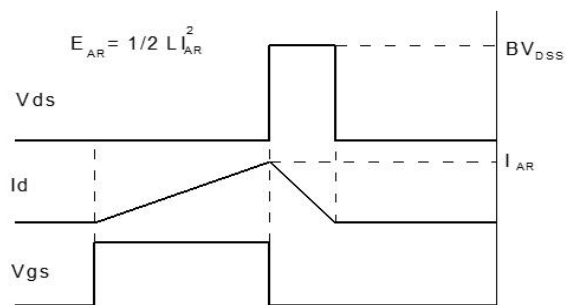
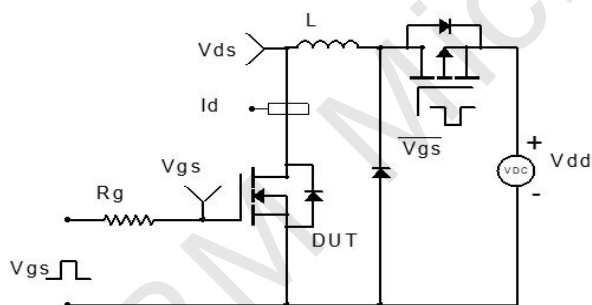


Figure 3: Unclamped Inductive Switching Test Circuit & Waveform

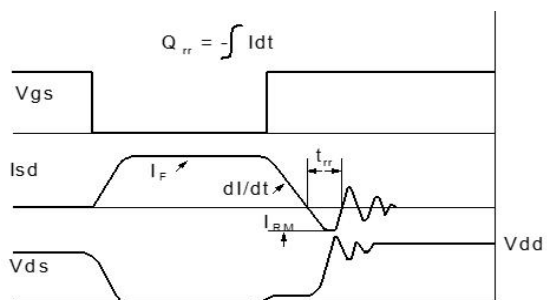
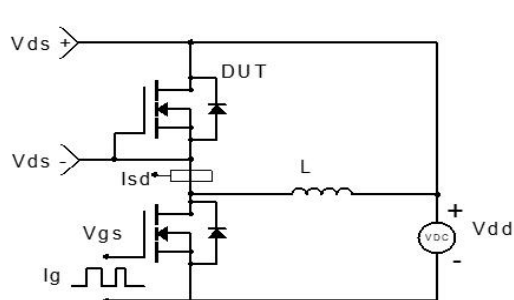
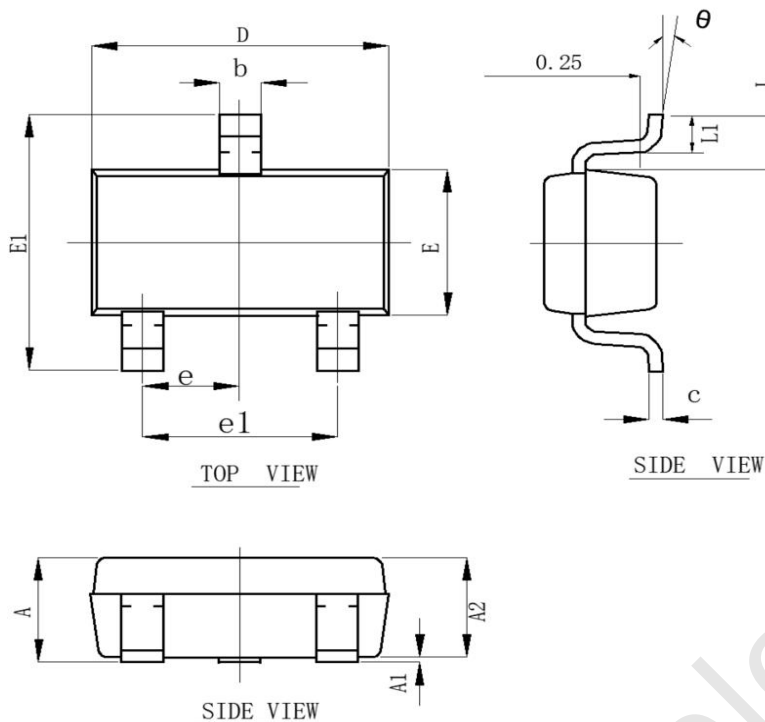


Figure 4: Diode Recovery Test Circuit & Waveform

Package Mechanical Data(SOT-23)



COMMON DIMENSIONS In Millimeters		
SYMBOL	MIN	MAX
A	0.900	1.150
A1	0.000	0.100
A2	0.900	1.050
b	0.300	0.500
c	0.080	0.150
D	2.800	3.000
E	1.200	1.400
E1	2.250	2.550
L	0.550 REF.	
θ	0°	8°
L1	0.300	0.500
e	0.950 TYP.	
e1	1.800	2.000

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