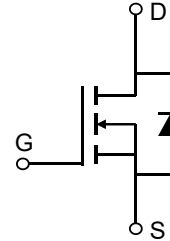


### Description

#### Features

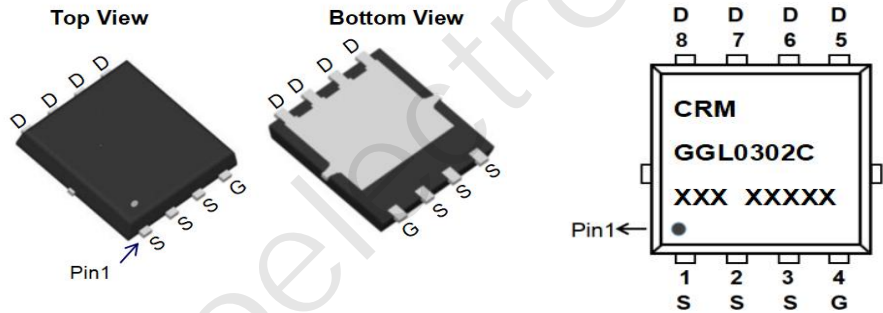
- 30V, 125A
- $R_{DS(ON)}$  Typ = 2.3mΩ @  $V_{GS} = 10V$
- $R_{DS(ON)}$  Typ = 3.5mΩ @  $V_{GS} = 4.5V$
- Advanced Split Gate Trench Technology
- Excellent  $R_{DS(ON)}$  and Low Gate Charge
- 100% UIS TESTED!
- 100%  $\Delta V_{ds}$  TESTED!



Schematic Diagram

#### Application

- Load Switch
- PWM Application
- Power Management



Marking and Pin Assignment

#### Package Marking and Ordering Information

Device	Marking	Package	Outline	Reel Size	Reel (pcs)	Per Carton (pcs)
CRMGGL0302C	CRMGGL0302C	PDFN5x6-8L	TAPING	13"	5000	50000

#### Absolute Maximum Ratings (@ $T_J = 25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter	Value	Units	
V <sub>DS</sub>	Drain-to-Source Voltage	30	V	
V <sub>GS</sub>	Gate-to-Source Voltage	±20	V	
I <sub>D</sub>	Continuous Drain Current	T <sub>C</sub> = 25°C	125	A
		T <sub>C</sub> = 100°C	75	A
I <sub>DM</sub>	Pulsed Drain Current <sup>(1)</sup>	500	A	
E <sub>AS</sub>	Single Pulsed Avalanche Energy <sup>(2)</sup>	90	mJ	
P <sub>D</sub>	Power Dissipation	T <sub>C</sub> = 25°C	79	W
R <sub>θJC</sub>	Thermal Resistance, Junction to Case	1.58	°C/W	
T <sub>J</sub> , T <sub>STG</sub>	Junction & Storage Temperature Range	-55 to 150	°C	

### Electrical Characteristics ( $T_J = 25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
Off Characteristics						
V <sub>(BR)DSS</sub>	Drain-Source Breakdown Voltage	I <sub>D</sub> = 250μA, V <sub>GS</sub> = 0V	30	-	-	V
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	V <sub>DS</sub> = 30V, V <sub>GS</sub> = 0V	-	-	1.0	μA
I <sub>GSS</sub>	Gate-Body Leakage Current	V <sub>DS</sub> = 0V, V <sub>GS</sub> = ±20V	-	-	±100	nA
On Characteristics						
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250μA	1.2	1.7	2.4	V
R <sub>DS(ON)</sub>	Static Drain-Source ON-Resistance <sup>(3)</sup>	V <sub>GS</sub> = 10V, I <sub>D</sub> = 12A	-	2.3	3	mΩ
		V <sub>GS</sub> = 4.5V, I <sub>D</sub> = 8A	-	3.5	4.6	mΩ
Dynamic Characteristics						
C <sub>iss</sub>	Input Capacitance	V <sub>GS</sub> = 0V, V <sub>DS</sub> = 15V, f = 1MHz	-	1837	-	pF
C <sub>oss</sub>	Output Capacitance		-	1606	-	pF
C <sub>rss</sub>	Reverse Transfer Capacitance		-	83	-	pF
Q <sub>g</sub>	Total Gate Charge	V <sub>GS</sub> = 0 to 10V V <sub>DS</sub> = 15V, I <sub>D</sub> = 20A	-	31	-	nC
Q <sub>gs</sub>	Gate Source Charge		-	5.8	-	nC
Q <sub>gd</sub>	Gate Drain("Miller") Charge		-	6	-	nC
Switching Characteristics						
t <sub>d(on)</sub>	Turn-On DelayTime	V <sub>GS</sub> = 10V, V <sub>DD</sub> = 15V I <sub>D</sub> = 20A, R <sub>GEN</sub> = 3Ω	-	14	-	ns
t <sub>r</sub>	Turn-On Rise Time		-	7	-	ns
t <sub>d(off)</sub>	Turn-Off DelayTime		-	35	-	ns
t <sub>f</sub>	Turn-Off Fall Time		-	7	-	ns
Drain-Source Diode Characteristics and Max Ratings						
I <sub>S</sub>	Maximum Continuous Drain to Source Diode Forward Current	V <sub>GS</sub> = 0V, I <sub>S</sub> = 10A	-	-	125	A
I <sub>SM</sub>	Maximum Pulsed Drain to Source Diode Forward Current		-	-	500	A
V <sub>SD</sub>	Drain to Source Diode Forward Voltage		-	-	1.2	V
t <sub>rr</sub>	Body Diode Reverse Recovery Time		-	50	-	ns
Q <sub>rr</sub>	Body Diode Reverse Recovery Charge		I <sub>F</sub> = 15A, di/dt = 100A/us	-	20	-

- Notes:
1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature.
  2.  $E_{AS}$  condition: Starting  $T_J = 25^\circ\text{C}$ ,  $V_{DD} = 15\text{V}$ ,  $V_G = 10\text{V}$ ,  $R_G = 25\Omega$ ,  $L = 0.5\text{mH}$ ,  $I_{AS} = 19\text{A}$
  3. Pulse Test: Pulse Width  $\leq 300\mu\text{s}$ , Duty Cycle  $\leq 0.5\%$ .

## Test Circuit

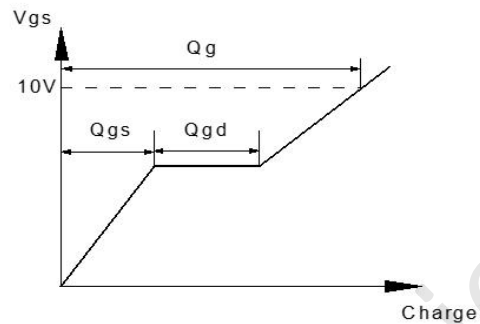
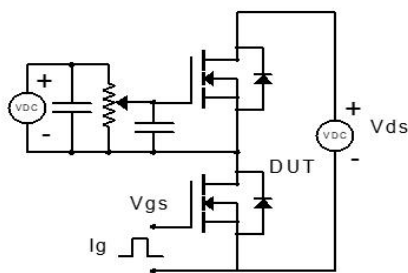


Figure 1: Gate Charge Test Circuit & Waveform

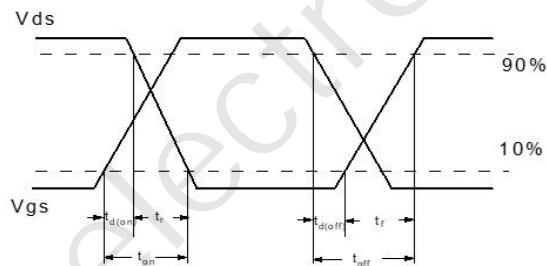
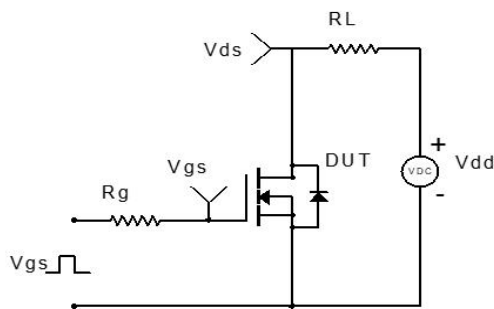


Figure 2: Resistive Switching Test Circuit & Waveform

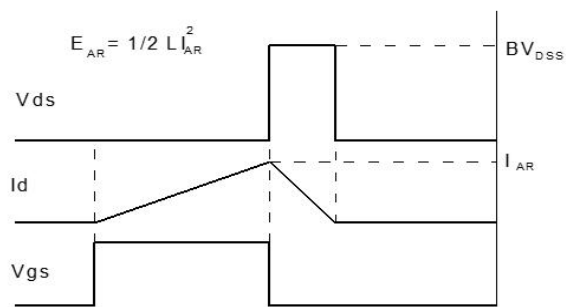
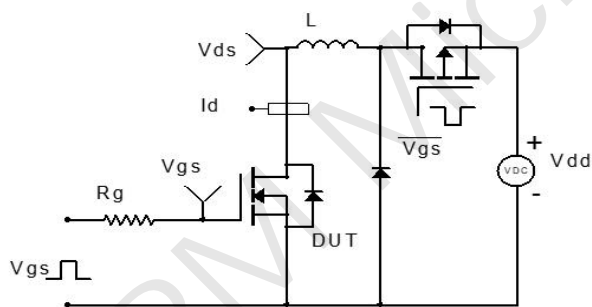


Figure 3: Unclamped Inductive Switching Test Circuit & Waveform

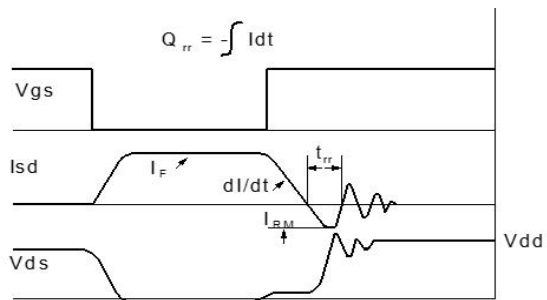
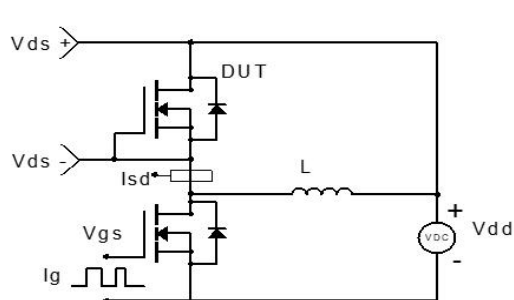
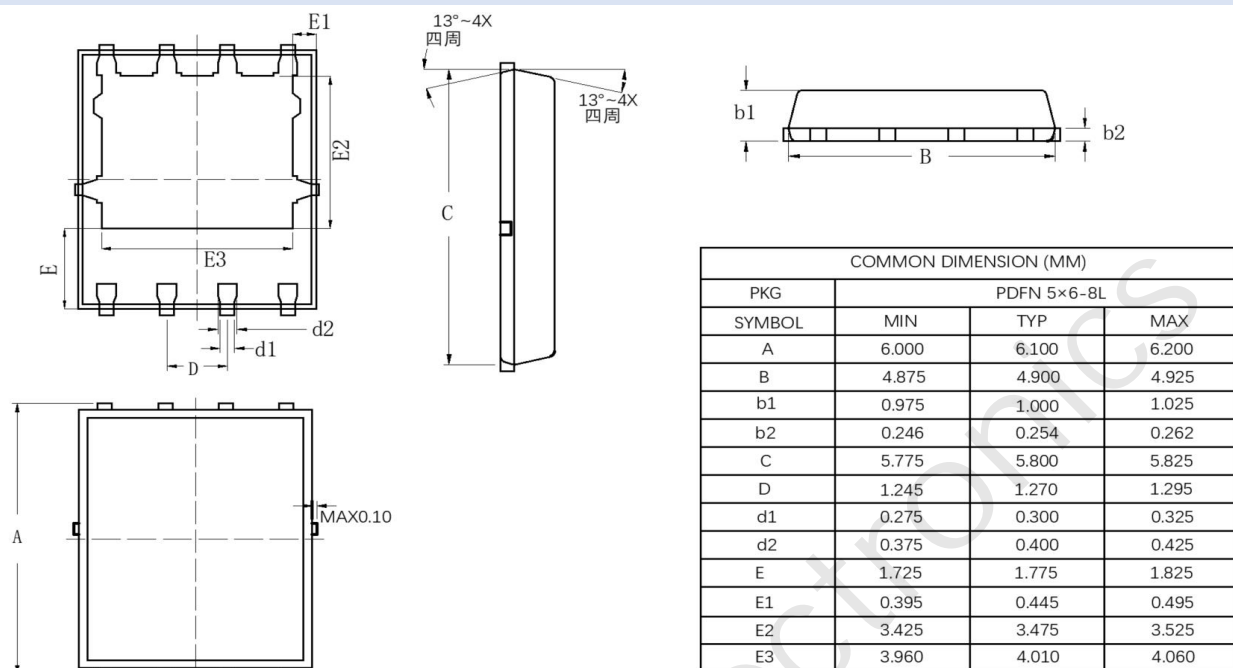


Figure 4: Diode Recovery Test Circuit & Waveform

### Package Mechanical Data(PDFN5x6-8L)




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