

CRMETL1038A N-Channel 100V, 29mΩ Typ. Power MOSFET

Description



• 100V, 25A

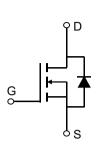
 $R_{DS(ON)}$ Typ = 29m Ω @ V_{GS} = 10V

 $R_{DS(ON)}$ Typ = 31m Ω @ V_{GS} = 4.5V

- Advanced Trench Technology
- Excellent R_{DS(ON)} and Low Gate Charge
- 100% UIS TESTED!
- 100% ΔVds TESTED!

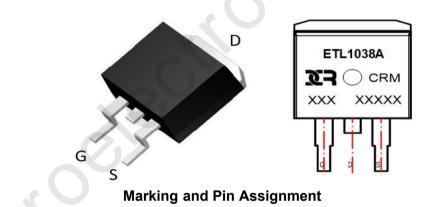
Application

- Load Switch
- PWM Application
- Power Management





Schematic Diagram



Package Marking and Ordering Information

Device	Marking	Package	Outline	Reel Size	Reel (pcs)	Per Carton (pcs)
CRMETL1038A	CRMETL1038A	TO-263-3L	TAPING	13"	800	4000

Absolute Maximum Ratings (@ T_J = 25°C unless otherwise specified)

Symbol	Parameter		Value	Units
V _{DS}	Drain-to-Source Voltage		100	V
V_{GS}	Gate-to-Source Voltage		±20	V
I _D	Continuous Drain Current	T _c = 25°C	25	А
		T _c = 100°C	15	А
I _{DM}	Pulsed Drain Current ⁽¹⁾		100	А
E _{AS}	Single Pulsed Avalanche Energy ⁽²⁾		64	mJ
P _D	Power Dissipation	T _C = 25°C	44.5	W
$R_{ extsf{ heta}JC}$	Thermal Resistance, Junction to Case		2.8	°C/W
Τ _J , T _{stg}	Junction & Storage Temperature Range		-55 to 150	°C



Electrical Characteristics (T₁ = 25°C unless otherwise specified)

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
Off Chara	acteristics					
V _{(BR)DSS}	Drain-Source Breakdown Voltage	I _D = 250μA, V _{GS} = 0V	100	-	-	V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = 100V, V _{GS} = 0V	-	-	1.0	μA
I _{GSS}	Gate-Body Leakage Current	$V_{DS} = 0V, V_{GS} = \pm 20V$	-	-	±100	nA
On Chara	acteristics					
V _{GS(th)}	Gate Threshold Voltage	V_{DS} = V_{GS} , I_D = 250 μ A	1.2	1.7	2.3	V
D		V _{GS} = 10V, I _D = 15A	-	29	38	mΩ
R _{DS(ON)}	Static Drain-Source ON-Resistance ⁽³⁾	V _{GS} = 4.5V, I _D = 10A	-	31	40	mΩ
Dynamic	Characteristics					
C _{iss}	Input Capacitance		-	2550	-	pF
C _{oss}	Output Capacitance	V _{GS} = 0V, V _{DS} = 25V, f = 1MHz		100	-	pF
C _{rss}	Reverse Transfer Capacitance			88	-	pF
Q _g	Total Gate Charge) -	66	-	nC
Q_{gs}	Gate Source Charge	$V_{GS} = 0$ to 10V $V_{DS} = 25V$, $I_D = 15A$	-	10	-	nC
Q_{gd}	Gate Drain("Miller") Charge	$v_{\rm DS} = 25 v, i_{\rm D} = 15 A$	-	14	-	nC
Switchin	g Characteristics					
t _{d(on)}	Turn-On DelayTime		-	11	-	ns
t _r	Turn-On Rise Time	V _{GS} = 10V, V _{DD} = 30V	-	45	-	ns
t _{d(off)}	Turn-Off DelayTime	I _D = 25A, R _{GEN} = 1.8Ω	-	67	-	ns
t _f	Turn-Off Fall Time		-	48	-	ns
Drain-So	urce Diode Characteristics and M	lax Ratings				
I _S	Maximum Continuous Drain to Source Di	ode Forward Current	-	-	25	А
I _{SM}	Maximum Pulsed Drain to Source Diode	Forward Current	-	-	100	А
V _{SD}	Drain to Source Diode Forward Voltage	V _{GS} = 0V, I _S = 15A	-	-	1.2	V
trr	Body Diode Reverse Recovery Time		-	28	-	ns
Qrr	Body Diode Reverse Recovery Charge	I _F = 25A, di/dt = 100A/us	-	40	-	nC

Notes:

1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature.

2. E_{AS} condition: Starting T_J=25°C, V_{DD}=50V, V_G=10V, R_G=250hm, L=0.5mH, I_{AS}=16A

3. Pulse Test: Pulse Width ${\leqslant}300\mu s,$ Duty Cycle ${\leqslant}0.5\%.$



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Test Circuit

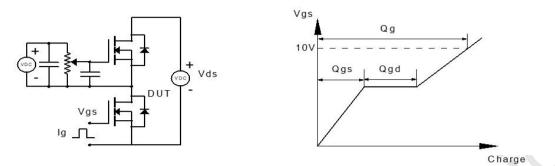


Figure 1: Gate Charge Test Circuit & Waveform

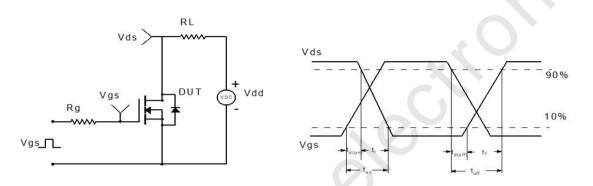


Figure 2: Resistive Switching Test Circuit & Waveform

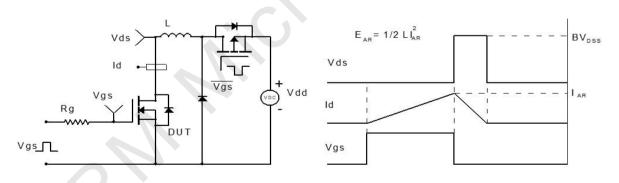


Figure 3: Unclamped Inductive Switching Test Circuit& Waveform

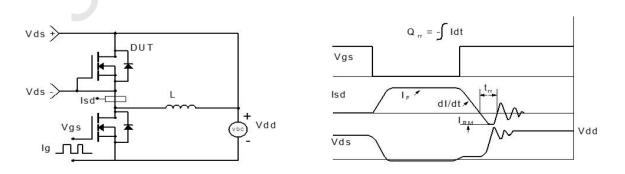
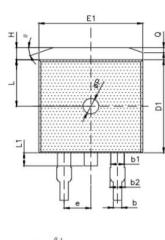
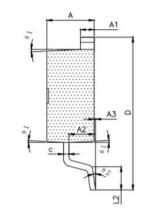


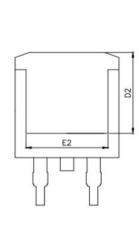
Figure 4: Diode Recovery Test Circuit & Waveform



Package Mechanical Data(TO-263-3L)







SYMBOL	MILLIMETER				
	MIN	NOM	MAX		
A	4.40	4.50	4.60		
A1	1.20	1.30	1.40		
A2	2.30	2.40	2.50		
A3	0.03	0.13	0.23		
b	0.70	0.80	0.90		
b1	1.21	1.27	1.40		
b2	1.25	1.35	1.45		
с	0.40	0.50	0.60		
D	14.80	15.10	15.40		
D1	9.10	9.20	9.30		
D2	8.00		10.20		
E	9.70	9.90			
E1	9.68	9.88	10.08		
E2	7.80				
e	2.54 (BSC)				
н	1.00	1.20	1.40		
L	4.30	4.60	4.90		
L1	1.10	1.30	1.50		
L2	2.10	2.30	2.50		
ØP	1.40 1.50		1.60		
Q	0.50 (REF)				
θ	16'	20*	24'		
0 1	1*	3.	5*		
02	0.	-	9*		

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