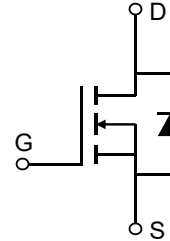


Description

Features

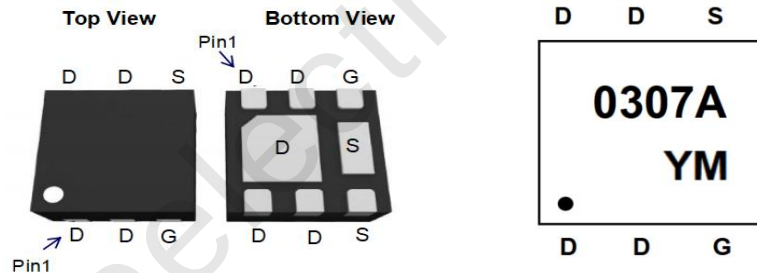
- 30V, 22A
 $R_{DS(ON)}$ Typ = 8.0mΩ @ $V_{GS} = 10V$
 $R_{DS(ON)}$ Typ = 12.2mΩ @ $V_{GS} = 4.5V$
- Advanced Trench Technology
- Excellent $R_{DS(ON)}$ and Low Gate Charge
- Lead Free



Schematic Diagram

Application

- Load Switch
- PWM Application
- Power Management



Marking and Pin Assignment

Package Marking and Ordering Information

Device	Marking	Package	Outline	Reel Size	Reel (pcs)	Per Carton (pcs)
CRMVTL0307A	0307A	DFN2020-6L	TAPING	7"	3000	120000

Absolute Maximum Ratings (@ $T_J = 25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter	Value	Units	
V _{DS}	Drain-to-Source Voltage	30	V	
V _{GS}	Gate-to-Source Voltage	±20	V	
I _D	Continuous Drain Current	T _C = 25°C	22	A
		T _C = 100°C	13.2	A
I _{DM}	Pulsed Drain Current ⁽¹⁾	88	A	
P _D	Power Dissipation	T _C = 25°C	7.8	W
R _{θJC}	Thermal Resistance, Junction to Case	16	°C/W	
T _J , T _{STG}	Junction & Storage Temperature Range	-55 to 150	°C	

Electrical Characteristics ($T_J = 25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
Off Characteristics						
V _{(BR)DSS}	Drain-Source Breakdown Voltage	I _D = 250μA, V _{GS} = 0V	30	-	-	V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = 30V, V _{GS} = 0V	-	-	1.0	μA
I _{GSS}	Gate-Body Leakage Current	V _{DS} = 0V, V _{GS} = ±20V	-	-	±100	nA
On Characteristics						
V _{GS(th)}	Gate Threshold Voltage	V _{DS} = V _{GS} , I _D = 250μA	1	1.5	2.2	V
R _{DS(ON)}	Static Drain-Source ON-Resistance ⁽²⁾	V _{GS} = 10V, I _D = 5A	-	8	10.5	mΩ
		V _{GS} = 4.5V, I _D = 3A	-	12	15.5	mΩ
Dynamic Characteristics						
C _{iss}	Input Capacitance	V _{GS} = 0V, V _{DS} = 15V, f = 1MHz	-	1061	-	pF
C _{oss}	Output Capacitance		-	127	-	pF
C _{rss}	Reverse Transfer Capacitance		-	100	-	pF
Q _g	Total Gate Charge	V _{GS} = 0 to 10V V _{DS} = 15V, I _D = 20A	-	20	-	nC
Q _{gs}	Gate Source Charge		-	4	-	nC
Q _{gd}	Gate Drain("Miller") Charge		-	5	-	nC
Switching Characteristics						
t _{d(on)}	Turn-On DelayTime	V _{GS} = 10V, V _{DD} = 15V I _D = 20A, R _{GEN} = 3Ω	-	6	-	ns
t _r	Turn-On Rise Time		-	19	-	ns
t _{d(off)}	Turn-Off DelayTime		-	22	-	ns
t _f	Turn-Off Fall Time		-	5	-	ns
Drain-Source Diode Characteristics and Max Ratings						
I _S	Maximum Continuous Drain to Source Diode Forward Current		-	-	22	A
I _{SM}	Maximum Pulsed Drain to Source Diode Forward Current		-	-	88	A
V _{SD}	Drain to Source Diode Forward Voltage	V _{GS} = 0V, I _S = 5A	-	-	1.2	V
trr	Body Diode Reverse Recovery Time	I _F = 20A, di/dt = 100A/us	-	8	-	ns
Qrr	Body Diode Reverse Recovery Charge		-	1.6	-	nC

Notes: 1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature.

2. Pulse Test: Pulse Width $\leq 300\mu\text{s}$, Duty Cycle $\leq 0.5\%$.

Typical Performance Characteristics

Figure 1: Output Characteristics

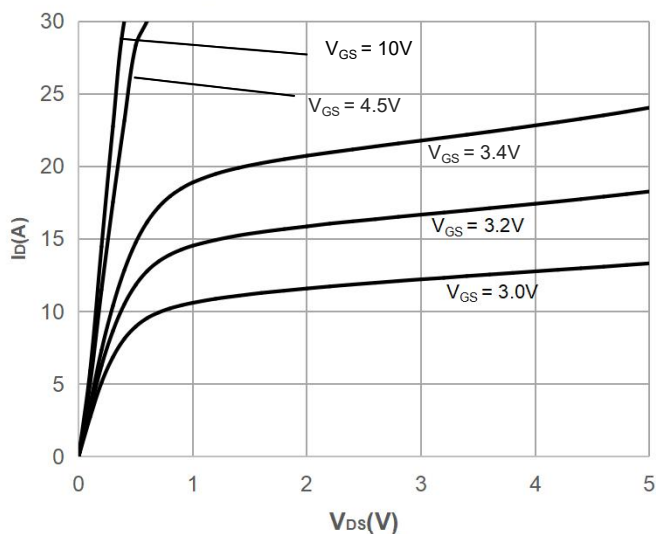


Figure 2: Typical Transfer Characteristics

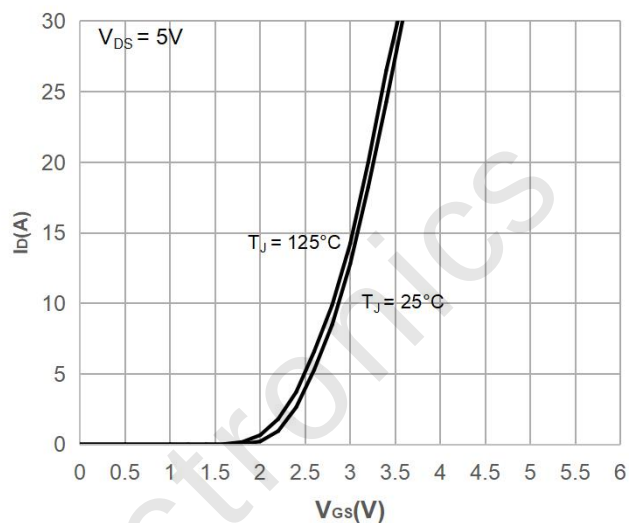


Figure 3: On-resistance vs. Drain Current

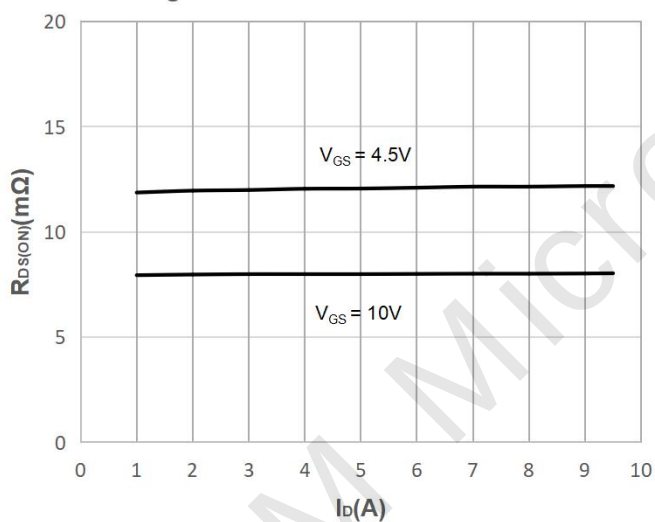


Figure 4: Body Diode Characteristics

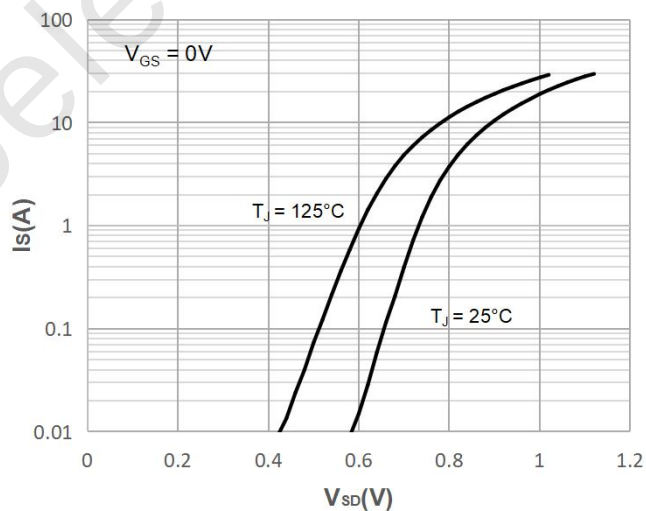


Figure 5: Gate Charge Characteristics

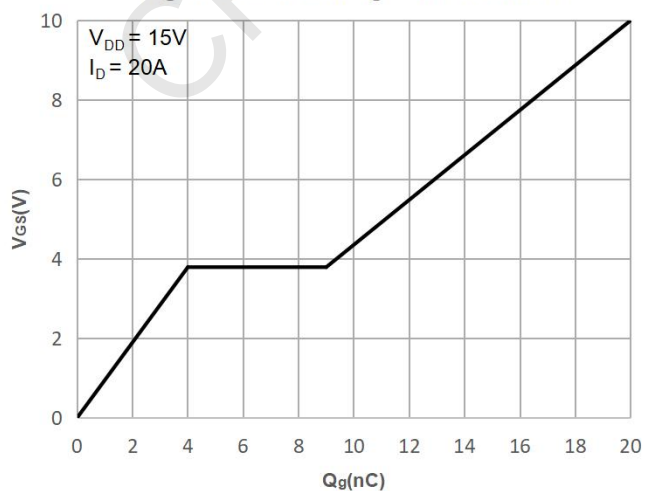
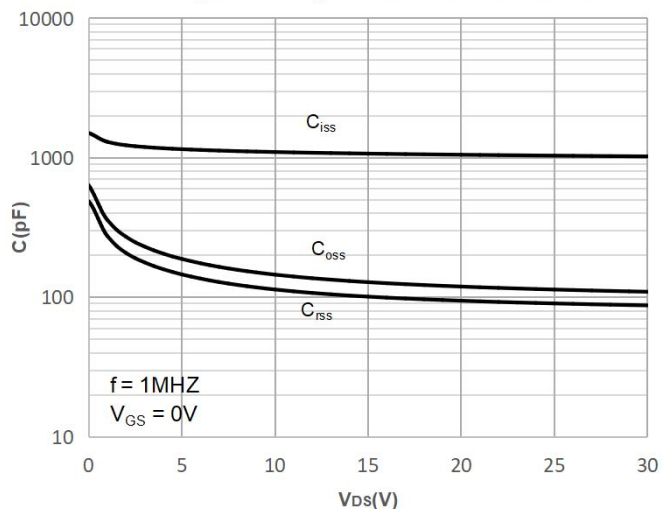


Figure 6: Capacitance Characteristics



Typical Performance Characteristics

Figure 7: Normalized Breakdown voltage vs. Junction Temperature

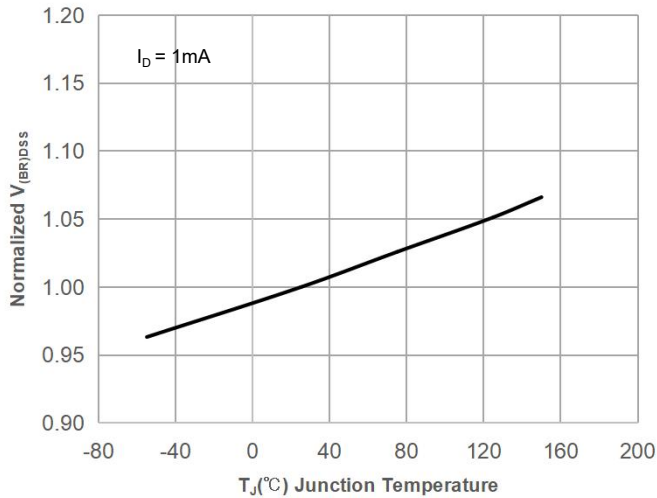


Figure 8: Normalized on Resistance vs. Junction Temperature

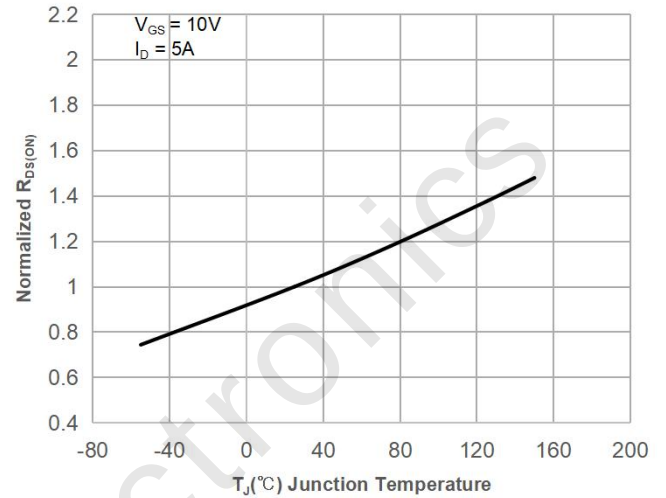


Figure 9: Maximum Safe Operating Area

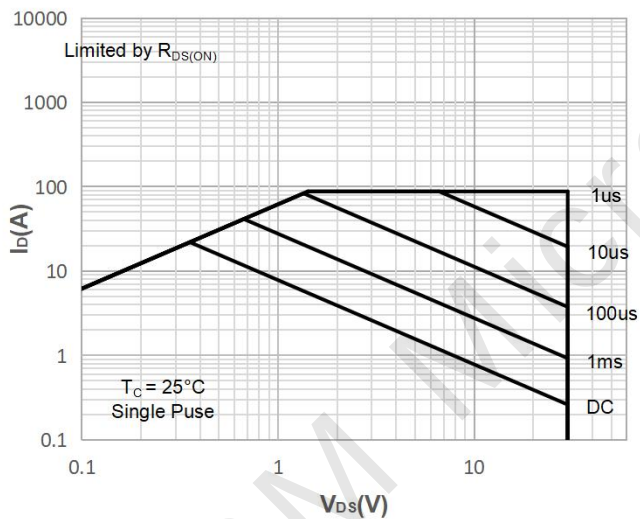


Figure 10: Maximum Continuous Drain Current vs. Case Temperature

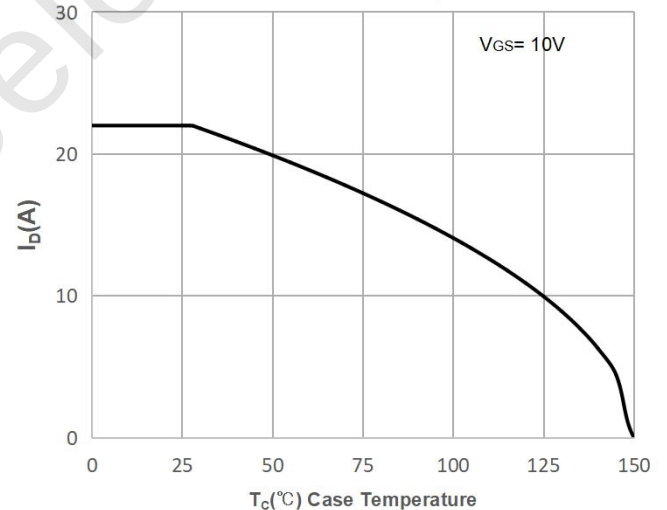


Figure 11: Normalized Maximum Transient Thermal Impedance

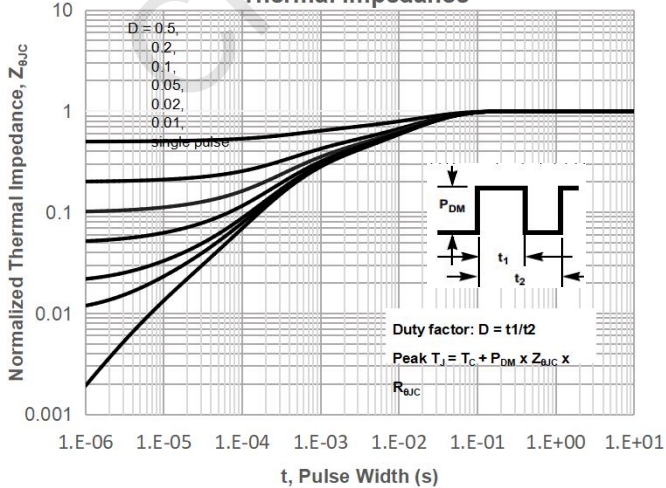
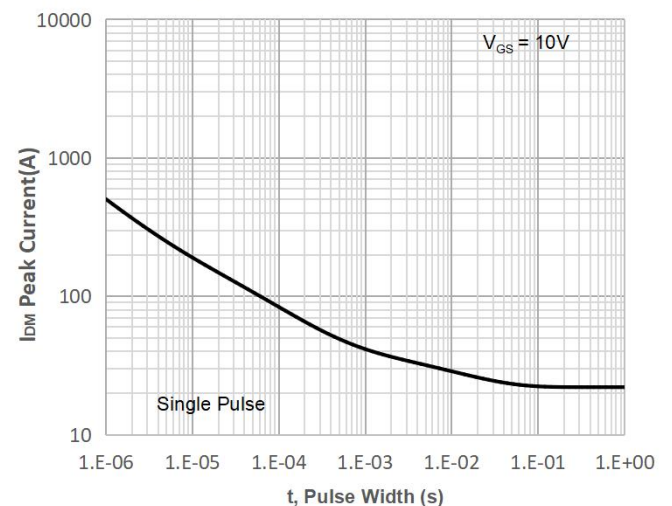


Figure 12: Peak Current Capacity



Test Circuit

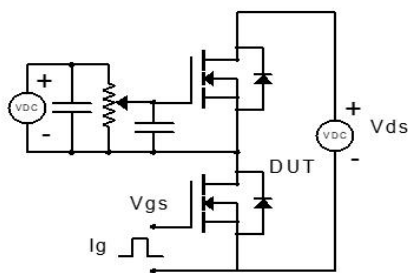


Figure 1: Gate Charge Test Circuit & Waveform

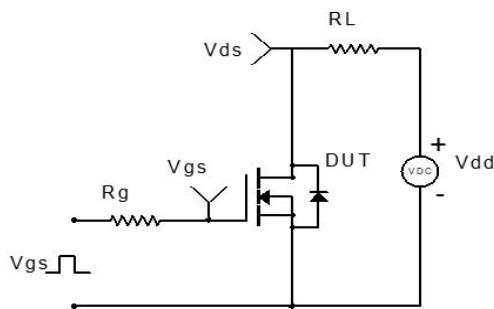


Figure 2: Resistive Switching Test Circuit & Waveform

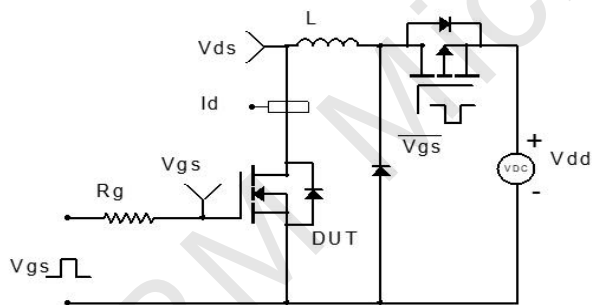


Figure 3: Unclamped Inductive Switching Test Circuit & Waveform

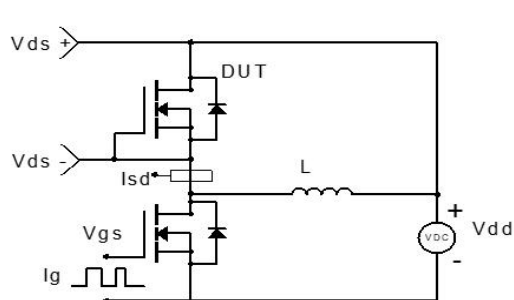
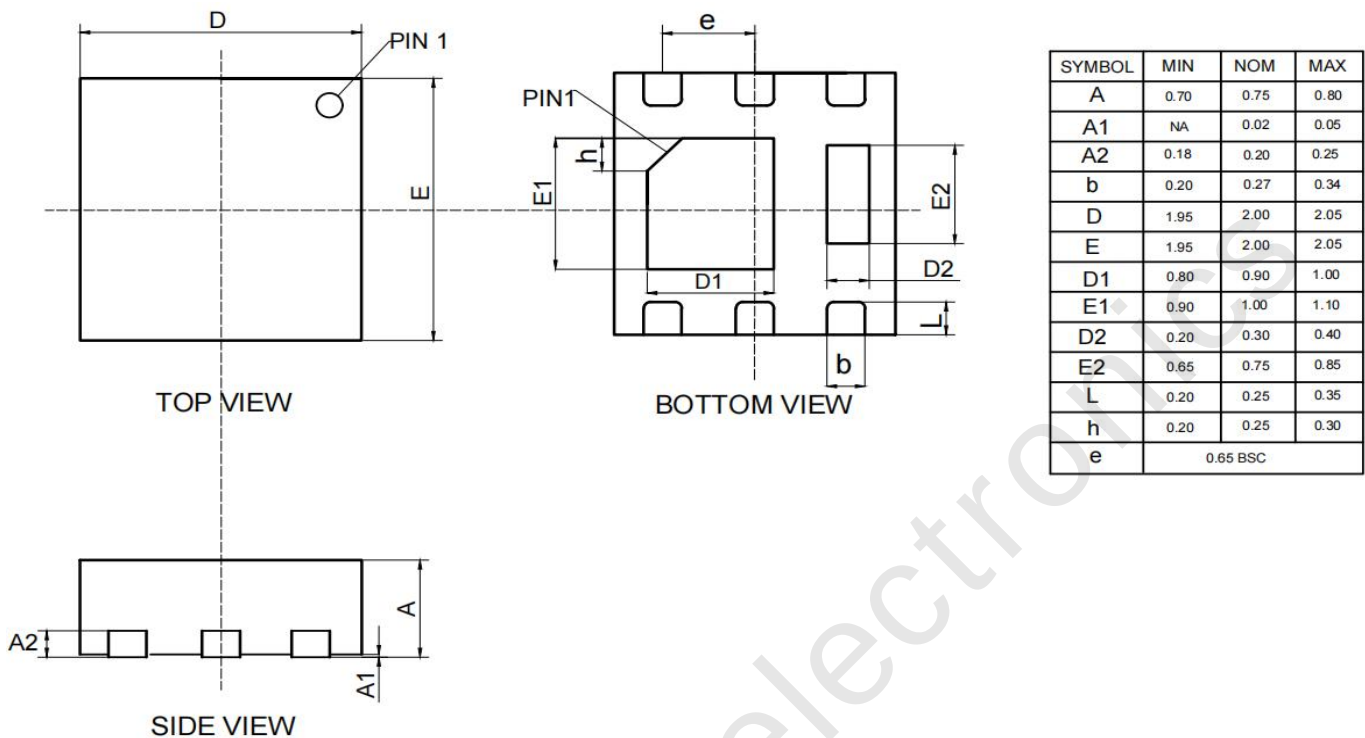


Figure 4: Diode Recovery Test Circuit & Waveform

Package Mechanical Data(DFN2020-6L)



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