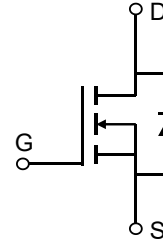


Description

Features

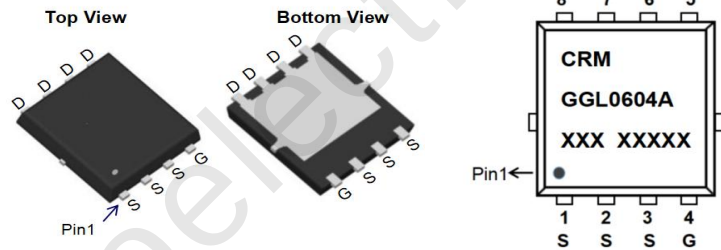
- 60V, 95A
- $R_{DS(ON)}$ Typ = 3.6mΩ @ $V_{GS} = 10V$
- $R_{DS(ON)}$ Typ = 4.8mΩ @ $V_{GS} = 4.5V$
- Advanced Split Gate Trench Technology
- Excellent $R_{DS(ON)}$ and Low Gate Charge
- 100% UIS TESTED!
- 100% ΔV_{ds} TESTED!



Schematic Diagram

Application

- Load Switch
- PWM Application
- Power Management



Marking and Pin Assignment

Package Marking and Ordering Information

Device	Marking	Package	Outline	Reel Size	Reel (pcs)	Per Carton (pcs)
CRMGGL0604A	CRMGGL0604A	PDFN5x6-8L	TAPING	13"	5000	50000

Absolute Maximum Ratings (@ $T_J = 25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter	Value	Units
V_{DS}	Drain-to-Source Voltage	60	V
V_{GS}	Gate-to-Source Voltage	± 20	V
I_D	Continuous Drain Current	$T_C = 25^\circ\text{C}$	95
		$T_C = 100^\circ\text{C}$	57
I_{DM}	Pulsed Drain Current ⁽¹⁾	380	A
E_{AS}	Single Pulsed Avalanche Energy ⁽²⁾	138	mJ
P_D	Power Dissipation	$T_C = 25^\circ\text{C}$	78
$R_{\theta JC}$	Thermal Resistance, Junction to Case	1.6	$^\circ\text{C/W}$
T_J, T_{STG}	Junction & Storage Temperature Range	-55 to 150	$^\circ\text{C}$

Electrical Characteristics ($T_J = 25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
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Off Characteristics

$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	$I_D = 250\mu\text{A}$, $V_{GS} = 0\text{V}$	60	-	-	V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS} = 60\text{V}$, $V_{GS} = 0\text{V}$	-	-	1.0	μA
I_{GSS}	Gate-Body Leakage Current	$V_{DS} = 0\text{V}$, $V_{GS} = \pm 20\text{V}$	-	-	± 100	nA

On Characteristics

$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS} = V_{GS}$, $I_D = 250\mu\text{A}$	1.2	1.6	2.5	V
$R_{DS(ON)}$	Static Drain-Source ON-Resistance ⁽³⁾	$V_{GS} = 10\text{V}$, $I_D = 20\text{A}$	-	3.6	4.7	mΩ
		$V_{GS} = 4.5\text{V}$, $I_D = 10\text{A}$	-	4.8	6.2	mΩ

Dynamic Characteristics

C_{iss}	Input Capacitance	$V_{GS} = 0\text{V}$, $V_{DS} = 30\text{V}$, $f = 1\text{MHz}$	-	2000	-	pF
C_{oss}	Output Capacitance		-	660	-	pF
C_{rss}	Reverse Transfer Capacitance		-	28	-	pF
Q_g	Total Gate Charge	$V_{GS} = 0$ to 10V $V_{DS} = 30\text{V}$, $I_D = 20\text{A}$	-	35	-	nC
Q_{gs}	Gate Source Charge		-	10	-	nC
Q_{gd}	Gate Drain("Miller") Charge		-	7	-	nC

Switching Characteristics

$t_{d(on)}$	Turn-On DelayTime	$V_{GS} = 10\text{V}$, $V_{DD} = 30\text{V}$ $I_D = 20\text{A}$, $R_{GEN} = 4.5\Omega$	-	12	-	ns
t_r	Turn-On Rise Time		-	34	-	ns
$t_{d(off)}$	Turn-Off DelayTime		-	25	-	ns
t_f	Turn-Off Fall Time		-	30	-	ns

Drain-Source Diode Characteristics and Max Ratings

I_S	Maximum Continuous Drain to Source Diode Forward Current	$V_{GS} = 0\text{V}$, $I_S = 20\text{A}$	-	-	95	A
I_{SM}	Maximum Pulsed Drain to Source Diode Forward Current		-	-	380	A
V_{SD}	Drain to Source Diode Forward Voltage		-	-	1.2	V
t_{rr}	Body Diode Reverse Recovery Time		-	38	-	ns
Q_{rr}	Body Diode Reverse Recovery Charge		-	23	-	nC

- Notes:
1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature.
 2. E_{AS} condition: Starting $T_J = 25^\circ\text{C}$, $V_{DD} = 30\text{V}$, $V_G = 10\text{V}$, $R_G = 25\text{ohm}$, $L = 0.5\text{mH}$, $I_{AS} = 23.5\text{A}$
 3. Pulse Test: Pulse Width $\leq 300\mu\text{s}$, Duty Cycle $\leq 0.5\%$.

Typical Performance Characteristics

Figure 1: Output Characteristics

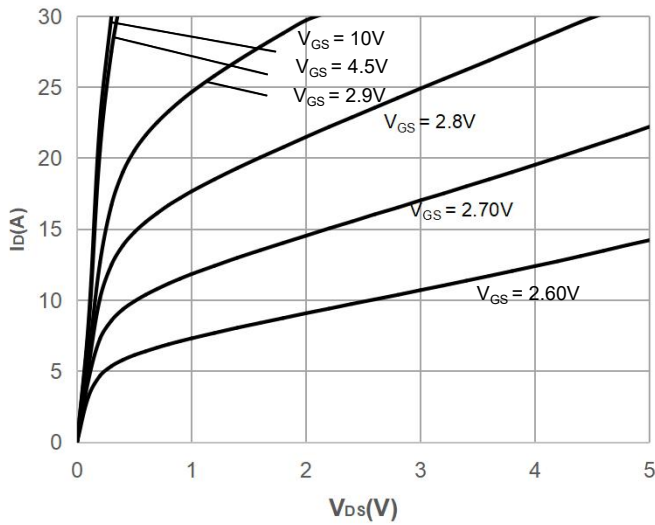


Figure 2: Typical Transfer Characteristics

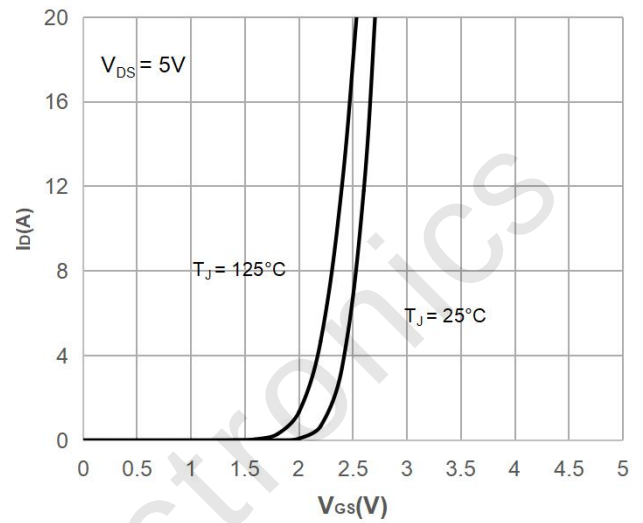


Figure 3: On-resistance vs. Drain Current

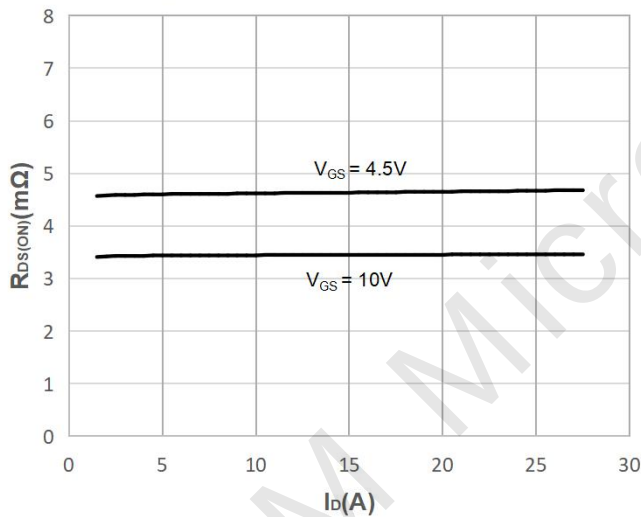


Figure 4: Body Diode Characteristics

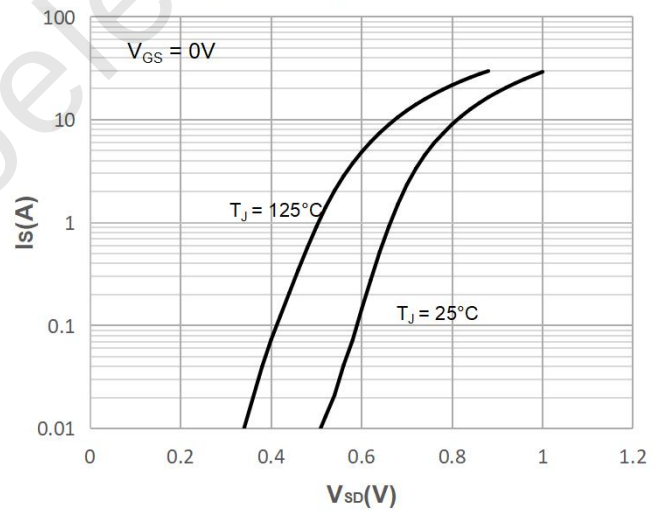


Figure 5: Gate Charge Characteristics

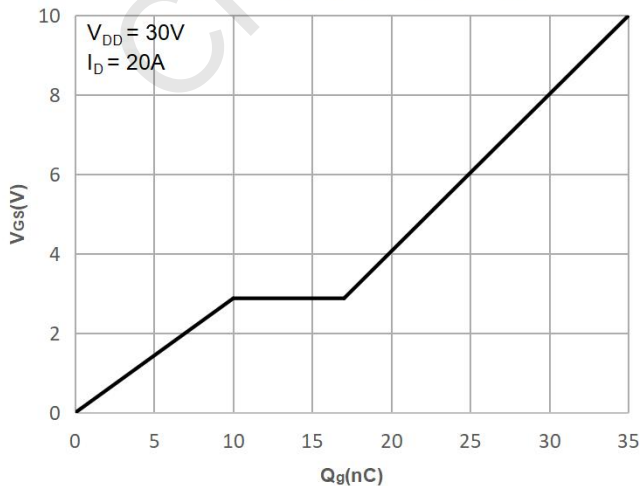
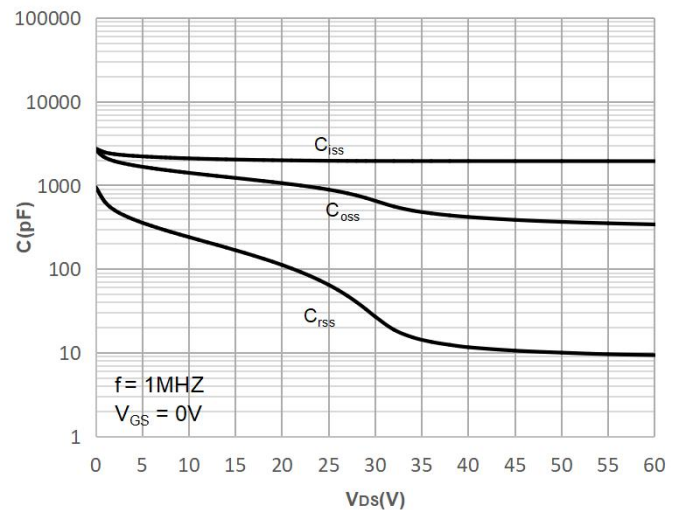


Figure 6: Capacitance Characteristics



Typical Performance Characteristics

Figure 7: Normalized Breakdown voltage vs. Junction Temperature

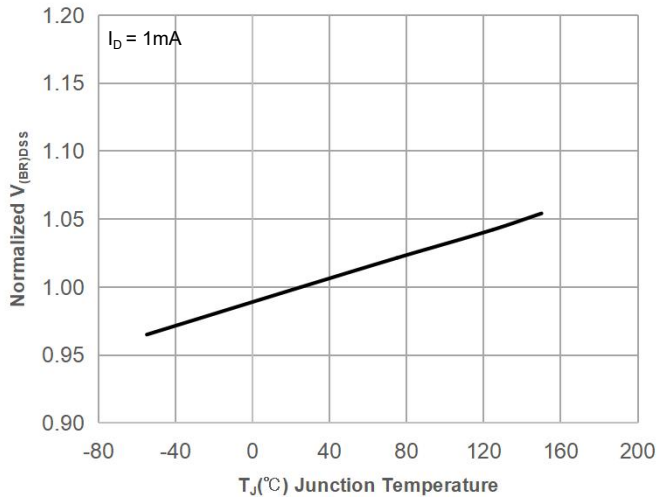


Figure 8: Normalized on Resistance vs. Junction Temperature

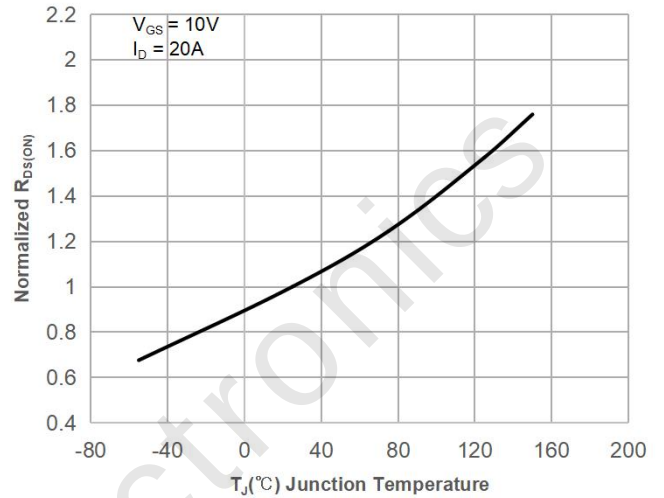


Figure 9: Maximum Safe Operating Area

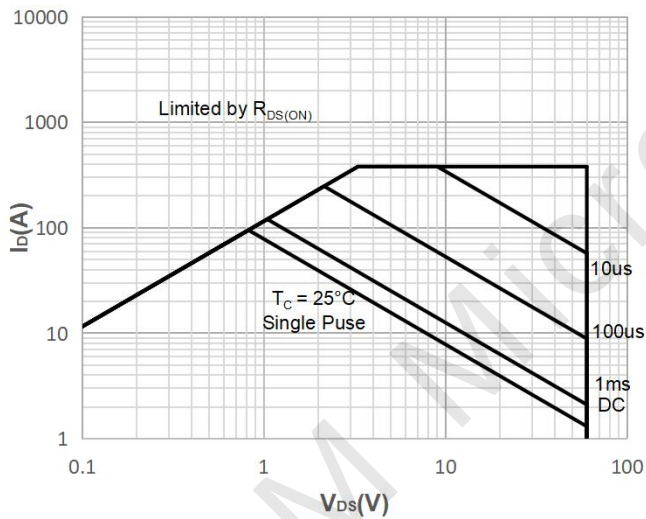


Figure 10: Maximum Continuous Drain Current vs. Case Temperature

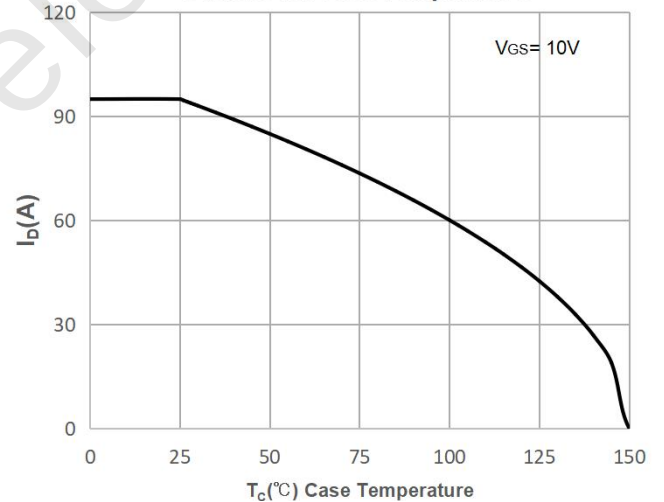


Figure 11: Normalized Maximum Transient Thermal Impedance

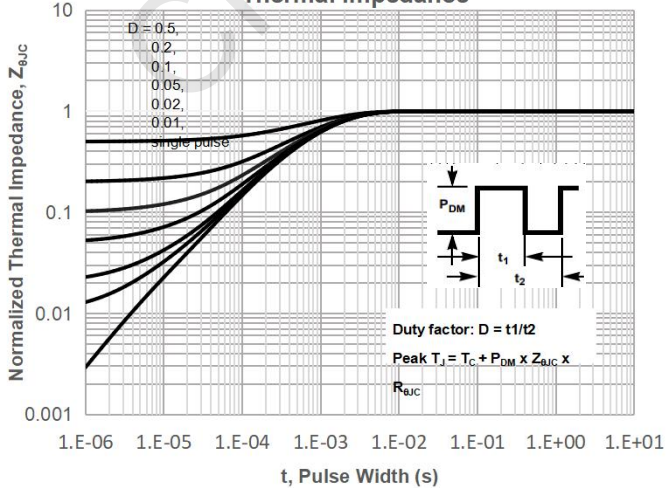
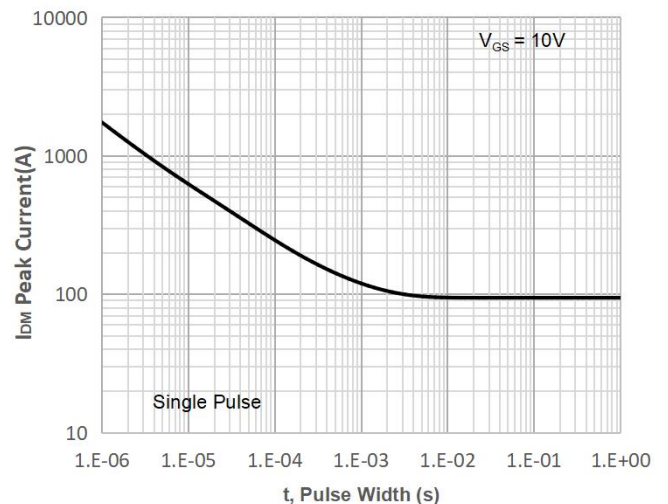


Figure 12: Peak Current Capacity



Test Circuit

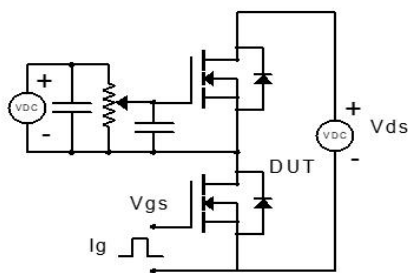


Figure 1: Gate Charge Test Circuit & Waveform

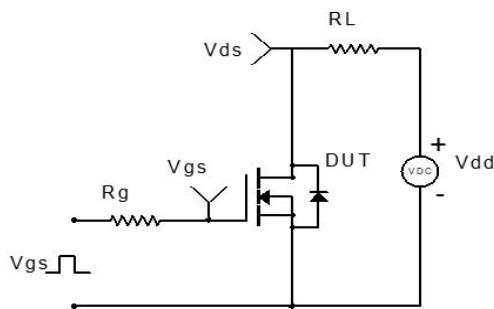


Figure 2: Resistive Switching Test Circuit & Waveform

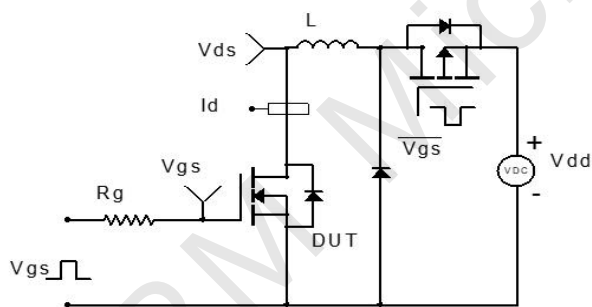


Figure 3: Unclamped Inductive Switching Test Circuit & Waveform

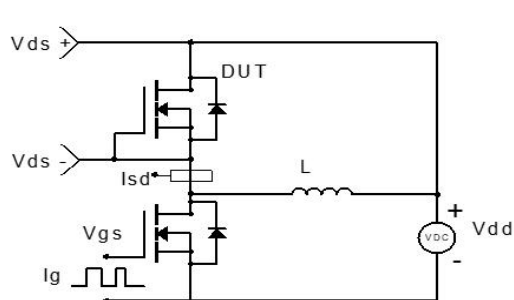
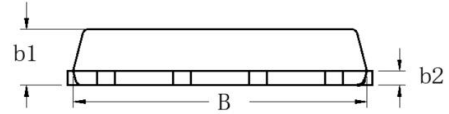
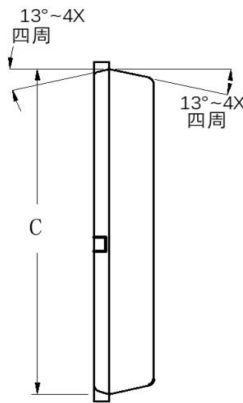
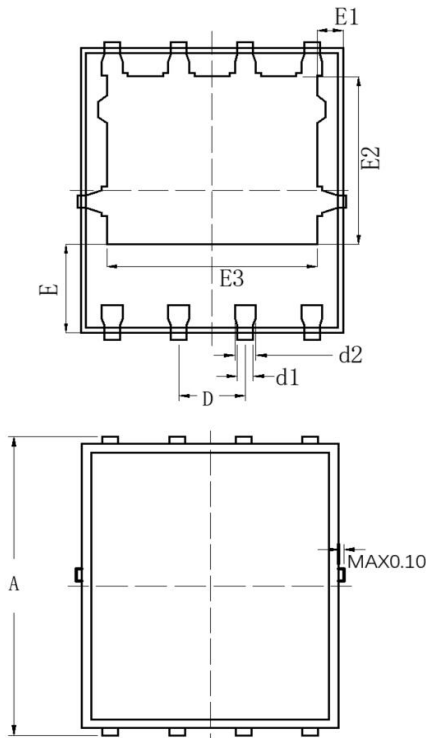


Figure 4: Diode Recovery Test Circuit & Waveform

Package Mechanical Data(PDFN5x6-8L)



COMMON DIMENSION (MM)			
PKG	PDFN 5×6-8L		
SYMBOL	MIN	TYP	MAX
A	6.000	6.100	6.200
B	4.875	4.900	4.925
b1	0.975	1.000	1.025
b2	0.246	0.254	0.262
C	5.775	5.800	5.825
D	1.245	1.270	1.295
d1	0.275	0.300	0.325
d2	0.375	0.400	0.425
E	1.725	1.775	1.825
E1	0.395	0.445	0.495
E2	3.425	3.475	3.525
E3	3.960	4.010	4.060

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