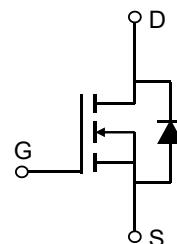


## Description

### Features

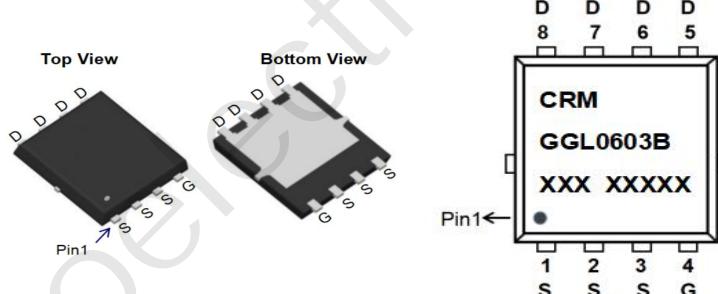
- 60V, 160A
- $R_{DS(ON)}$  Typ = 1.9mΩ @  $V_{GS}$  = 10V
- $R_{DS(ON)}$  Typ = 2.5mΩ @  $V_{GS}$  = 4.5V
- Advanced Split Gate Trench Technology
- Excellent  $R_{DS(ON)}$  and Low Gate Charge
- 100% UIS TESTED!
- 100%  $\Delta V_{ds}$  TESTED!



Schematic Diagram

### Application

- Load Switch
- PWM Application
- Power Management



Marking and Pin Assignment

### Package Marking and Ordering Information

Device	Marking	Package	Outline	Reel Size	Reel (pcs)	Per Carton (pcs)
CRMGGL0603B	CRMGGL0603B	PDFN5x6-8L	TAPING	13"	5000	50000

### Absolute Maximum Ratings (@ $T_J$ = 25°C unless otherwise specified)

Symbol	Parameter	Value	Units
$V_{DS}$	Drain-to-Source Voltage	60	V
$V_{GS}$	Gate-to-Source Voltage	$\pm 20$	V
$I_D$	Continuous Drain Current $T_C = 25^\circ\text{C}$	160	A
	$T_C = 100^\circ\text{C}$	96	A
$I_{DM}$	Pulsed Drain Current <sup>(1)</sup>	640	A
$E_{AS}$	Single Pulsed Avalanche Energy <sup>(2)</sup>	400	mJ
$P_D$	Power Dissipation $T_C = 25^\circ\text{C}$	101	W
$R_{\theta JC}$	Thermal Resistance, Junction to Case	1.24	°C/W
$T_J, T_{STG}$	Junction & Storage Temperature Range	-55 to 150	°C

### Electrical Characteristics ( $T_J = 25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
<b>Off Characteristics</b>						
$V_{(\text{BR})\text{DSS}}$	Drain-Source Breakdown Voltage	$I_D = 250\mu\text{A}, V_{GS} = 0\text{V}$	60	-	-	V
$I_{\text{DSS}}$	Zero Gate Voltage Drain Current	$V_{DS} = 60\text{V}, V_{GS} = 0\text{V}$	-	-	1.0	$\mu\text{A}$
$I_{\text{GSS}}$	Gate-Body Leakage Current	$V_{DS} = 0\text{V}, V_{GS} = \pm 20\text{V}$	-	-	$\pm 100$	nA
<b>On Characteristics</b>						
$V_{GS(\text{th})}$	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_D = 250\mu\text{A}$	1	1.6	2.5	V
$R_{\text{DS(ON)}}$	Static Drain-Source ON-Resistance <sup>(3)</sup>	$V_{GS} = 10\text{V}, I_D = 20\text{A}$	-	1.9	2.5	$\text{m}\Omega$
		$V_{GS} = 4.5\text{V}, I_D = 15\text{A}$	-	2.5	3.3	$\text{m}\Omega$
<b>Dynamic Characteristics</b>						
$C_{\text{iss}}$	Input Capacitance		-	5324	-	pF
$C_{\text{oss}}$	Output Capacitance	$V_{GS} = 0\text{V}, V_{DS} = 25\text{V}, f = 1\text{MHz}$	-	1543	-	pF
$C_{\text{rss}}$	Reverse Transfer Capacitance		-	46	-	pF
$Q_g$	Total Gate Charge		-	101	-	nC
$Q_{gs}$	Gate Source Charge	$V_{GS} = 0 \text{ to } 10\text{V}$ $V_{DS} = 30\text{V}, I_D = 30\text{A}$	-	17	-	nC
$Q_{gd}$	Gate Drain("Miller") Charge		-	22	-	nC
<b>Switching Characteristics</b>						
$t_{d(on)}$	Turn-On DelayTime		-	15.5	-	ns
$t_r$	Turn-On Rise Time	$V_{GS} = 10\text{V}, V_{DD} = 30\text{V}$	-	38	-	ns
$t_{d(off)}$	Turn-Off DelayTime	$I_D = 30\text{A}, R_{\text{GEN}} = 3\Omega$	-	78	-	ns
$t_f$	Turn-Off Fall Time		-	95	-	ns
<b>Drain-Source Diode Characteristics and Max Ratings</b>						
$I_S$	Maximum Continuous Drain to Source Diode Forward Current		-	-	160	A
$I_{\text{SM}}$	Maximum Pulsed Drain to Source Diode Forward Current		-	-	640	A
$V_{SD}$	Drain to Source Diode Forward Voltage	$V_{GS} = 0\text{V}, I_S = 30\text{A}$	-	-	1.2	V
$trr$	Body Diode Reverse Recovery Time		-	54.7	-	ns
$Qrr$	Body Diode Reverse Recovery Charge	$I_F = 30\text{A}, di/dt = 100\text{A}/\mu\text{s}$	-	60	-	nC

Notes:

1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature.

2. E<sub>AS</sub> condition: Starting  $T_J=25^\circ\text{C}$ ,  $V_{DD}=30\text{V}$ ,  $V_G=10\text{V}$ ,  $R_G=25\text{ohm}$ ,  $L=0.5\text{mH}$ ,  $I_{AS}=40\text{A}$

3. Pulse Test: Pulse Width  $\leq 300\mu\text{s}$ , Duty Cycle  $\leq 0.5\%$ .

## Typical Performance Characteristics

Figure 1: Output Characteristics

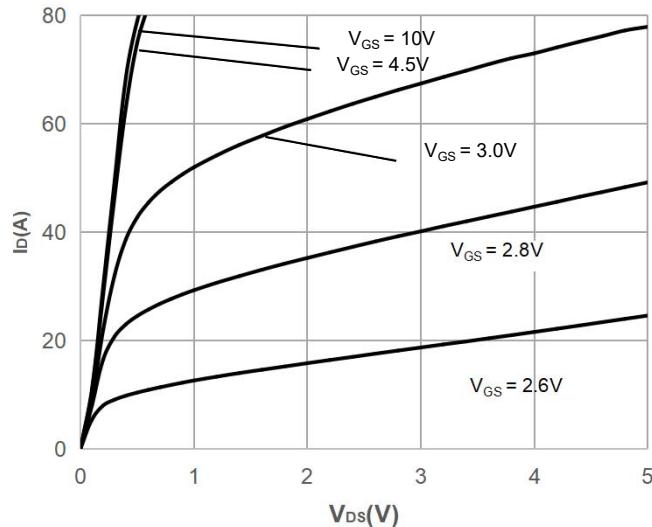


Figure 2: Typical Transfer Characteristics

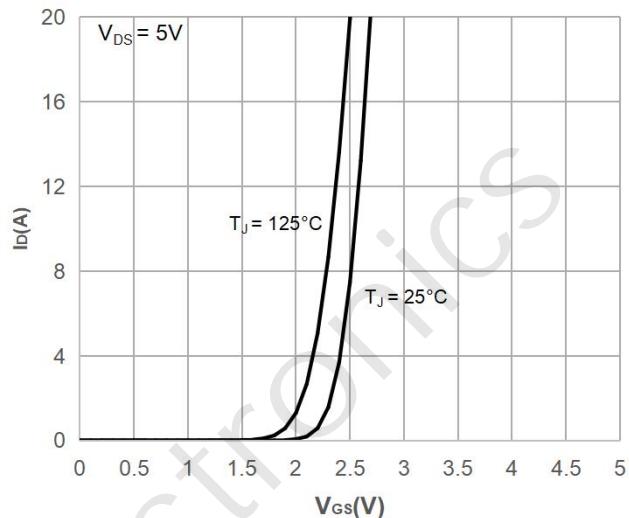


Figure 3: On-resistance vs. Drain Current

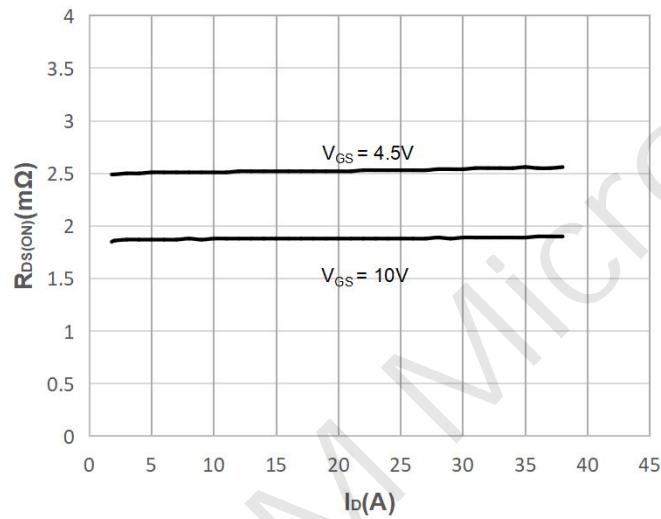


Figure 4: Body Diode Characteristics

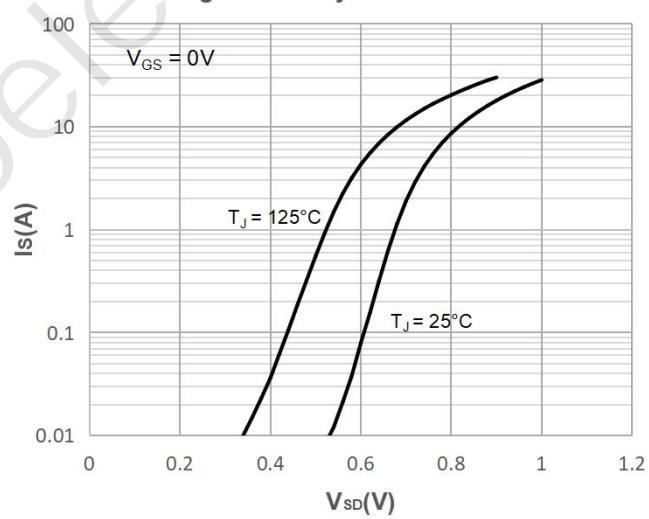


Figure 5: Gate Charge Characteristics

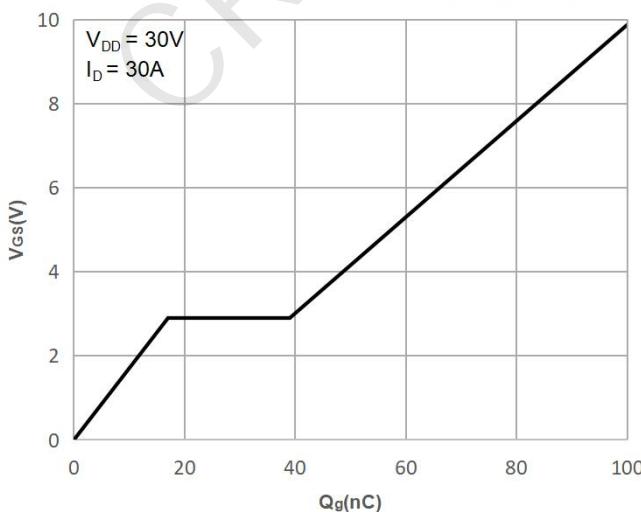
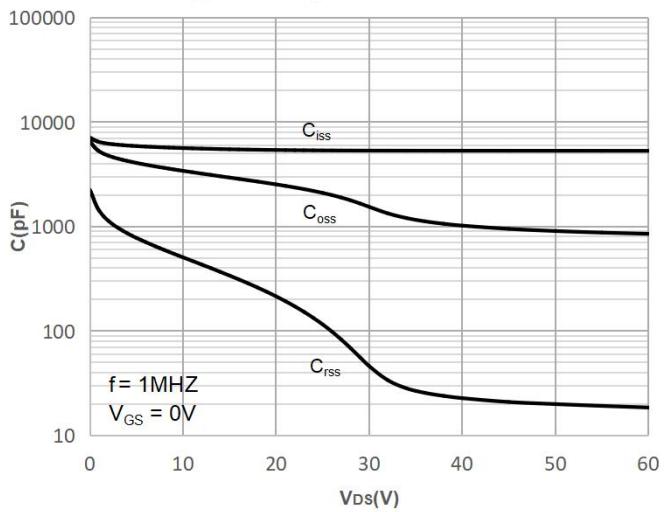
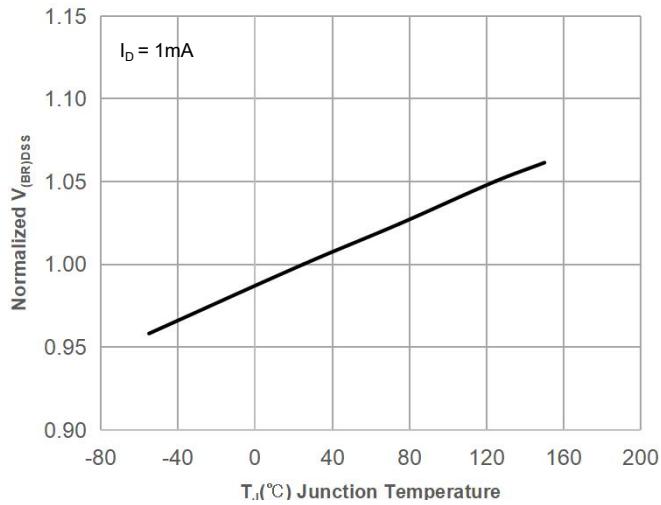


Figure 6: Capacitance Characteristics

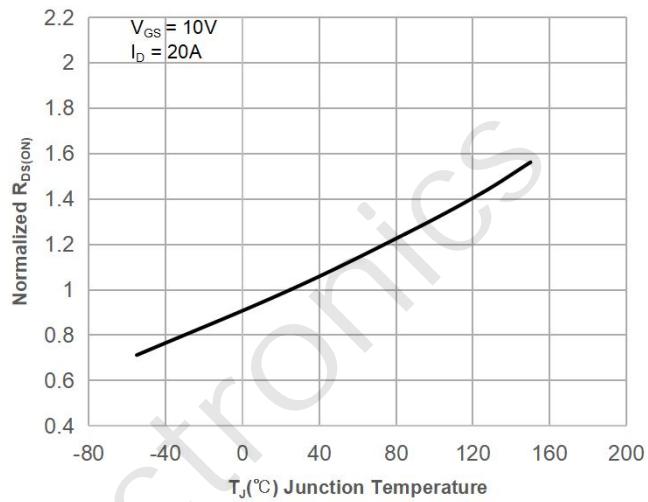


## Typical Performance Characteristics

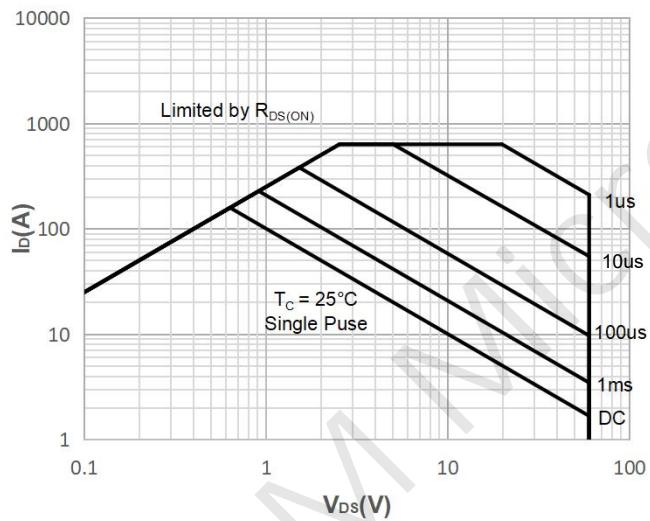
**Figure 7: Normalized Breakdown voltage vs. Junction Temperature**



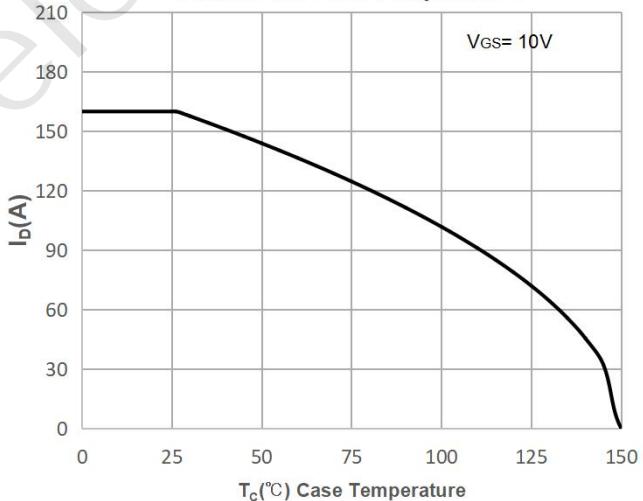
**Figure 8: Normalized on Resistance vs. Junction Temperature**



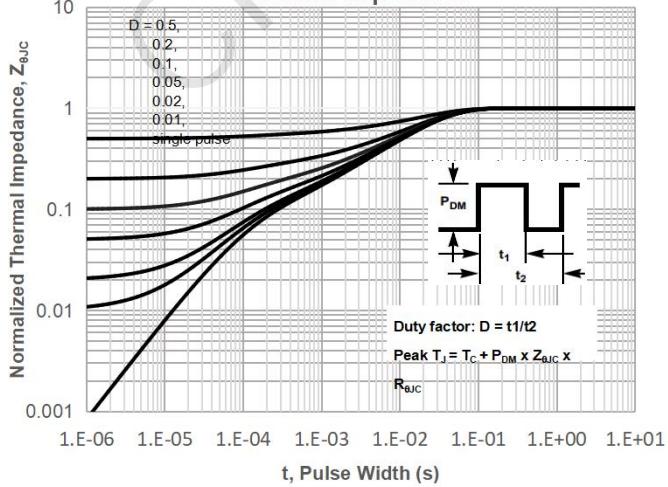
**Figure 9: Maximum Safe Operating Area**



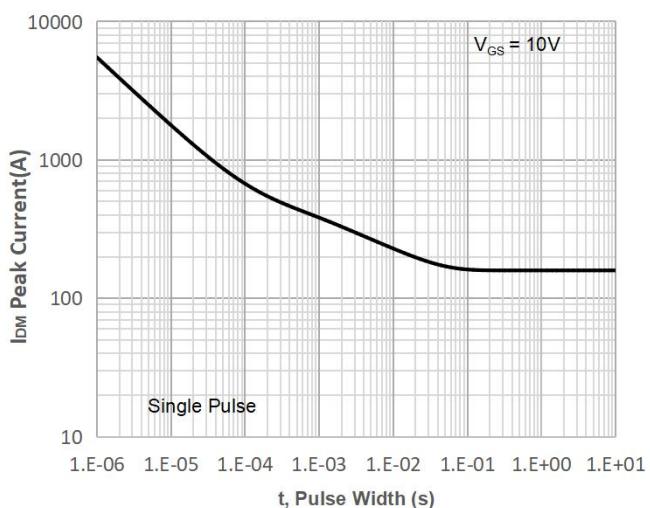
**Figure 10: Maximum Continuous Drain Current vs. Case Temperature**



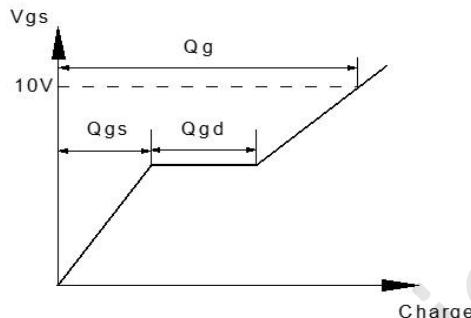
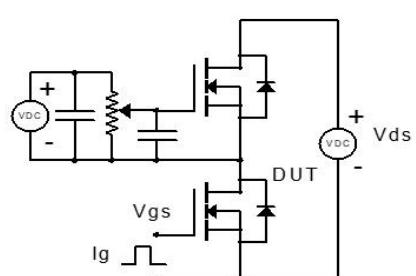
**Figure 11: Normalized Maximum Transient Thermal Impedance**



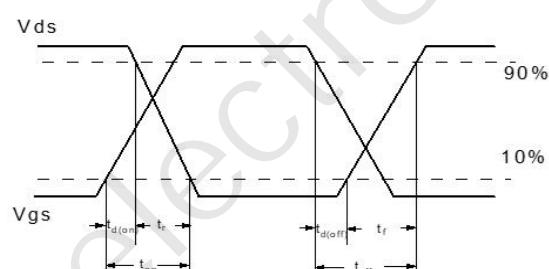
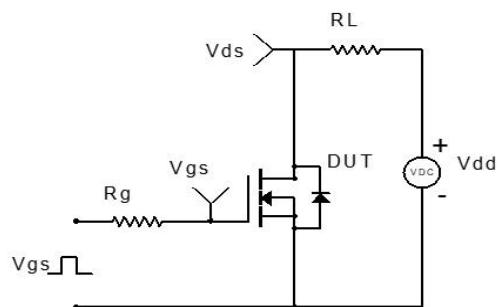
**Figure 12: Peak Current Capacity**



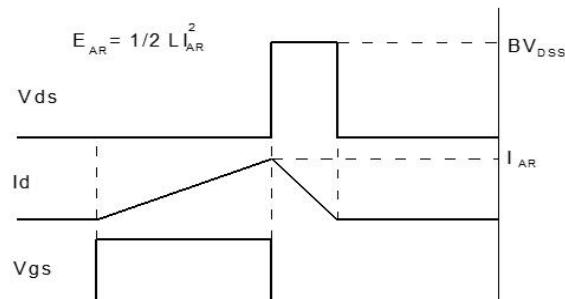
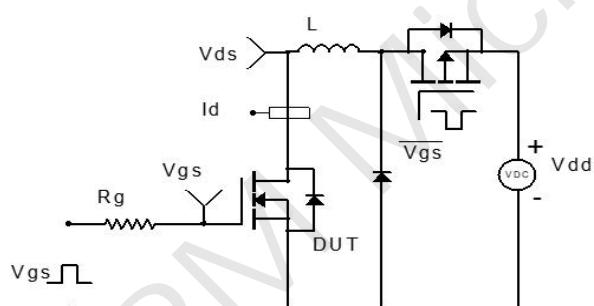
## Test Circuit



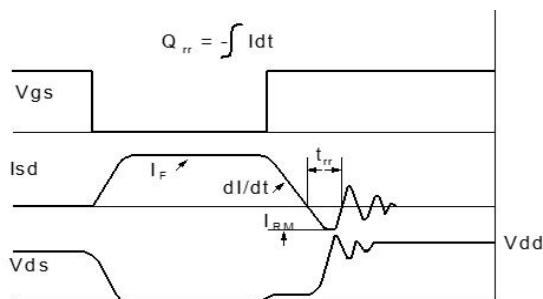
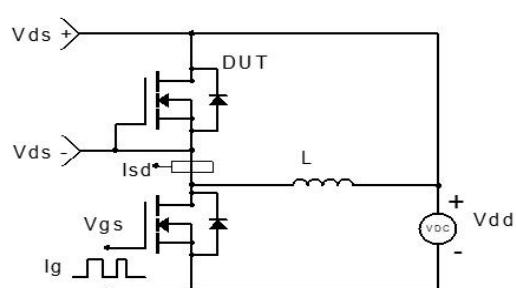
**Figure 1: Gate Charge Test Circuit & Waveform**



**Figure 2: Resistive Switching Test Circuit & Waveform**

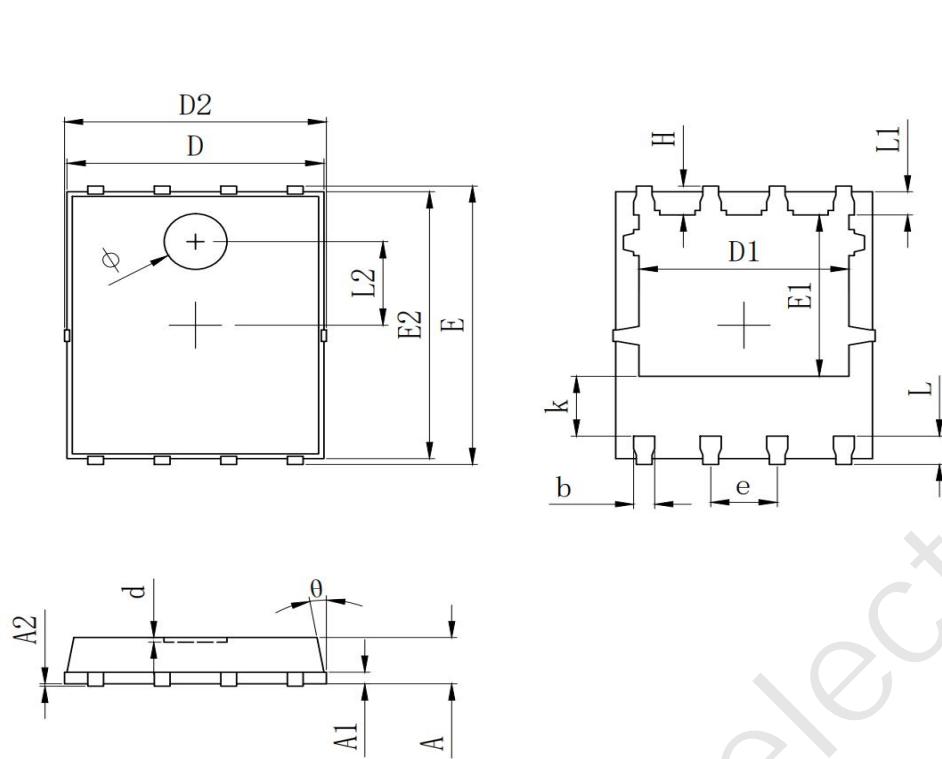


**Figure 3: Unclamped Inductive Switching Test Circuit & Waveform**



**Figure 4: Diode Recovery Test Circuit & Waveform**

## Package Mechanical Data(PDFN5x6-8L)



SYMBOL	MILLIMETER		
	MIN	Typ.	MAX
A	0.900	1.000	1.100
A1	0.254	REF.	
A2	0~0.05		
D	4.824	4.900	4.976
D1	3.910	4.010	4.110
D2	4.924	5.000	5.076
E	5.924	6.000	6.076
E1	3.375	3.475	3.575
E2	5.674	5.750	5.826
b	0.350	0.400	0.450
e	1.270	Typ.	
L	0.534	0.610	0.686
L1	0.424	0.500	0.576
L2	1.800	REF.	
k	1.190	1.290	1.390
H	0.549	0.625	0.701
θ	8°	10°	12°
Φ	1.100	1.200	1.300
d			0.100

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