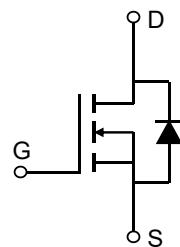


Description

Features

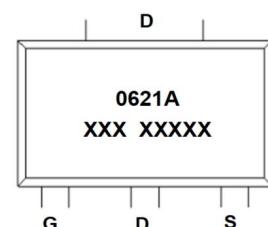
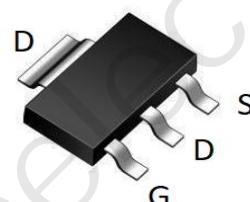
- 60V, 6.5A
- $R_{DS(ON)}$ Typ = 23mΩ @ V_{GS} = 10V
- $R_{DS(ON)}$ Typ = 26mΩ @ V_{GS} = 4.5V
- Advanced Trench Technology
- Excellent $R_{DS(ON)}$ and Low Gate Charge
- Lead Free



Schematic Diagram

Application

- Load Switch
- PWM Application
- Power Management



Marking and Pin Assignment

Package Marking and Ordering Information

| Device | Marking | Package | Outline | Reel Size | Reel (pcs) | Per Carton (pcs) |
|-------------|---------|------------|---------|-----------|------------|------------------|
| CRMYTL0621A | 0621A | SOT-223-3L | TAPING | 13" | 4000 | 48000 |

Absolute Maximum Ratings (@ T_J = 25°C unless otherwise specified)

| Symbol | Parameter | Value | Units |
|-----------------|--|---------------------------|-------|
| V_{DS} | Drain-to-Source Voltage | 60 | V |
| V_{GS} | Gate-to-Source Voltage | ± 20 | V |
| I_D | Continuous Drain Current $T_A = 25^\circ\text{C}$ | 6.5 | A |
| | | $T_A = 100^\circ\text{C}$ | A |
| I_{DM} | Pulsed Drain Current ⁽¹⁾ | 26 | A |
| P_D | Power Dissipation $T_A = 25^\circ\text{C}$ | 2.2 | W |
| $R_{\theta JA}$ | Thermal Resistance, Junction to Ambient ⁽²⁾ | 57 | °C/W |
| T_J, T_{STG} | Junction & Storage Temperature Range | -55 to 150 | °C |

Electrical Characteristics ($T_J = 25^\circ\text{C}$ unless otherwise specified)

| Symbol | Parameter | Conditions | Min. | Typ. | Max. | Unit |
|---|--|--|------|------|-----------|------------------|
| Off Characteristics | | | | | | |
| $V_{(\text{BR})\text{DSS}}$ | Drain-Source Breakdown Voltage | $I_D = 250\mu\text{A}, V_{GS} = 0\text{V}$ | 60 | - | - | V |
| I_{DSS} | Zero Gate Voltage Drain Current | $V_{DS} = 60\text{V}, V_{GS} = 0\text{V}$ | - | - | 1.0 | μA |
| I_{GSS} | Gate-Body Leakage Current | $V_{DS} = 0\text{V}, V_{GS} = \pm 20\text{V}$ | - | - | ± 100 | nA |
| On Characteristics | | | | | | |
| $V_{GS(\text{th})}$ | Gate Threshold Voltage | $V_{DS} = V_{GS}, I_D = 250\mu\text{A}$ | 1.2 | 1.6 | 2.3 | V |
| $R_{\text{DS(ON)}}$ | Static Drain-Source ON-Resistance ⁽³⁾ | $V_{GS} = 10\text{V}, I_D = 3\text{A}$ | - | 23 | 30 | $\text{m}\Omega$ |
| | | $V_{GS} = 4.5\text{V}, I_D = 2\text{A}$ | - | 26 | 34 | $\text{m}\Omega$ |
| Dynamic Characteristics | | | | | | |
| C_{iss} | Input Capacitance | | - | 1035 | - | pF |
| C_{oss} | Output Capacitance | $V_{GS} = 0\text{V}, V_{DS} = 25\text{V}, f = 1\text{MHz}$ | - | 65 | - | pF |
| C_{rss} | Reverse Transfer Capacitance | | - | 60 | - | pF |
| Q_g | Total Gate Charge | | - | 25 | - | nC |
| Q_{gs} | Gate Source Charge | $V_{GS} = 0 \text{ to } 10\text{V}$ | - | 4.5 | - | nC |
| Q_{gd} | Gate Drain("Miller") Charge | $V_{DS} = 30\text{V}, I_D = 5\text{A}$ | - | 6.5 | - | nC |
| Switching Characteristics | | | | | | |
| $t_{d(\text{on})}$ | Turn-On DelayTime | | - | 7 | - | ns |
| t_r | Turn-On Rise Time | $V_{GS} = 10\text{V}, V_{DD} = 30\text{V}$ | - | 20 | - | ns |
| $t_{d(\text{off})}$ | Turn-Off DelayTime | $I_D = 20\text{A}, R_{\text{GEN}} = 3\Omega$ | - | 16 | - | ns |
| t_f | Turn-Off Fall Time | | - | 23 | - | ns |
| Drain-Source Diode Characteristics and Max Ratings | | | | | | |
| I_S | Maximum Continuous Drain to Source Diode Forward Current | | - | - | 6.5 | A |
| I_{SM} | Maximum Pulsed Drain to Source Diode Forward Current | | - | - | 26 | A |
| V_{SD} | Drain to Source Diode Forward Voltage | $V_{GS} = 0\text{V}, I_S = 5\text{A}$ | - | - | 1.2 | V |
| trr | Body Diode Reverse Recovery Time | | - | 29 | - | ns |
| Qrr | Body Diode Reverse Recovery Charge | $I_F = 5\text{A}, di/dt = 100\text{A}/\mu\text{s}$ | - | 49 | - | nC |

Notes:

1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature.

2. $R_{\theta JA}$ is measured with the device mounted on a 1inch² pad of 2oz copper FR4 PCB

3. Pulse Test: Pulse Width $\leq 300\mu\text{s}$, Duty Cycle $\leq 0.5\%$.

Test Circuit

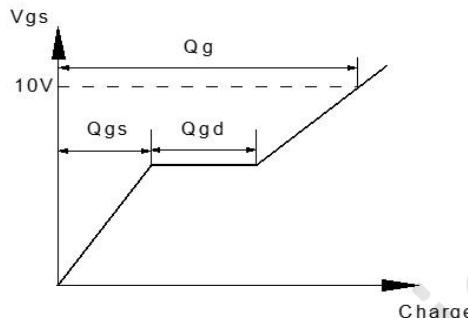
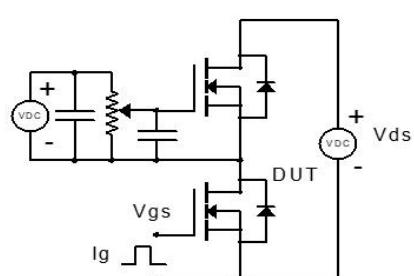


Figure 1: Gate Charge Test Circuit & Waveform

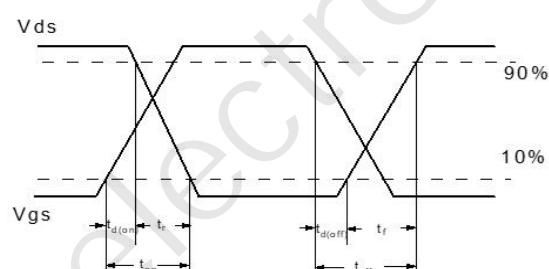
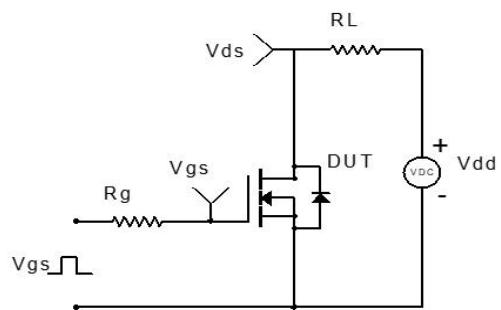


Figure 2: Resistive Switching Test Circuit & Waveform

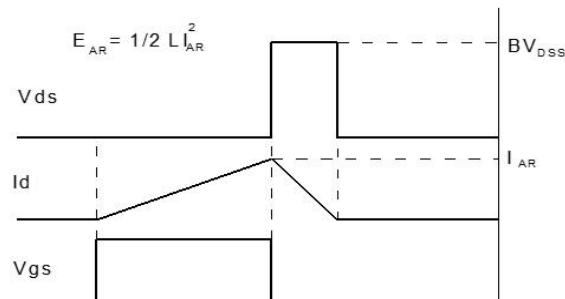
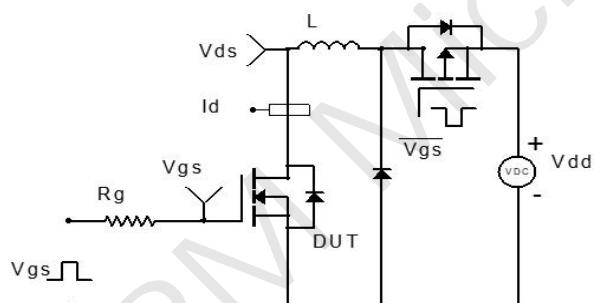


Figure 3: Unclamped Inductive Switching Test Circuit & Waveform

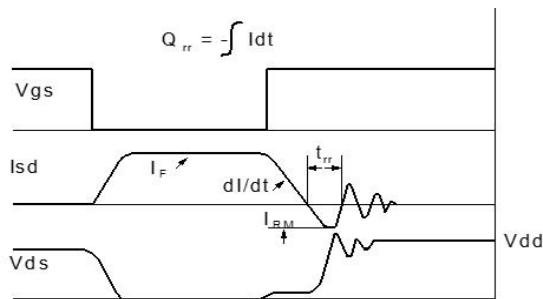
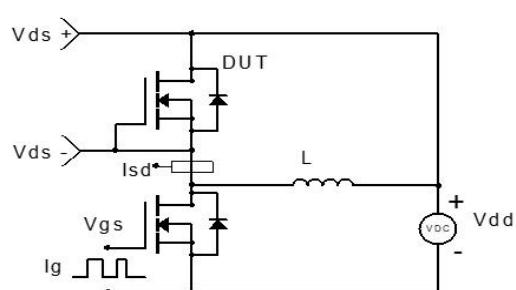
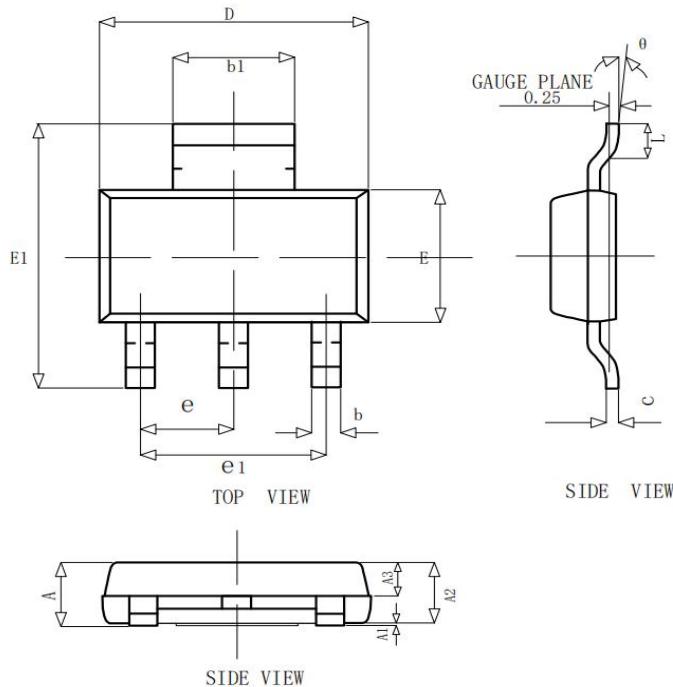


Figure 4: Diode Recovery Test Circuit & Waveform

Package Mechanical Data(SOT-223-3L)



COMMON DIMENSIONS
(UNITS OF MEASURE=mm)

| SYMBOL | MIN | NOM | MAX |
|--------|------|--------|------|
| A | — | — | 1.80 |
| A1 | 0.00 | 0.05 | 0.10 |
| A2 | 1.50 | 1.60 | 1.70 |
| A3 | 0.85 | 0.90 | 0.95 |
| b | 0.66 | 0.70 | 0.80 |
| b1 | 2.96 | 3.00 | 3.10 |
| c | 0.25 | 0.30 | 0.35 |
| D | 6.30 | 6.50 | 6.70 |
| E | 3.30 | 3.50 | 3.70 |
| E1 | 6.80 | 7.00 | 7.20 |
| e | | 2.3BSC | |
| e1 | 4.40 | 4.60 | 4.80 |
| L | 0.90 | — | 1.15 |
| θ | 0° | 5° | 10° |

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