

# CRMKTH0202A N-Channel 22V, 2.1mΩ Typ. Power MOSFET

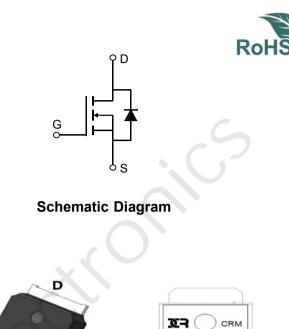
## Description

## **Features**

• 22V, 115A

 $R_{DS(ON)}$  Typ = 2.1m $\Omega$  @ V<sub>GS</sub> = 10V

- Advanced Trench Technology
- Excellent R<sub>DS(ON)</sub> and Low Gate Charge
- 100% UIS TESTED!
- 100% ΔVds TESTED!



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# Application

- Load Switch
- PWM Application
- Power Management

### Marking and Pin Assignment

### Package Marking and Ordering Information

Device	Marking	Package	Outline	Reel Size	Reel (pcs)	Per Carton (pcs)
CRMKTH0202A	CRMKTH0202A	TO-252-3L	TAPING	13"	2500	25000

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### Absolute Maximum Ratings (@ T<sub>J</sub> = 25°C unless otherwise specified)

Symbol	Parameter		Value	Units
V <sub>DS</sub>	Drain-to-Source Voltage		22	V
V <sub>GS</sub>	Gate-to-Source Voltage		±20	V
	Continuous Drain Current	$T_{C} = 25^{\circ}C$	115	А
Ι <sub>D</sub>		T <sub>C</sub> = 100°C	69	А
I <sub>DM</sub>	Pulsed Drain Current <sup>(1)</sup>		460	А
E <sub>AS</sub>	Single Pulsed Avalanche Energy <sup>(2)</sup>		196	mJ
P <sub>D</sub>	Power Dissipation	T <sub>C</sub> = 25°C	62.5	W
$R_{ extsf{ heta}JC}$	Thermal Resistance, Junction to Case		2	°C/W
$T_{J}, T_{STG}$	Junction & Storage Temperature Range		-55 to 150	°C



### **Electrical Characteristics** (T<sub>J</sub> = 25°C unless otherwise specified)

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
Off Chara	acteristics					
V <sub>(BR)DSS</sub>	Drain-Source Breakdown Voltage	$I_{D} = 250 \mu A, V_{GS} = 0 V$	22	-	-	V
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	V <sub>DS</sub> = 22V, V <sub>GS</sub> = 0V	-	-	1.0	μA
I <sub>GSS</sub>	Gate-Body Leakage Current	$V_{DS} = 0V, V_{GS} = \pm 20V$	-	-	±100	nA
On Chara	acteristics				6	
V <sub>GS(th)</sub>	Gate Threshold Voltage	$V_{DS}$ = $V_{GS}$ , $I_D$ = 250 $\mu$ A	1.7	2.2	2.7	V
R <sub>DS(ON)</sub>	Static Drain-Source ON-Resistance <sup>(3)</sup>	V <sub>GS</sub> = 10V, I <sub>D</sub> = 20A	-	2.1	2.73	mΩ
Dynamic	Characteristics					
C <sub>iss</sub>	Input Capacitance		-	3578	-	pF
$C_{oss}$	Output Capacitance	V <sub>GS</sub> = 0V, V <sub>DS</sub> = 10V, f = 1MHz	-	717	-	pF
C <sub>rss</sub>	Reverse Transfer Capacitance	1 - 110112	Χ-	487	-	pF
Q <sub>g</sub>	Total Gate Charge	(	-	43	-	nC
$Q_{gs}$	Gate Source Charge	$V_{GS} = 0$ to 10V $V_{DS} = 10V$ , $I_{D} = 30A$	9.	8	-	nC
$Q_{gd}$	Gate Drain("Miller") Charge	v <sub>DS</sub> = 10v, i <sub>D</sub> = 30A	-	13	-	nC
Switchin	g Characteristics					
t <sub>d(on)</sub>	Turn-On DelayTime		-	14	-	ns
t <sub>r</sub>	Turn-On Rise Time	V <sub>GS</sub> = 10V, V <sub>DD</sub> = 10V	-	32	-	ns
$t_{d(off)}$	Turn-Off DelayTime	$I_D$ = 30A, $R_{GEN}$ = 3 $\Omega$	-	78	-	ns
t <sub>f</sub>	Turn-Off Fall Time		-	80	-	ns
Drain-So	urce Diode Characteristics and M	lax Ratings				
I <sub>S</sub>	Maximum Continuous Drain to Source Diode Forward Current			-	115	А
I <sub>SM</sub>	Maximum Pulsed Drain to Source Diode Forward Current		-	-	460	А
$V_{SD}$	Drain to Source Diode Forward Voltage	V <sub>GS</sub> = 0V, I <sub>S</sub> = 20A	-	-	1.2	V
trr	Body Diode Reverse Recovery Time		-	15	-	ns
Qrr	Body Diode Reverse Recovery Charge	I <sub>F</sub> = 20A, di/dt = 100A/us	-	8	-	nC

Notes:

1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature.

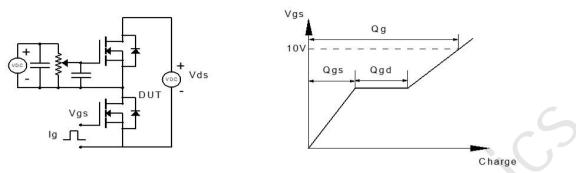
2. E\_{AS} condition: Starting T\_J=25°C, V\_{DD}=10V, V\_G=10V, R\_G=250hm, L=0.5mH, I\_{AS}=28A

3. Pulse Test: Pulse Width $\leqslant$ 300µs, Duty Cycle $\leqslant$ 0.5%.

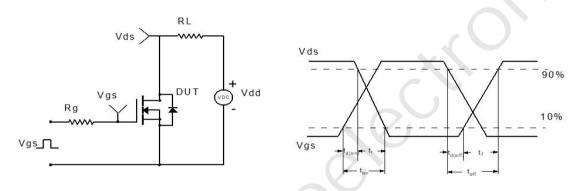


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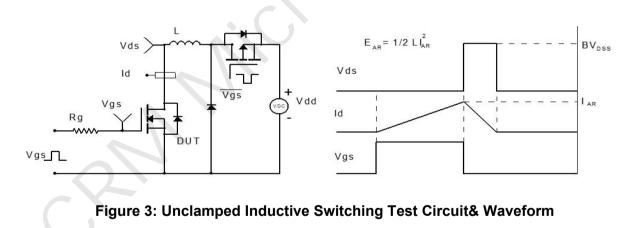
## **Test Circuit**







### Figure 2: Resistive Switching Test Circuit & Waveform



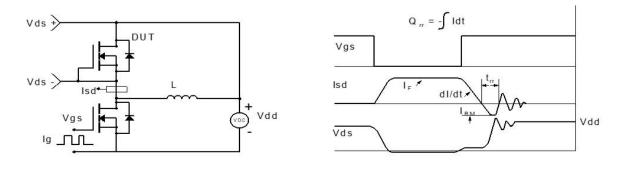
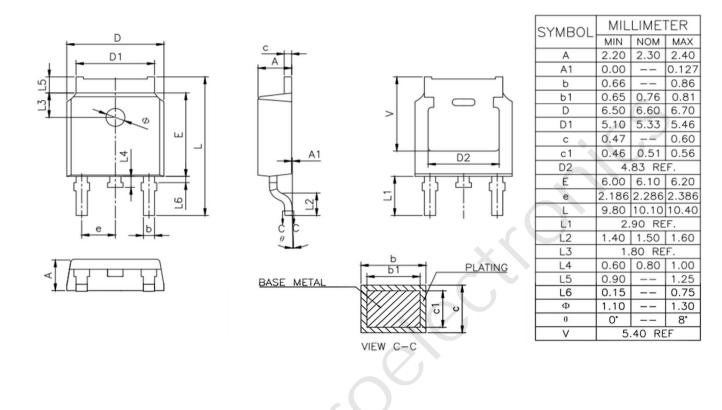


Figure 4: Diode Recovery Test Circuit & Waveform



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## Package Mechanical Data(TO-252-3L)



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## **Contact information**

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