

CRMKGL0402A

N-Channel 40V, 2mΩ Typ. Power MOSFET

Description

Features

• 40V, 120A

 $R_{DS(ON)}$ Typ = 2m Ω @ V_{GS} = 10V

 $R_{DS(ON)}$ Typ = 2.5m Ω @ V_{GS} = 4.5V

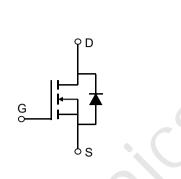
- Advanced Split Gate Trench Technology
- Excellent R_{DS(ON)} and Low Gate Charge
- 100% UIS TESTED!
- 100% ΔVds TESTED!

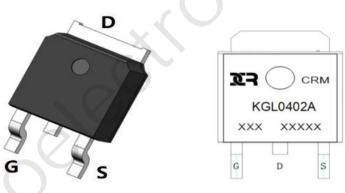
Application

PWM Application

• Power Management

· Load Switch





Schematic Diagram

Marking and Pin Assignment

Package Marking and Ordering Information

Device	Marking	Package	Outline	Reel Size	Reel (pcs)	Per Carton (pcs)
CRMKGL0402A	CRMKGL0402A	TO-252-3L	TAPING	13"	2500	25000

Absolute Maximum Ratings (@ T_J = 25°C unless otherwise specified)

Symbol	Parameter		Value	Units
V _{DS}	Drain-to-Source Voltage		40	V
V _{GS}	Gate-to-Source Voltage		±20	V
	Continuous Drain Current	T _C = 25°C	120	А
Ι _D		T _C = 100°C	72	А
I _{DM}	Pulsed Drain Current ⁽¹⁾		480	А
E _{AS}	Single Pulsed Avalanche Energy ⁽²⁾		225	mJ
P _D	Power Dissipation	T _C = 25°C	62.5	W
$R_{ ext{ ext{ ext{ ext{ ext{ ext{ ext{ ext$	Thermal Resistance, Junction to Case		2	°C/W
T _J , T _{stg}	Junction & Storage Temperature Range		-55 to 150	°C



Electrical Characteristics (T_J = 25°C unless otherwise specified)

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
Off Chara	acteristics					
V _{(BR)DSS}	Drain-Source Breakdown Voltage	I _D = 250μA, V _{GS} = 0V	40	-	-	V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = 40V, V _{GS} = 0V	-	-	1.0	μA
I _{GSS}	Gate-Body Leakage Current	$V_{DS} = 0V, V_{GS} = \pm 20V$	-	-	±100	nA
On Chara	acteristics				G	
V _{GS(th)}	Gate Threshold Voltage	V_{DS} = V_{GS} , I_D = 250 μ A	1	1.5	2	V
R _{DS(ON)}	Static Drain-Source ON-Resistance ⁽³⁾	V _{GS} = 10V, I _D = 30A	-	2	2.6	mΩ
		V _{GS} = 4.5V, I _D = 20A	-	2.5	3.25	mΩ
Dynamic	Characteristics					
C _{iss}	Input Capacitance		-	2762	-	pF
C _{oss}	Output Capacitance	V _{GS} = 0V, V _{DS} = 20V, f = 1MHz	Χ-	1070	-	pF
C _{rss}	Reverse Transfer Capacitance			70	-	pF
Q _g	Total Gate Charge		J .	42	-	nC
Q_{gs}	Gate Source Charge	$V_{GS} = 0$ to 10V $V_{DS} = 20V$, $I_{D} = 75A$	-	10	-	nC
Q_gd	Gate Drain("Miller") Charge	$v_{\rm DS} = 20$ V, $I_{\rm D} = 73$ A	-	7	-	nC
Switchin	g Characteristics					
t _{d(on)}	Turn-On DelayTime		-	9	-	ns
t _r	Turn-On Rise Time	V _{GS} = 10V, V _{DD} = 20V	-	103	-	ns
$t_{d(off)}$	Turn-Off DelayTime	I_{D} = 75A, R_{GEN} = 1.6 Ω	-	37	-	ns
t _f	Turn-Off Fall Time		-	129	-	ns
Drain-So	urce Diode Characteristics and M	lax Ratings				
I _S	Maximum Continuous Drain to Source Diode Forward Current			-	120	А
I _{SM}	Maximum Pulsed Drain to Source Diode Forward Current		-	-	480	А
V_{SD}	Drain to Source Diode Forward Voltage	V _{GS} = 0V, I _S = 30A	-	-	1.2	V
trr	Body Diode Reverse Recovery Time		-	38	-	ns
Qrr	Body Diode Reverse Recovery Charge	I _F = 20A, di/dt = 100A/us	-	19	-	nC

Notes:

1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature.

2. E_{AS} condition: Starting T_J=25°C, V_{DD}=20V, V_G=10V, R_G=250hm, L=0.5mH, I_{AS}=30A

3. Pulse Test: Pulse Width ${\leqslant}300\mu s,$ Duty Cycle ${\leqslant}0.5\%.$



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Test Circuit

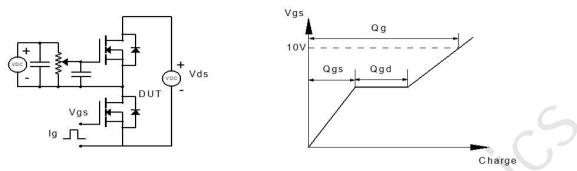


Figure 1: Gate Charge Test Circuit & Waveform

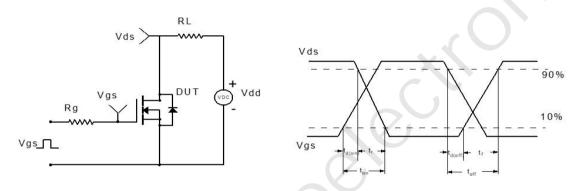
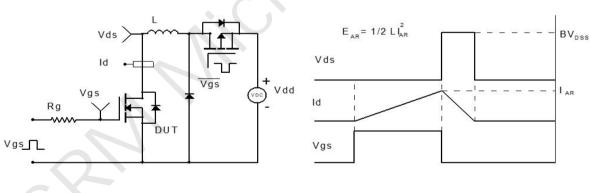


Figure 2: Resistive Switching Test Circuit & Waveform





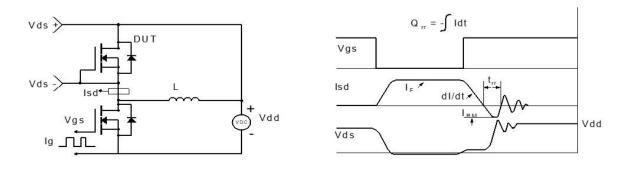
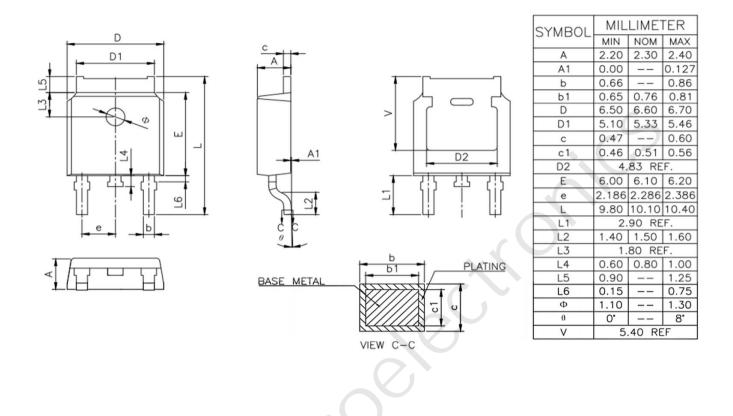


Figure 4: Diode Recovery Test Circuit & Waveform



Package Mechanical Data(TO-252-3L)



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