

Description

Features

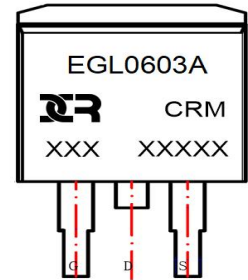
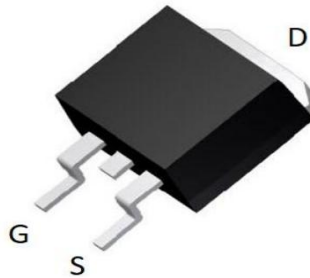
- 60V, 170A
- $R_{DS(ON)}$ Typ = 2.5mΩ @ $V_{GS} = 10V$
- $R_{DS(ON)}$ Typ = 3.2mΩ @ $V_{GS} = 4.5V$
- Advanced Split Gate Trench Technology
- Excellent $R_{DS(ON)}$ and Low Gate Charge
- 100% UIS TESTED!
- 100% ΔV_{ds} TESTED!



Schematic Diagram

Application

- Load Switch
- PWM Application
- Power Management



Marking and Pin Assignment

Package Marking and Ordering Information

Device	Marking	Package	Outline	Reel Size	Reel (pcs)	Per Carton (pcs)
CRMEGL0603A	CRMEGL0603A	TO-263-3L	TAPING	13"	800	4000

Absolute Maximum Ratings (@ $T_J = 25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter	Value	Units	
V _{DS}	Drain-to-Source Voltage	60	V	
V _{GS}	Gate-to-Source Voltage	±20	V	
I _D	Continuous Drain Current	T _C = 25°C	170	A
		T _C = 100°C	102	A
I _{DM}	Pulsed Drain Current ⁽¹⁾	680	A	
E _{AS}	Single Pulsed Avalanche Energy ⁽²⁾	361	mJ	
P _D	Power Dissipation	T _C = 25°C	156	W
R _{θJC}	Thermal Resistance, Junction to Case	0.8	°C/W	
T _J , T _{STG}	Junction & Storage Temperature Range	-55 to 150	°C	

Electrical Characteristics ($T_J = 25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
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Off Characteristics

$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	$I_D = 250\mu\text{A}, V_{GS} = 0\text{V}$	60	-	-	V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS} = 60\text{V}, V_{GS} = 0\text{V}$	-	-	1.0	μA
I_{GSS}	Gate-Body Leakage Current	$V_{DS} = 0\text{V}, V_{GS} = \pm 20\text{V}$	-	-	± 100	nA

On Characteristics

$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_D = 250\mu\text{A}$	1	1.7	2.5	V
$R_{DS(ON)}$	Static Drain-Source ON-Resistance ⁽³⁾	$V_{GS} = 10\text{V}, I_D = 30\text{A}$	-	2.5	3.3	mΩ
		$V_{GS} = 4.5\text{V}, I_D = 20\text{A}$	-	3.2	4.2	mΩ

Dynamic Characteristics

C_{iss}	Input Capacitance	$V_{GS} = 0\text{V}, V_{DS} = 25\text{V},$ $f = 1\text{MHz}$	-	5300	-	pF
C_{oss}	Output Capacitance		-	2150	-	pF
C_{rss}	Reverse Transfer Capacitance		-	125	-	pF
Q_g	Total Gate Charge	$V_{GS} = 0 \text{ to } 10\text{V}$ $V_{DS} = 30\text{V}, I_D = 30\text{A}$	-	101	-	nC
Q_{gs}	Gate Source Charge		-	17	-	nC
Q_{gd}	Gate Drain("Miller") Charge		-	22	-	nC

Switching Characteristics

$t_{d(on)}$	Turn-On DelayTime	$V_{GS} = 10\text{V}, V_{DD} = 30\text{V}$ $I_D = 30\text{A}, R_{GEN} = 3\Omega$	-	16	-	ns
t_r	Turn-On Rise Time		-	38	-	ns
$t_{d(off)}$	Turn-Off DelayTime		-	78	-	ns
t_f	Turn-Off Fall Time		-	95	-	ns

Drain-Source Diode Characteristics and Max Ratings

I_S	Maximum Continuous Drain to Source Diode Forward Current	$V_{GS} = 0\text{V}, I_S = 30\text{A}$	-	-	170	A
I_{SM}	Maximum Pulsed Drain to Source Diode Forward Current		-	-	680	A
V_{SD}	Drain to Source Diode Forward Voltage		-	-	1.2	V
t_{rr}	Body Diode Reverse Recovery Time		-	54.7	-	ns
Q_{rr}	Body Diode Reverse Recovery Charge		-	60	-	nC

- Notes:
1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature.
 2. E_{AS} condition: Starting $T_J = 25^\circ\text{C}$, $V_{DD} = 30\text{V}$, $V_G = 10\text{V}$, $R_G = 25\Omega$, $L = 0.5\text{mH}$, $I_{AS} = 38\text{A}$
 3. Pulse Test: Pulse Width $\leq 300\mu\text{s}$, Duty Cycle $\leq 0.5\%$.

Typical Performance Characteristics

Figure 1: Output Characteristics

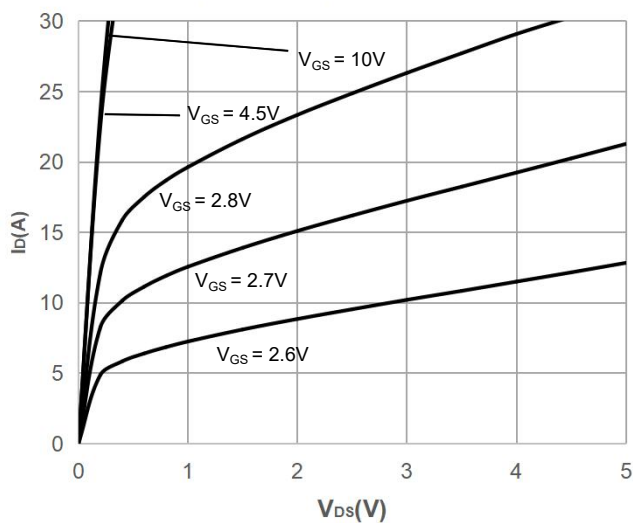


Figure 2: Typical Transfer Characteristics

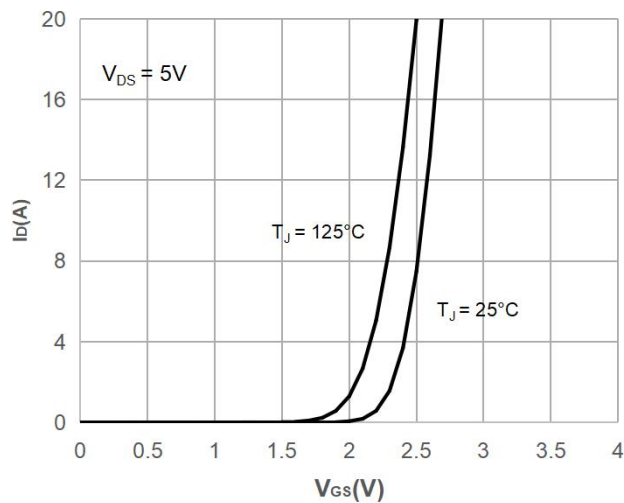


Figure 3: On-resistance vs. Drain Current

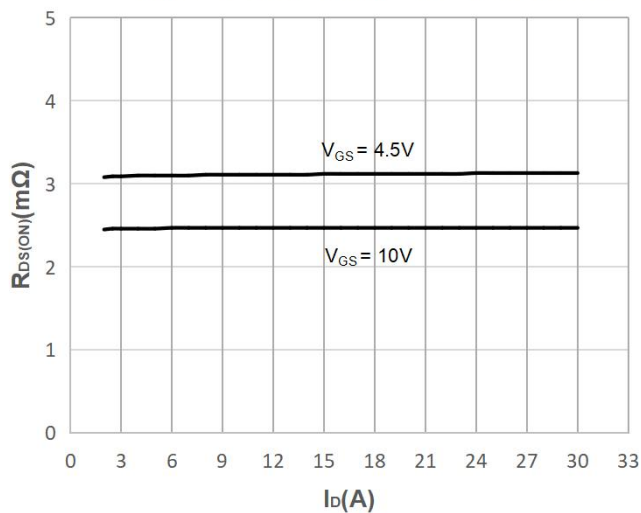


Figure 4: Body Diode Characteristics

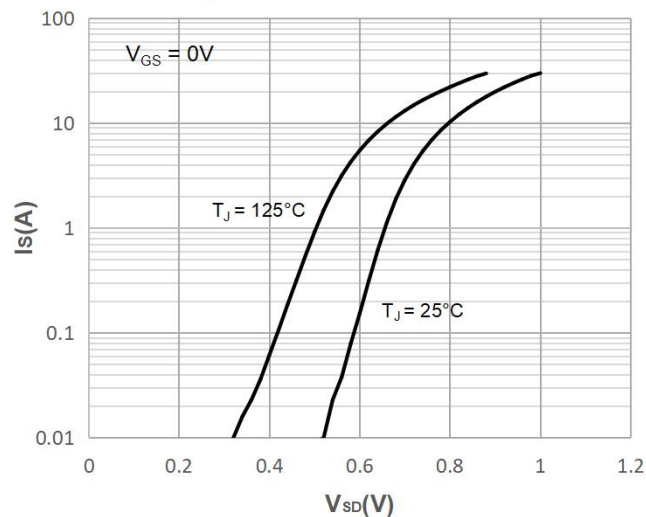


Figure 5: Gate Charge Characteristics

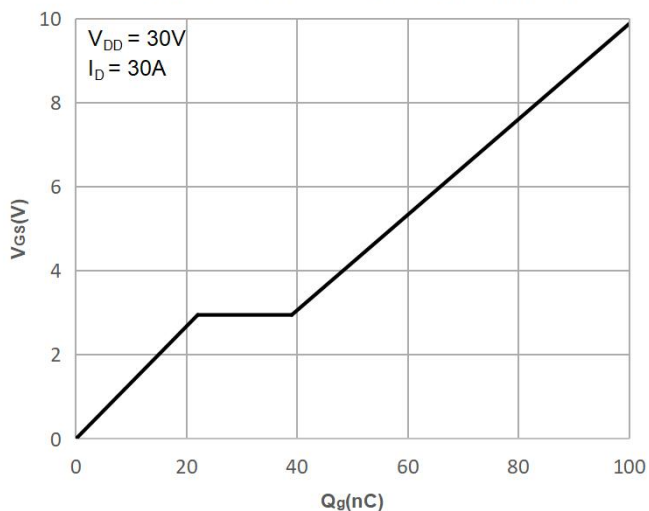
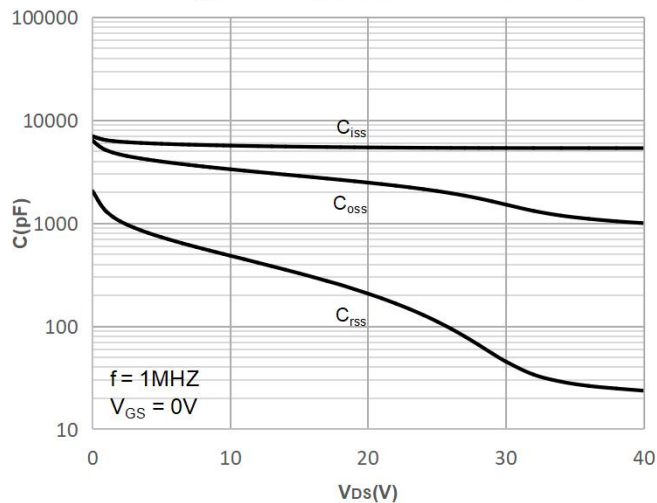


Figure 6: Capacitance Characteristics



Typical Performance Characteristics

Figure 7: Normalized Breakdown voltage vs. Junction Temperature

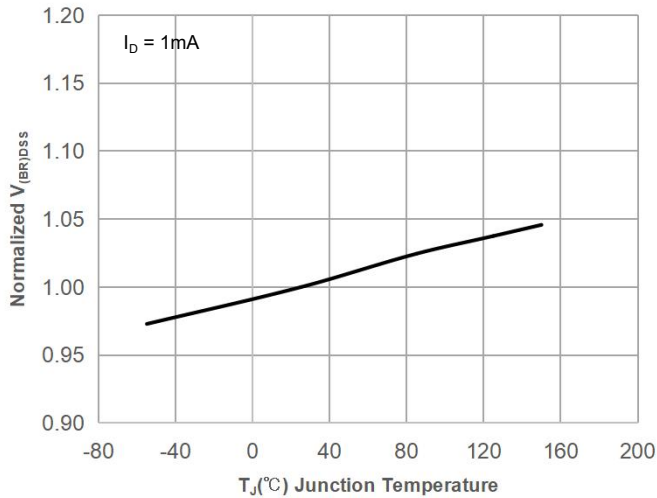


Figure 8: Normalized on Resistance vs. Junction Temperature

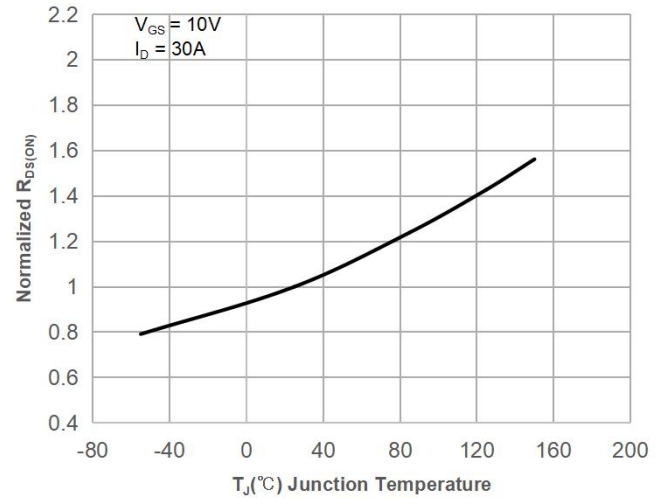


Figure 9: Maximum Safe Operating Area

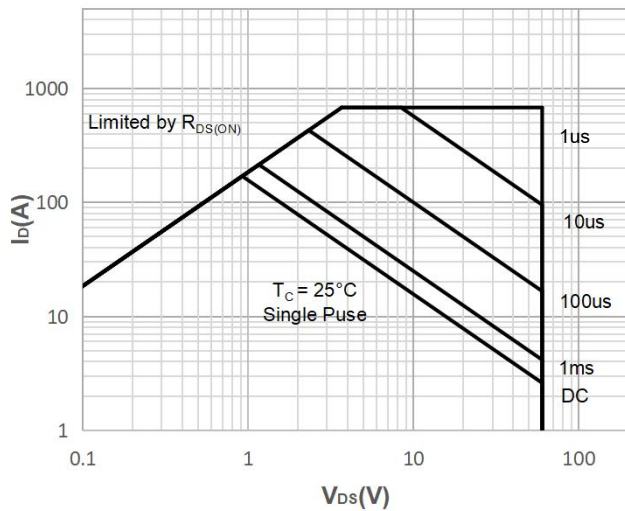


Figure 10: Maximum Continuous Drain Current vs. Case Temperature

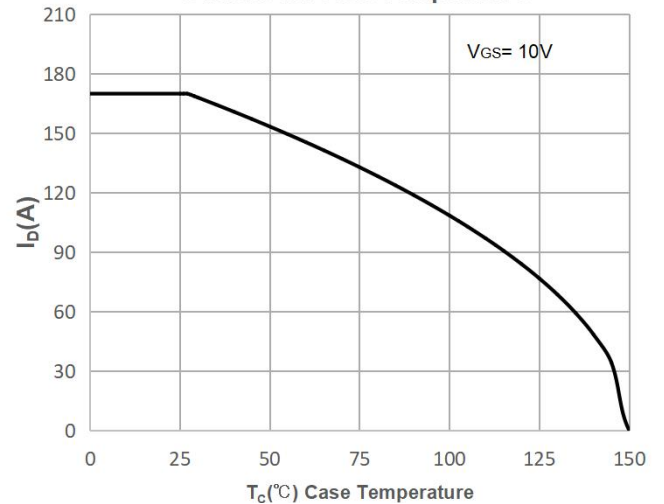


Figure 11: Normalized Maximum Transient Thermal Impedance

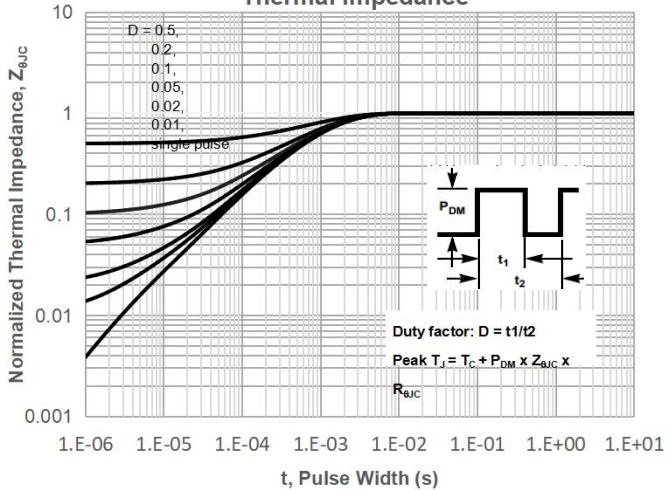
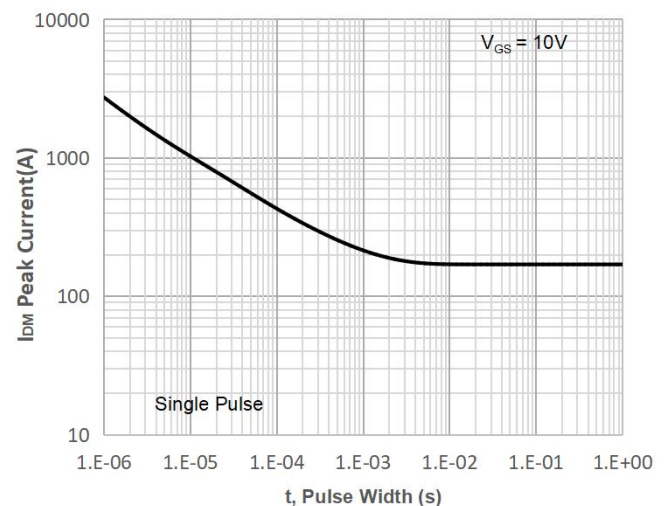


Figure 12: Peak Current Capacity



Test Circuit

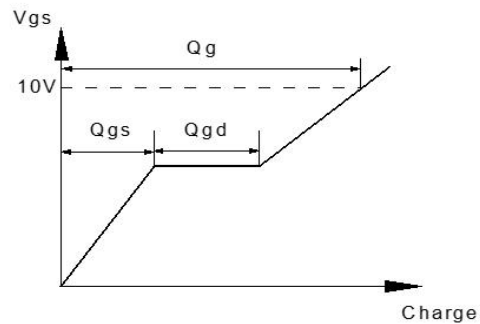
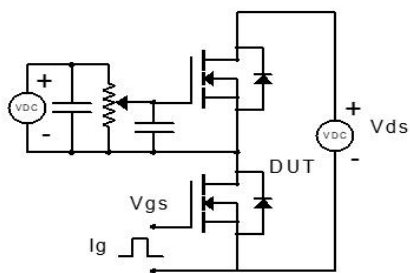


Figure 1: Gate Charge Test Circuit & Waveform

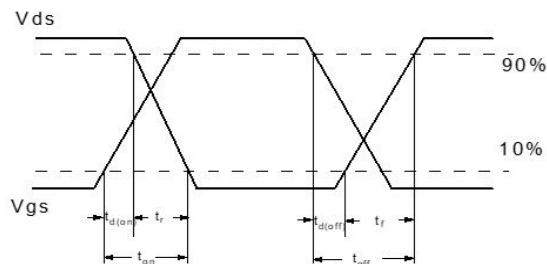
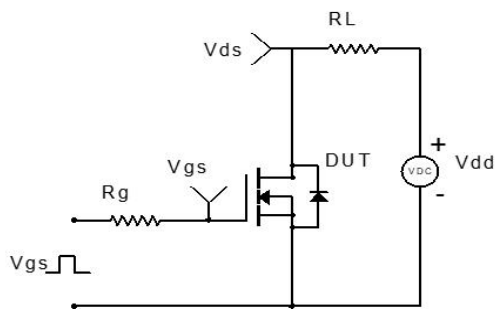


Figure 2: Resistive Switching Test Circuit & Waveform

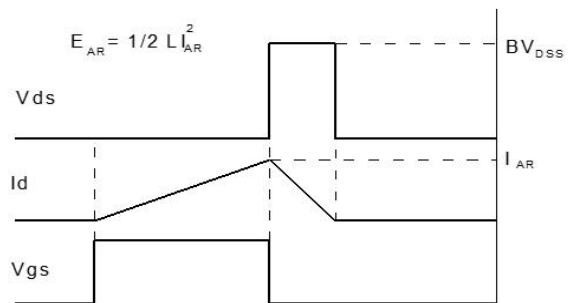
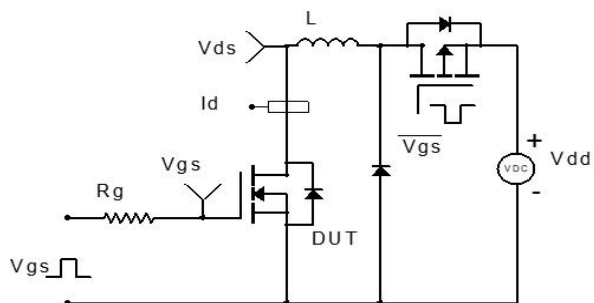


Figure 3: Unclamped Inductive Switching Test Circuit & Waveform

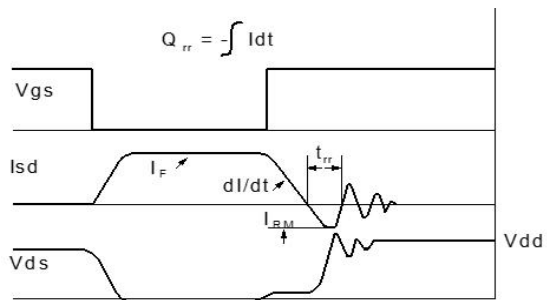
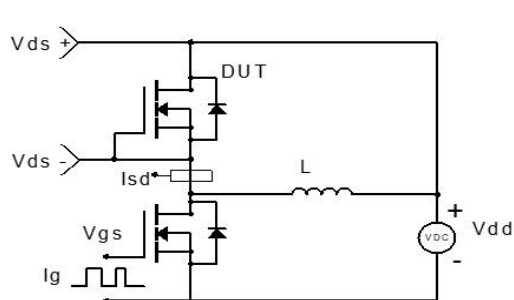
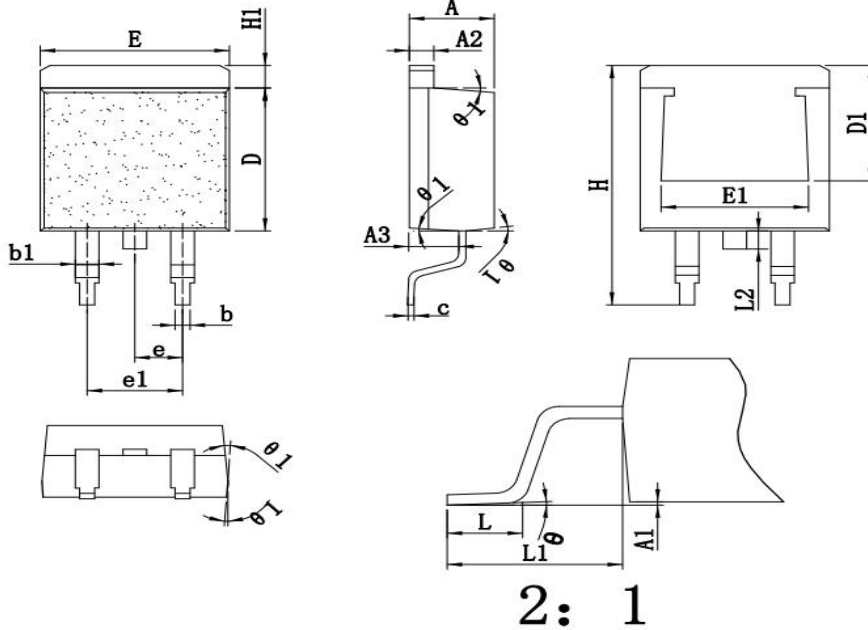


Figure 4: Diode Recovery Test Circuit & Waveform

Package Mechanical Data(TO-263-3L)



SYMBOL	mm		
	MIN	NOM	MAX
*A	4.42	4.52	4.62
*A1	0.00	0.10	0.20
*A2	1.24	1.27	1.32
*A3	2.50	2.60	2.70
*b	0.77	0.81	0.84
*b1	1.23	1.28	1.41
*c	0.33	0.38	0.43
*D	8.80	8.95	9.10
D1	7.25REF		
*E	9.92	10.07	10.22
E1	7.85REF		
*e	2.50	2.54	2.58
e1	5.08REF		
*H	14.80	15.10	15.30
H1	1.12	1.28	1.42
*L	2.10	2.23	2.36
L1	4.55	4.75	4.95
L2	1.10	1.30	1.50
*θ	0°	2°	5°
θ1	3°	-	9°

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