CRMEGH2024A

N-Channel 200V, $22m\Omega$ Typ. Power MOSFET

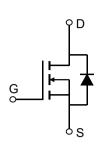
Description

Features

• 200V, 60A

 $R_{DS(ON)}$ Typ = 22m Ω @ V_{GS} = 10V

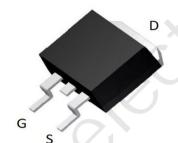
- Advanced Split Gate Trench Technology
- Excellent R_{DS(ON)} and Low Gate Charge
- 100% UIS TESTED!
- 100% ΔVds TESTED!

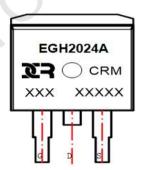




Application

- Load Switch
- PWM Application
- Power Management





Marking and Pin Assignment

Package Marking and Ordering Information

Device	Marking	Package	Outline	Reel Size	Reel (pcs)	Per Carton (pcs)
CRMEGH2024A	CRMEGH2024A	TO-263-3L	TAPING	13"	800	4000

Absolute Maximum Ratings (@ T_J = 25°C unless otherwise specified)

Symbol	Parameter		Value	Units
V_{DS}	Drain-to-Source Voltage		200	V
V_{GS}	Gate-to-Source Voltage		±20	V
	Continuous Drain Current	T _C = 25°C	60	Α
I _D		T _C = 100°C	36	Α
I _{DM}	Pulsed Drain Current (1)		240	Α
E _{AS}	Single Pulsed Avalanche Energy (2)		605	mJ
P_{D}	Power Dissipation	T _C = 25°C	176	W
$R_{ hetaJC}$	Thermal Resistance, Junction to Case		0.71	°C/W
T_J,T_STG	Junction & Storage Temperature Range		-55 to 150	°C

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Electrical Characteristics (T_J = 25°C unless otherwise specified)

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
Off Chara	acteristics					
V _{(BR)DSS}	Drain-Source Breakdown Voltage	$I_D = 250 \mu A, V_{GS} = 0 V$	200	-	-	V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = 200V, V _{GS} = 0V	-	-	1.0	μΑ
I _{GSS}	Gate-Body Leakage Current	$V_{DS} = 0V, V_{GS} = \pm 20V$	-	-	±100	nA
On Chara	acteristics				6	
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	2.5	3.1	3.5	V
R _{DS(ON)}	Static Drain-Source ON-Resistance ⁽³⁾	$V_{GS} = 10V, I_D = 30A$	-	22	28.6	mΩ
Dynamic	Characteristics					
C _{iss}	Input Capacitance		-	1293	-	pF
C_{oss}	Output Capacitance	$V_{GS} = 0V, V_{DS} = 25V,$ f = 1MHz	-, (1890	-	pF
C_{rss}	Reverse Transfer Capacitance	1 - 1101112	-	50	-	pF
Q_g	Total Gate Charge			45	-	nC
Q_gs	Gate Source Charge	$V_{GS} = 0 \text{ to } 10V$ $V_{DS} = 100V, I_{D} = 30A$) -	15	-	nC
Q_{gd}	Gate Drain("Miller") Charge	V _{DS} - 100V, I _D - 30A	-	13	-	nC
Switchin	g Characteristics					
t _{d(on)}	Turn-On DelayTime		-	28	-	ns
t _r	Turn-On Rise Time	$V_{GS} = 10V, V_{DD} = 100V$	-	23	-	ns
$t_{\text{d(off)}}$	Turn-Off DelayTime	$I_D = 30A$, $R_{GEN} = 3\Omega$	-	35	-	ns
t_{f}	Turn-Off Fall Time		-	24	-	ns
Drain-So	urce Diode Characteristics and M	/lax Ratings				
I _s	Maximum Continuous Drain to Source Diode Forward Current		-	-	60	Α
I _{sm}	Maximum Pulsed Drain to Source Diode	Forward Current	-	-	240	Α
V_{SD}	Drain to Source Diode Forward Voltage	V _{GS} = 0V, I _S = 30A	-	-	1.2	V
trr	Body Diode Reverse Recovery Time	L 00A 17/11 400A	-	120	-	ns
Qrr	Body Diode Reverse Recovery Charge	$I_F = 30A$, di/dt = 100A/us	-	400	-	nC

Notes:

^{1.} Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature.

^{2.} E_{AS} condition: Starting T_J =25°C, V_{DD} =50V, V_G =10V, R_G =25ohm, L=10mH, I_{AS} =11A

^{3.} Pulse Test: Pulse Width≤300µs, Duty Cycle≤0.5%.

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Test Circuit

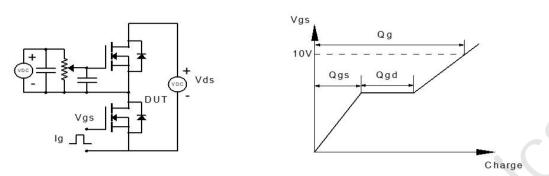


Figure 1: Gate Charge Test Circuit & Waveform

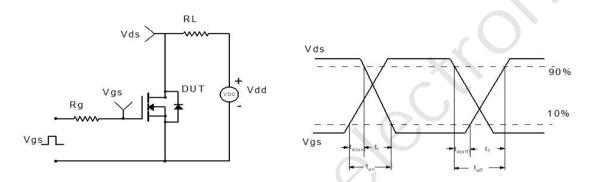


Figure 2: Resistive Switching Test Circuit & Waveform

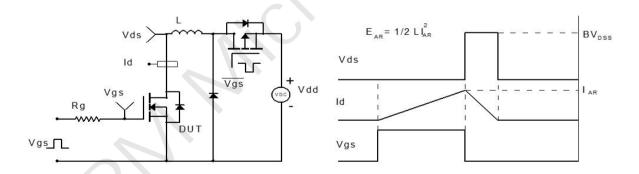


Figure 3: Unclamped Inductive Switching Test Circuit& Waveform

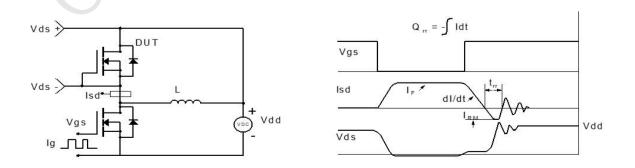
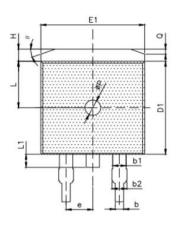


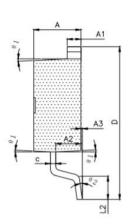
Figure 4: Diode Recovery Test Circuit & Waveform

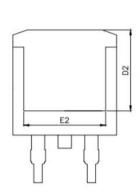
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Package Mechanical Data(TO-263-3L)







SYMBOL	MILLIMETER					
	MIN	NOM	MAX			
Α	4.40	4.50	4.60			
A1	1.20	1.30	1.40			
A2	2.30	2.40	2.50			
A3	0.03	0.13	0.23			
b	0.70	0.80	0.90			
ь1	1.21	1.27	1.40			
b2	1.25	1.35	1.45			
С	0.40	0.50	0.60			
D	14.80	15.10	15.40			
D1	9.10	9.20	9.30			
D2	8.00)				
E	9.70	9.90	10.20			
E1	9.68	9.88	10.08			
E2	7.80					
е	2.54 (BSC)					
Н	1.00	1.20	1.40			
L	4.30	4.60	4.90			
L1	1.10	1.30	1.50			
L2	2.10	2.30	2.50			
ΦP	1.40	1.50	1.60			
Q	0.50 (REF)					
θ	16*	20°	24*			
91	1*	3.	5*			
92	0.	-	9,			

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