## CRMEGH0602A

#### N-Channel 60V, 1.8mΩ Typ. Power MOSFET

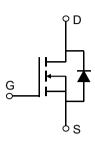
### **Description**

#### **Features**

• 60V, 200A

 $R_{DS(ON)}$  Typ = 1.8m $\Omega$  @  $V_{GS}$  = 10V

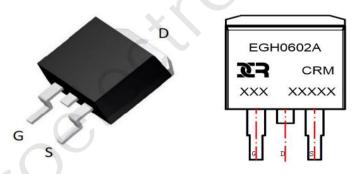
- Advanced Split Gate Trench Technology
- Excellent R<sub>DS(ON)</sub> and Low Gate Charge
- 100% UIS TESTED!
- 100% ΔVds TESTED!





# **Application**

- Load Switch
- PWM Application
- Power Management



**Marking and Pin Assignment** 

### **Package Marking and Ordering Information**

Device	Marking	Package	Outline	Reel Size	Reel (pcs)	Per Carton (pcs)
CRMEGH0602A	CRMEGH0602A	TO-263-3L	TAPING	13"	800	4000

## **Absolute Maximum Ratings** (@ $T_J = 25^{\circ}C$ unless otherwise specified)

Symbol	Parameter		Value	Units
$V_{DS}$	Drain-to-Source Voltage		60	V
$V_{GS}$	Gate-to-Source Voltage		±20	V
	Continuous Drain Current	T <sub>C</sub> = 25°C	200	А
I <sub>D</sub>	Continuous Drain Current	T <sub>C</sub> = 100°C	120	Α
I <sub>DM</sub>	Pulsed Drain Current <sup>(1)</sup>		800	А
E <sub>AS</sub>	Single Pulsed Avalanche Energy (2)		784	mJ
$P_{D}$	Power Dissipation	T <sub>C</sub> = 25°C	138	W
$R_{ heta JC}$	Thermal Resistance, Junction to Case		0.9	°C/W
$T_J,T_STG$	Junction & Storage Temperature Range		-55 to 150	°C

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### **Electrical Characteristics** (T<sub>J</sub> = 25°C unless otherwise specified)

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
Off Char	acteristics					
V <sub>(BR)DSS</sub>	Drain-Source Breakdown Voltage	$I_D = 250 \mu A, V_{GS} = 0 V$	60	-	-	V
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	$V_{DS} = 60V, V_{GS} = 0V$	-	-	1.0	μΑ
I <sub>GSS</sub>	Gate-Body Leakage Current	$V_{DS} = 0V, V_{GS} = \pm 20V$	-	-	±100	nA
On Chara	acteristics				6	
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	2	3	4	V
R <sub>DS(ON)</sub>	Static Drain-Source ON-Resistance <sup>(3)</sup>	V <sub>GS</sub> = 10V, I <sub>D</sub> = 30A	-	1.8	2.2	mΩ
Dynamic	Characteristics					
C <sub>iss</sub>	Input Capacitance		-	7397	-	pF
$C_{oss}$	Output Capacitance	$V_{GS} = 0V, V_{DS} = 25V,$ f = 1MHz	-	3885	-	pF
$C_{rss}$	Reverse Transfer Capacitance	I - IIVINZ	Χ-\	203	-	pF
Q <sub>g</sub>	Total Gate Charge			120	-	nC
$Q_{gs}$	Gate Source Charge	$V_{GS} = 0 \text{ to } 10V$ $V_{DS} = 30V, I_{D} = 30A$	<b>U</b> .	37	-	nC
$Q_{gd}$	Gate Drain("Miller") Charge	v <sub>DS</sub> - 30 v, i <sub>D</sub> - 30A	-	33	-	nC
Switchin	g Characteristics					
t <sub>d(on)</sub>	Turn-On DelayTime		-	26	-	ns
t <sub>r</sub>	Turn-On Rise Time	$V_{GS} = 10V, V_{DD} = 30V$	-	33	-	ns
$t_{\text{d(off)}}$	Turn-Off DelayTime	$I_D = 30A$ , $R_{GEN} = 3\Omega$	-	50	-	ns
$t_{f}$	Turn-Off Fall Time		-	25	-	ns
Drain-So	urce Diode Characteristics and I	Wax Ratings				
I <sub>s</sub>	Maximum Continuous Drain to Source D	iode Forward Current	-	-	200	Α
I <sub>SM</sub>	Maximum Pulsed Drain to Source Diode	Forward Current	-	-	800	Α
$V_{SD}$	Drain to Source Diode Forward Voltage	$V_{GS} = 0V, I_{S} = 30A$	_	_	1.2	V

Notes:

<sup>1.</sup> Repetitive Ratin CRMGGL0603B

<sup>2.</sup>  $E_{AS}$  condition: Starting  $T_J$ =25°C,  $V_{DD}$ =30V,  $V_G$ =10V,  $R_G$ =25ohm, L=0.5mH,  $I_{AS}$ =56A

<sup>3.</sup> Pulse Test: Pulse Width≤300µs, Duty Cycle≤0.5%.

# **Typical Performance Characteristics**

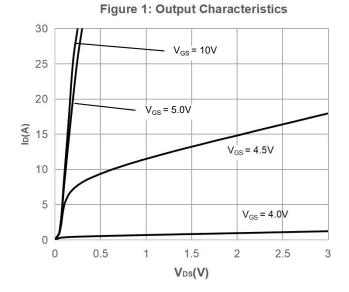


Figure 3: On-resistance vs. Drain Current

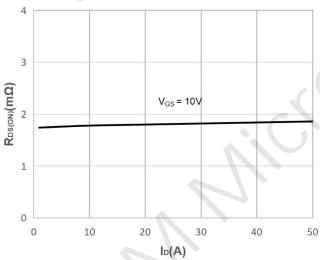


Figure 5: Gate Charge Characteristics

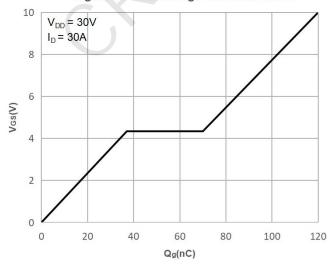


Figure 2: Typical Transfer Characteristics

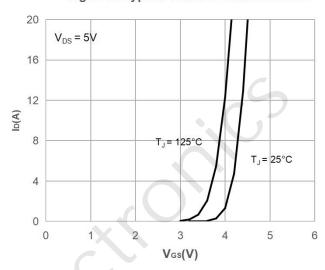


Figure 4: Body Diode Characteristics

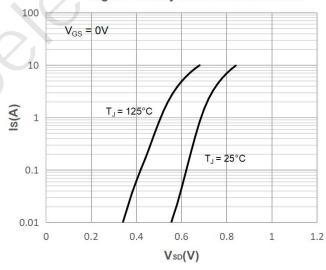
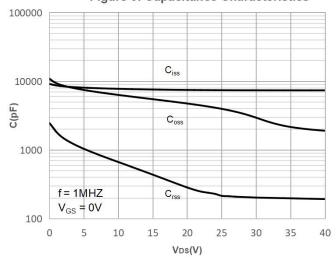


Figure 6: Capacitance Characteristics



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## **Typical Performance Characteristics**

Figure 7: Normalized Breakdown voltage vs.
Junction Temperature

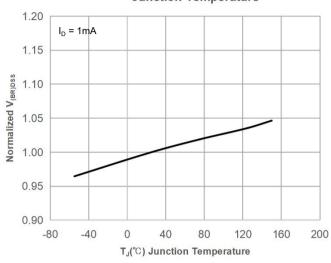


Figure 9: Maximum Safe Operating Area

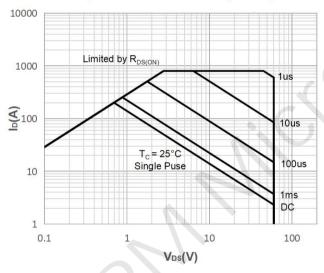


Figure 11: Normalized Maximum Transient

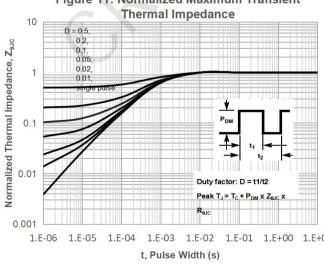


Figure 8: Normalized on Resistance vs. Junction Temperature

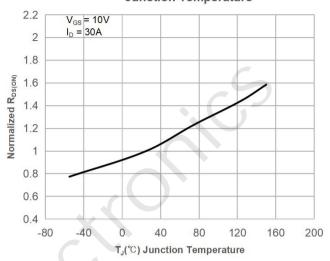


Figure 10: Maximum Continuous Drian
Current vs. Case Temperature

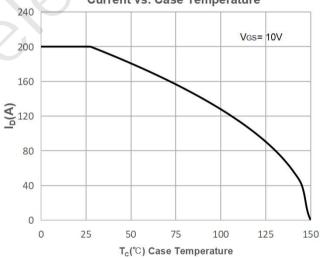
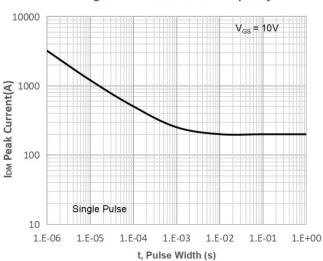


Figure 12: Peak Current Capacity



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### **Test Circuit**

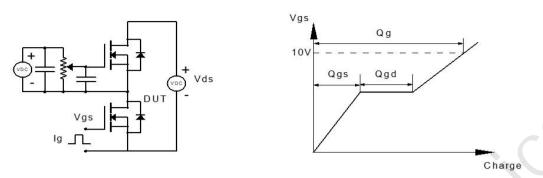


Figure 1: Gate Charge Test Circuit & Waveform

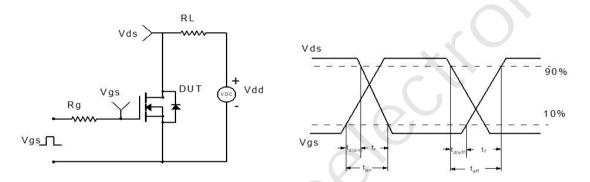


Figure 2: Resistive Switching Test Circuit & Waveform

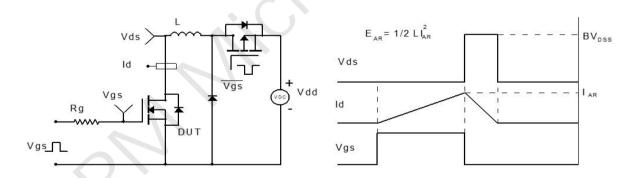


Figure 3: Unclamped Inductive Switching Test Circuit& Waveform

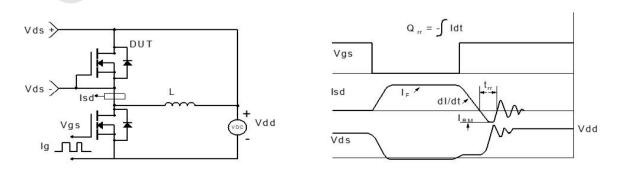
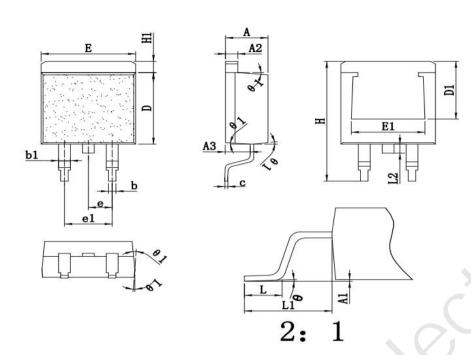


Figure 4: Diode Recovery Test Circuit & Waveform

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### Package Mechanical Data(TO-263-3L)



SYMBOL	mm				
SIMBUL	MIN	NOM	MAX		
<b>*</b> A	4. 42	4. 52	4. 62		
<b>*</b> A1	0.00	0.10	0. 20		
*A2	1. 24	1. 27	1. 32		
<b>*</b> A3	2. 50	2.60	2. 70		
<b>*</b> b	0.77	0.81	0.84		
<b>*</b> b1	1. 23	1. 28	1.41		
*c	0. 33	0.38	0. 43		
*D	8. 80	8. 95	9. 10		
D1	7. 25REF				
*E	9. 92	10.07	10. 22		
E1	7. 85REF				
<b>*</b> e	2. 50	2. 54 2.			
e1	5. 08REF				
<b>*</b> H	14. 80	15. 10	15. 30		
H1	1. 12	1. 28	1. 42		
*L	2. 10	2. 23	2. 36		
L1	4. 55	4. 75	4. 95		
L2	1. 10	1.30	1. 50		
*0	0°	2°	5°		
θ1	3°	_	9°		

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