CRMGTH0202A

N-Channel 22V, 1.7mΩ Typ. Power MOSFET

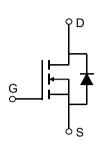
Description

Features

• 22V, 130A

 $R_{DS(ON)}$ Typ = 1.7m Ω @ V_{GS} = 10V

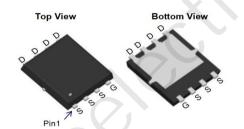
- Advanced Trench Technology
- Excellent R_{DS(ON)} and Low Gate Charge
- Lead Free
- 100% UIS TESTED!
- 100% ΔVds TESTED!

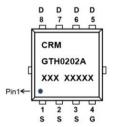




Application

- Load Switch
- PWM Application
- Power Management





Marking and Pin Assignment

Package Marking and Ordering Information

Device	Marking	Package	Outline	Reel Size	Reel (pcs)	Per Carton (pcs)
CRMGTH0202A	CRMGTH0202A	PDFN5x6-8L	TAPING	13"	5000	50000

Absolute Maximum Ratings (@ $T_J = 25^{\circ}C$ unless otherwise specified)

Symbol	Parameter		Value	Units
V_{DS}	Drain-to-Source Voltage		22	V
V _{GS}	Gate-to-Source Voltage		±20	V
	Continuous Drain Current	T _C = 25°C	130	А
I _D		T _C = 100°C	78	Α
I _{DM}	Pulsed Drain Current (1)		520	Α
E _{AS}	Single Pulsed Avalanche Energy (2)		196	mJ
P_{D}	Power Dissipation	T _C = 25°C	62.5	W
$R_{ heta JC}$	Thermal Resistance, Junction to Case		2	°C/W
T_J,T_STG	Junction & Storage Temperature Range		-55 to 150	°C



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Electrical Characteristics (T_J = 25°C unless otherwise specified)

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
_		Conditions	IVIIII.	ıyρ.	wax.	Oilit
	acteristics	1 050 4 1/ 01/	00			
$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	$I_D = 250 \mu A, V_{GS} = 0 V$	22	-	-	V
I _{DSS}	Zero Gate Voltage Drain Current	$V_{DS} = 22V, V_{GS} = 0V$	-	-	1.0	μА
I _{GSS}	Gate-Body Leakage Current	$V_{DS} = 0V, V_{GS} = \pm 20V$	-	-	±100	nA
On Chara	acteristics				<u></u>	
$V_{\text{GS(th)}}$	Gate Threshold Voltage	V_{DS} = V_{GS} , I_D = 250 μ A	1.7	2.2	2.7	V
R _{DS(ON)}	Static Drain-Source ON-Resistance ⁽³⁾	$V_{GS} = 10V, I_D = 20A$	-	1.7	2.2	$m\Omega$
Dynamic	Characteristics					
C _{iss}	Input Capacitance		-	3578	-	pF
C_{oss}	Output Capacitance	$V_{GS} = 0V, V_{DS} = 10V,$ f = 1MHz	-	717	-	pF
C_{rss}	Reverse Transfer Capacitance	I – TIVIMZ	X -	487	-	pF
Q _q	Total Gate Charge		-	43	-	nC
Q_{gs}	Gate Source Charge	$V_{GS} = 0$ to 10V	\mathcal{O}_{\perp}	8	-	nC
Q_{gd}	Gate Drain("Miller") Charge	$V_{DS} = 10V, I_{D} = 30A$	-	13	-	nC
Switchin	g Characteristics					
t _{d(on)}	Turn-On DelayTime		-	14	-	ns
t_r	Turn-On Rise Time	$V_{GS} = 10V, V_{DD} = 10V$	-	32	-	ns
$t_{d(off)}$	Turn-Off DelayTime	$I_D = 30A$, $R_{GEN} = 3\Omega$	-	78	-	ns
t _f	Turn-Off Fall Time		_	80	-	ns
Drain-So	urce Diode Characteristics and M	Max Ratings				
I _S	Maximum Continuous Drain to Source Diode Forward Current		-	-	130	Α
I _{SM}	Maximum Pulsed Drain to Source Diode Forward Current		-	-	520	Α
V _{SD}	Drain to Source Diode Forward Voltage	V _{GS} = 0V, I _S = 20A	-	-	1.2	V
trr	Body Diode Reverse Recovery Time		-	15	-	ns
Qrr	Body Diode Reverse Recovery Charge	$I_F = 20A$, di/dt = 100A/us	_	8	_	nC
QΠ	Body Blode Neverse Necovery Charge		-	J		110

Notes:

^{1.} Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature.

^{2.} E_{AS} condition: Starting T_J =25°C, V_{DD} =10V, V_G =10V, R_G =25ohm, L=0.5mH, I_{AS} =28A

^{3.} Pulse Test: Pulse Width≤300µs, Duty Cycle≤0.5%.

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Test Circuit

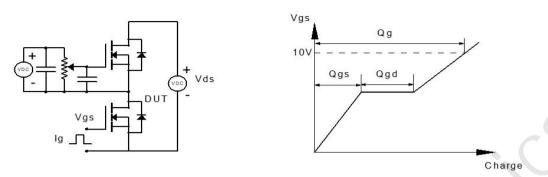


Figure 1: Gate Charge Test Circuit & Waveform

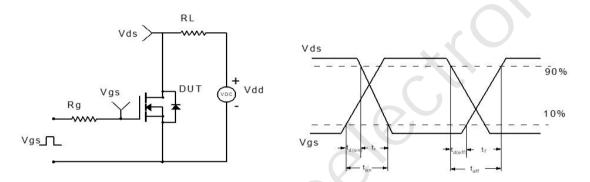


Figure 2: Resistive Switching Test Circuit & Waveform

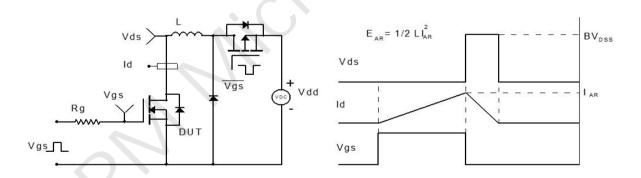


Figure 3: Unclamped Inductive Switching Test Circuit& Waveform

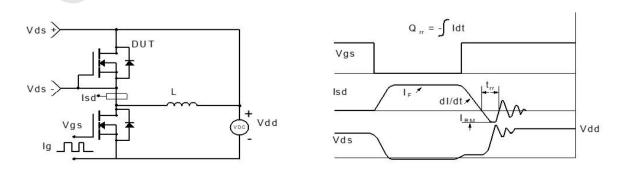
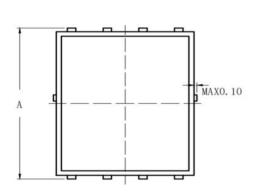


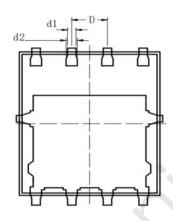
Figure 4: Diode Recovery Test Circuit & Waveform

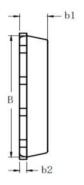
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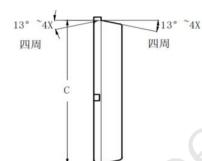
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Package Mechanical Data(PDFN5x6-8L)









COMMON IN DIMENSION (MM)					
PKG		PDFN 5×6-8L			
Symbol	MIN	MON	MAX		
A	6.000	6.100	6.200		
В	4.875	4.900	4.925		
Ь1	0. 975	1.000	1. 025		
b2	0.246	0.254	0.262		
С	5.775	5.800	5.825		
D	1.245	1.270	1.295		
d1	0.275	0.300	0.325		
d2	0.375	0.400	0.425		

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