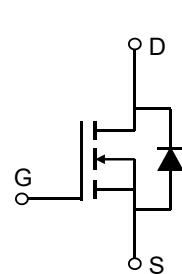


Description

Features

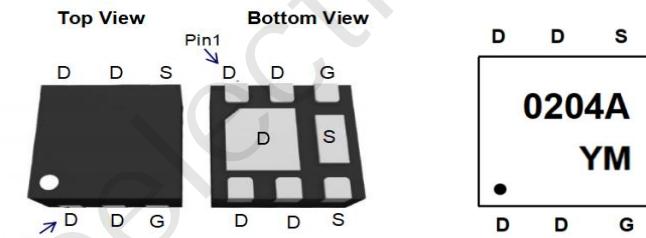
- 20V, 25A
- $R_{DS(ON)}$ Typ = 6.3mΩ @ V_{GS} = 4.5V
- $R_{DS(ON)}$ Typ = 8.0mΩ @ V_{GS} = 2.5V
- Advanced Trench Technology
- Excellent $R_{DS(ON)}$ and Low Gate Charge
- Lead Free
- 100% ΔV_{ds} TESTED!



Schematic Diagram

Application

- Load Switch
- PWM Application
- Power Management



Marking and Pin Assignment

Package Marking and Ordering Information

Device	Marking	Package	Outline	Reel Size	Reel (pcs)	Per Carton (pcs)
CRMVTU0204A	0204A	DFN2020-6L	TAPING	7"	3000	120000

Absolute Maximum Ratings (@ T_J = 25°C unless otherwise specified)

Symbol	Parameter	Value	Units
V_{DS}	Drain-to-Source Voltage	20	V
V_{GS}	Gate-to-Source Voltage	± 12	V
I_D	Continuous Drain Current $T_C = 25^\circ\text{C}$	25	A
	$T_C = 100^\circ\text{C}$	15	A
I_{DM}	Pulsed Drain Current ⁽¹⁾	100	A
P_D	Power Dissipation $T_C = 25^\circ\text{C}$	7.35	W
$R_{θJC}$	Thermal Resistance, Junction to Case	17	°C/W
T_J, T_{STG}	Junction & Storage Temperature Range	-55 to 150	°C

Electrical Characteristics ($T_J = 25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
Off Characteristics						
$V_{(\text{BR})\text{DSS}}$	Drain-Source Breakdown Voltage	$I_D = 250\mu\text{A}, V_{GS} = 0\text{V}$	20	-	-	V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS} = 20\text{V}, V_{GS} = 0\text{V}$	-	-	1.0	μA
I_{GSS}	Gate-Body Leakage Current	$V_{DS} = 0\text{V}, V_{GS} = \pm 12\text{V}$	-	-	± 100	nA
On Characteristics						
$V_{GS(\text{th})}$	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_D = 250\mu\text{A}$	0.4	0.7	1	V
$R_{\text{DS(ON)}}$	Static Drain-Source ON-Resistance ⁽²⁾	$V_{GS} = 4.5\text{V}, I_D = 5\text{A}$	-	6.3	8.2	$\text{m}\Omega$
		$V_{GS} = 2.5\text{V}, I_D = 3\text{A}$	-	8	10.4	$\text{m}\Omega$
Dynamic Characteristics						
C_{iss}	Input Capacitance		-	1945	-	pF
C_{oss}	Output Capacitance	$V_{GS} = 0\text{V}, V_{DS} = 10\text{V}, f = 1\text{MHz}$	-	248	-	pF
C_{rss}	Reverse Transfer Capacitance		-	218	-	pF
Q_g	Total Gate Charge		-	23	-	nC
Q_{gs}	Gate Source Charge	$V_{GS} = 0$ to 4.5V	-	4	-	nC
Q_{gd}	Gate Drain("Miller") Charge	$V_{DS} = 10\text{V}, I_D = 20\text{A}$	-	7	-	nC
Switching Characteristics						
$t_{d(on)}$	Turn-On DelayTime		-	12	-	ns
t_r	Turn-On Rise Time	$V_{GS} = 4.5\text{V}, V_{DD} = 10\text{V}$	-	33	-	ns
$t_{d(off)}$	Turn-Off DelayTime	$I_D = 20\text{A}, R_{\text{GEN}} = 3\Omega$	-	48	-	ns
t_f	Turn-Off Fall Time		-	95	-	ns
Drain-Source Diode Characteristics and Max Ratings						
I_S	Maximum Continuous Drain to Source Diode Forward Current		-	-	25	A
I_{SM}	Maximum Pulsed Drain to Source Diode Forward Current		-	-	100	A
V_{SD}	Drain to Source Diode Forward Voltage	$V_{GS} = 0\text{V}, I_S = 5\text{A}$	-	-	1.2	V
trr	Body Diode Reverse Recovery Time		-	11	-	ns
Qrr	Body Diode Reverse Recovery Charge	$I_F = 20\text{A}, di/dt = 100\text{A}/\mu\text{s}$	-	2.5	-	nC

Notes:

1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature.

2. Pulse Test: Pulse Width $\leq 300\mu\text{s}$, Duty Cycle $\leq 0.5\%$.

Test Circuit

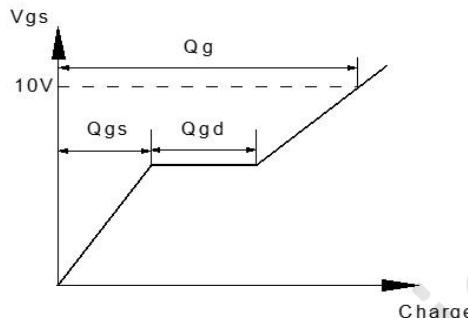
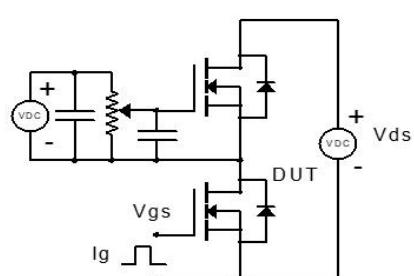


Figure 1: Gate Charge Test Circuit & Waveform

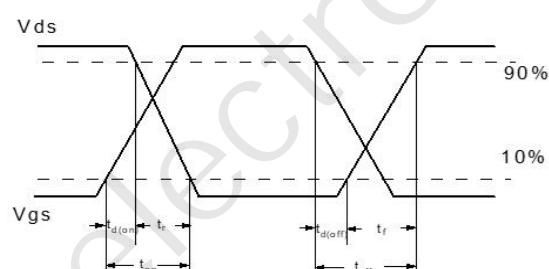
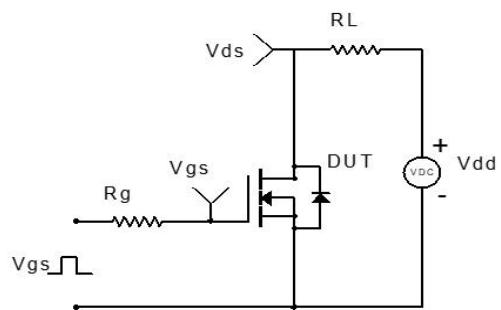


Figure 2: Resistive Switching Test Circuit & Waveform

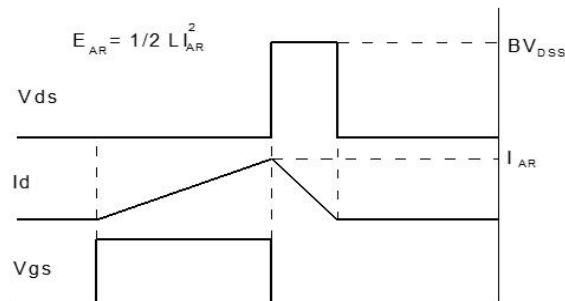
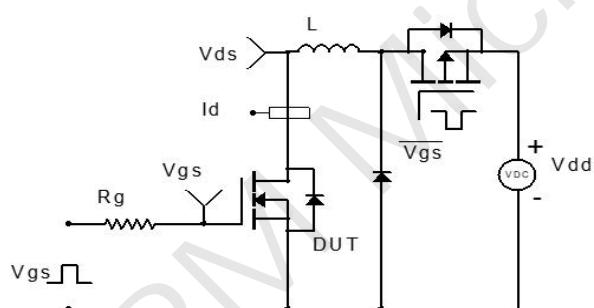


Figure 3: Unclamped Inductive Switching Test Circuit & Waveform

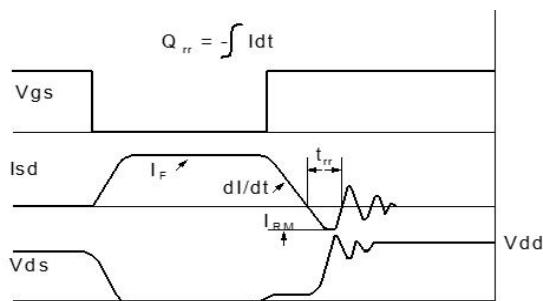
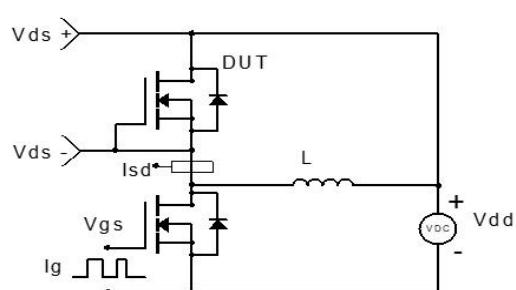
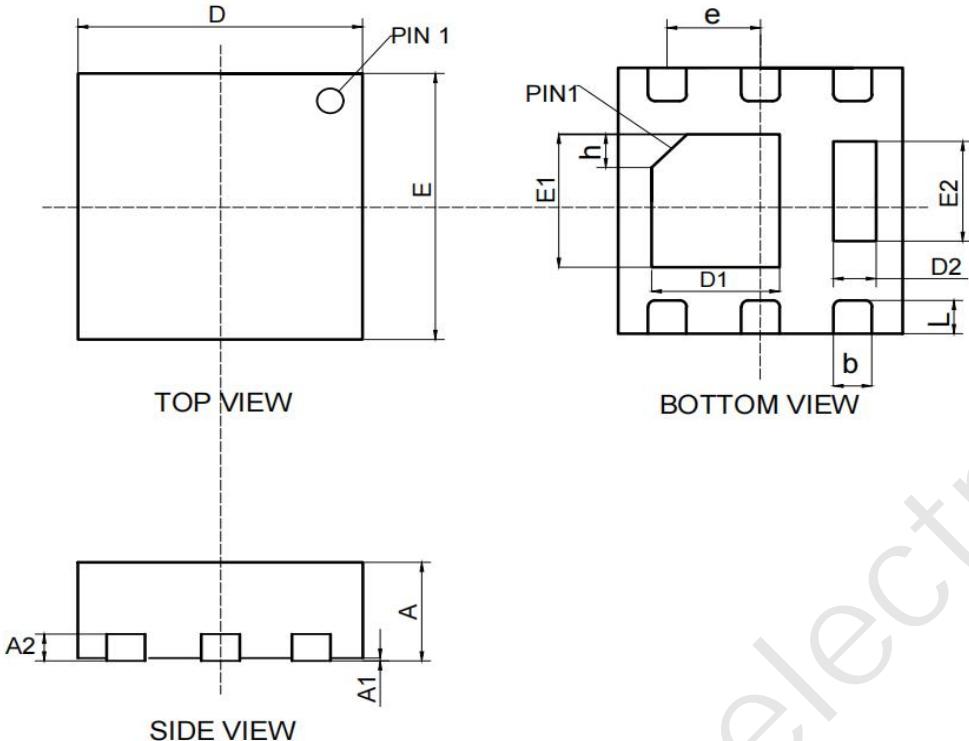


Figure 4: Diode Recovery Test Circuit & Waveform

Package Mechanical Data(DFN2020-6L)



SYMBOL	MIN	NOM	MAX
A	0.70	0.75	0.80
A1	NA	0.02	0.05
A2	0.18	0.20	0.25
b	0.20	0.27	0.34
D	1.95	2.00	2.05
E	1.95	2.00	2.05
D1	0.80	0.90	1.00
E1	0.90	1.00	1.10
D2	0.20	0.30	0.40
E2	0.65	0.75	0.85
L	0.20	0.25	0.35
h	0.20	0.25	0.30
e	0.65 BSC		

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