N-Channel 100V, 1.7mΩ Typ. Power MOSFET

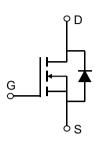
Description

Features

• 100V, 290A

 $R_{DS(ON)}$ Typ = 1.7m Ω @ V_{GS} = 10V

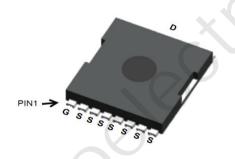
- Advanced Split Gate Trench Technology
- Excellent R_{DS(ON)} and Low Gate Charge
- 100% UIS TESTED!
- 100% ΔVds TESTED!

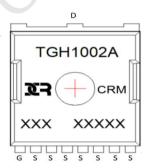




Application

- Load Switch
- PWM Application
- Power Management





Marking and Pin Assignment

Package Marking and Ordering Information

Device	Marking	Package	Outline	Reel Size	Reel (pcs)	Per Carton (pcs)
CRMTGH1002A	CRMTGH1002A	TOLL	TAPING	13"	2000	10000

Absolute Maximum Ratings (@ $T_J = 25^{\circ}C$ unless otherwise specified)

Symbol	Parameter		Value	Units
V_{DS}	Drain-to-Source Voltage		100	V
V_{GS}	Gate-to-Source Voltage		±20	V
	Continuous Drain Current	T _C = 25°C	290	Α
I _D	Continuous Drain Current	T _C = 100°C	174	Α
I _{DM}	Pulsed Drain Current (1)		1160	Α
E _{AS}	Single Pulsed Avalanche Energy (2)		729	mJ
P_{D}	Power Dissipation	T _C = 25°C	319	W
$R_{ heta JC}$	Thermal Resistance, Junction to Case		0.47	°C/W
T_J, T_STG	Junction & Storage Temperature Range		-55 to 175	°C

N-Channel 100V, 1.7m Ω Typ. Power MOSFET

Electrical Characteristics (T_J = 25°C unless otherwise specified)

Symbol	Parameter	Conditions	Min.	Tyro	Max.	Unit
9		Conditions	IVIIII.	Тур.	IVIAX.	Ullit
Off Chara	acteristics					
$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	$I_D = 250 \mu A, V_{GS} = 0 V$	100	-	-	V
I _{DSS}	Zero Gate Voltage Drain Current	$V_{DS} = 100V, V_{GS} = 0V$	-	-	1.0	μΑ
I_{GSS}	Gate-Body Leakage Current	$V_{DS} = 0V$, $V_{GS} = \pm 20V$	-	-	±100	nA
On Chara	acteristics				6	
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	2	2.8	4	V
R _{DS(ON)}	Static Drain-Source ON-Resistance ⁽³⁾	$V_{GS} = 10V, I_D = 30A$	-	1.7	2	mΩ
Dynamic	Characteristics					
C _{iss}	Input Capacitance		-	9385	-	pF
C_{oss}	Output Capacitance	$V_{GS} = 0V, V_{DS} = 50V,$ f = 300KHz	-	2911	-	pF
C_{rss}	Reverse Transfer Capacitance	1 – 300KHZ	X -\	92	-	pF
Q_g	Total Gate Charge			140	-	nC
Q_gs	Gate Source Charge	$V_{GS} = 0$ to 10V	U .	48	-	nC
Q_gd	Gate Drain("Miller") Charge	$V_{DS} = 50V, I_{D} = 90A$	-	30	-	nC
Switchin	g Characteristics					
t _{d(on)}	Turn-On DelayTime		-	30	-	ns
t _r	Turn-On Rise Time	$V_{GS} = 10V, V_{DD} = 50V$	-	105	-	ns
$t_{d(off)}$	Turn-Off DelayTime	$I_D = 90A$, $R_{GEN} = 3\Omega$	-	81	-	ns
t _f	Turn-Off Fall Time		_	109	-	ns
Drain-So	urce Diode Characteristics and M	Max Ratings				
I _S	Maximum Continuous Drain to Source Diode Forward Current		-	-	290	Α
I _{SM}	Maximum Pulsed Drain to Source Diode Forward Current		-	-	1160	Α
V _{SD}	Drain to Source Diode Forward Voltage	V _{GS} = 0V, I _S = 30A	-	-	1.4	V
trr	Body Diode Reverse Recovery Time	, 0	-	56	-	ns
Qrr	Body Diode Reverse Recovery Charge	$I_F = 90A$, di/dt = 100A/us	_	96	_	nC
~						

Notes:

^{1.} Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature.

^{2.} E_{AS} condition: Starting T_J =25°C, V_{DD} =30V, V_G =10V, R_G =25ohm, L=0.5mH, I_{AS} =54A

^{3.} Pulse Test: Pulse Width $\!\!\!\leq\!300\mu s,$ Duty Cycle $\!\!\!\leq\!0.5\%.$

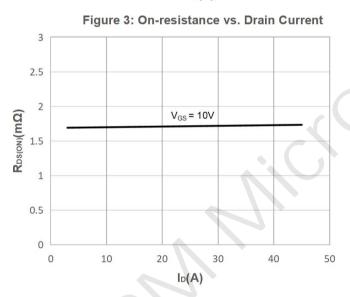
N-Channel 100V, 1.7mΩ Typ. Power MOSFET

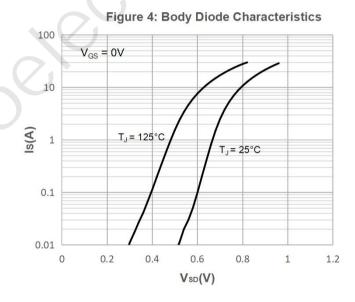
Typical Performance Characteristics

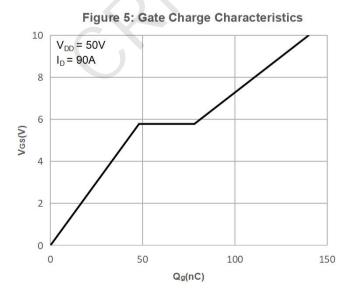
Figure 1: Output Characteristics 30 $V_{GS} = 10V$ 25 V_{GS} = 4.8V 20 15 V_{GS} = 4.6V 10 5 $V_{GS} = 4.2V$ 0 2 3 1 VDs(V)

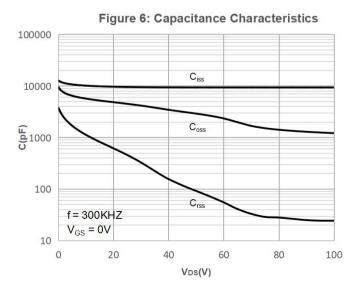
20 $V_{DS} = 5V$ 16 12 ID(A) T_J = 125°C 8 T_J= 25°C 4 0 2 0 0.5 1 1.5 2.5 3 3.5 4 4.5 5 5.5 6 Vgs(V)

Figure 2: Typical Transfer Characteristics









Typical Performance Characteristics

Figure 7: Normalized Breakdown voltage vs.
Junction Temperature

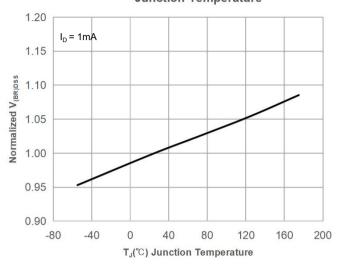


Figure 9: Maximum Safe Operating Area

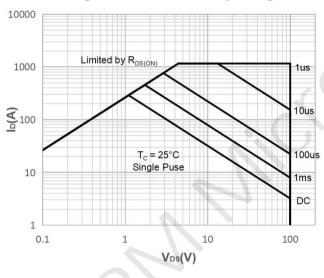


Figure 11: Normalized Maximum Transient

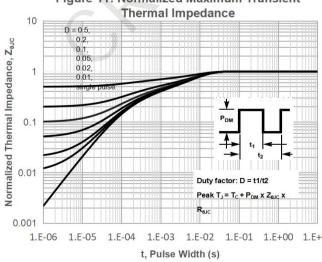


Figure 8: Normalized on Resistance vs.
Junction Temperature

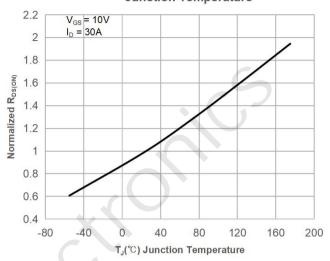


Figure 10: Maximum Continuous Drian
Current vs. Case Temperature

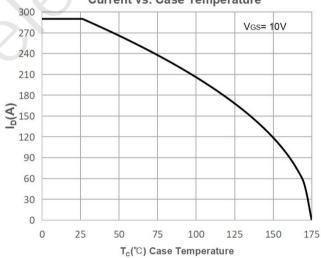
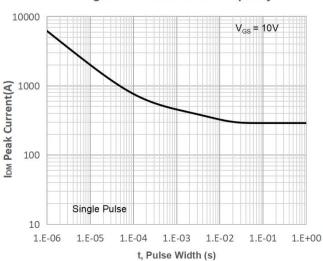


Figure 12: Peak Current Capacity



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Test Circuit

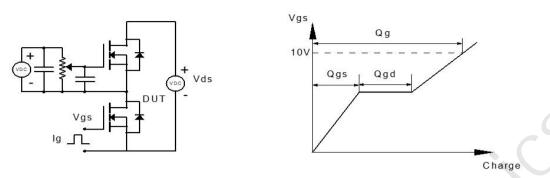


Figure 1: Gate Charge Test Circuit & Waveform

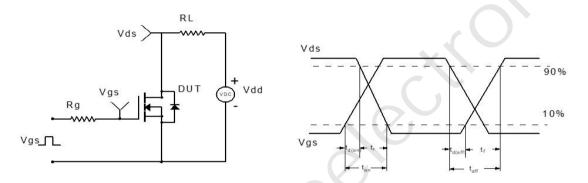


Figure 2: Resistive Switching Test Circuit & Waveform

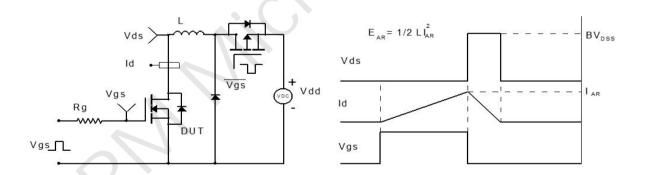


Figure 3: Unclamped Inductive Switching Test Circuit& Waveform

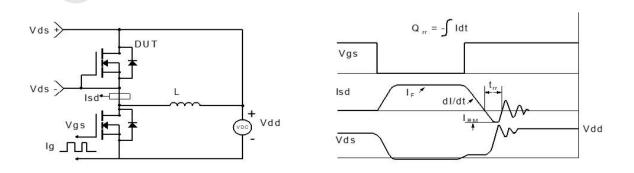
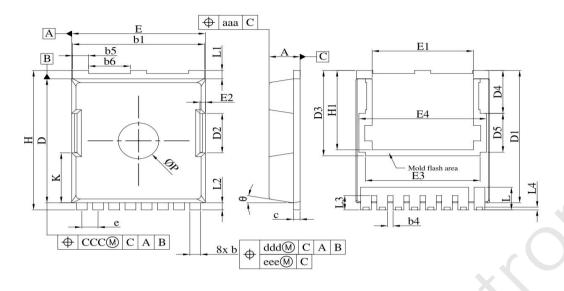


Figure 4: Diode Recovery Test Circuit & Waveform

S

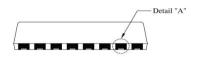
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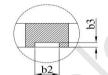
Package Mechanical Data(TOLL)



B	MILLIMETER				
L	MIN.	NOMINAL	MAX		
A	2.20	2.30	2.40		
b	0.70	0.80	0.90		
bl	9.70	9.80	9.90		
b2	0.36	0.45	0.55		
b3	0.05	0.100	1		
b4	0.30	0.40	0.50		
b5	1.10	1.20	1.30		
b6	3.00	3.10	3.20		
С	0.40	0.50	0.60		
D	10.28	10.38	10.55		
D1	10.98	11.08	11.18		
D2	3.20	3.30	3.40		
D3		7.15			
D4	3.59				
D5		3.26			
e	1.10	1.20	1.30		
Е	9.80	9.90	10.00		
E1	7.40	7.50	7.60		
E2	0.30	0.40	0.50		
E3		8.50			
E4		9.46			
Н	11.50	11.68	11.85		
H1	6.55	6.65	6.75		
K	4.08	4.18	4.28		
L	1.60	1.90	2.10		
Ll	0.50	0.70	0.90		
L2	0.50	0.60	0.70		
L3	1.00	1.20	1.30		
L4	0.13	0.23	0.33		
P	2.85	3.00	3.15		
θ		10° REF			
aaa	0.20				
ccc	0.20				
ddd		0.25			
eee		0.20			

COMMON





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