

### Description

#### Features

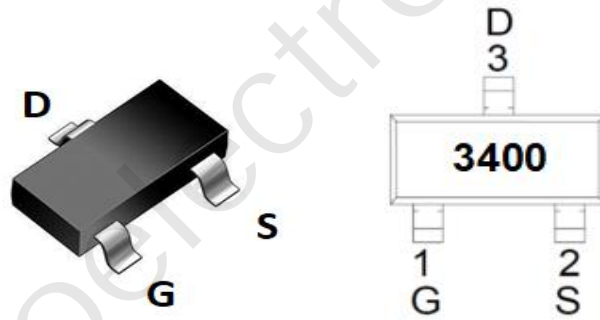
- 30V, 5.8A
- $R_{DS(ON)}$  Typ = 23mΩ @  $V_{GS} = 10V$
- $R_{DS(ON)}$  Typ = 25mΩ @  $V_{GS} = 4.5V$
- $R_{DS(ON)}$  Typ = 30mΩ @  $V_{GS} = 2.5V$
- Advanced Trench Technology
- Excellent  $R_{DS(ON)}$  and Low Gate Charge
- Lead Free



Schematic Diagram

#### Application

- Load Switch
- PWM Application
- Power Management



Marking and Pin Assignment

#### Package Marking and Ordering Information

Device	Marking	Package	Outline	Reel Size	Reel (pcs)	Per Carton (pcs)
CRMLTU3400A	3400	SOT-23	TAPING	7"	3000	120000

#### Absolute Maximum Ratings (@ $T_J = 25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter	Value	Units
$V_{DS}$	Drain-to-Source Voltage	30	V
$V_{GS}$	Gate-to-Source Voltage	±12	V
$I_D$	Continuous Drain Current	$T_A = 25^\circ\text{C}$	5.8
		$T_A = 100^\circ\text{C}$	3.48
$I_{DM}$	Pulsed Drain Current <sup>(1)</sup>	23.2	A
$P_D$	Power Dissipation	$T_A = 25^\circ\text{C}$	1.56
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient <sup>(2)</sup>	80	°C/W
$T_J, T_{STG}$	Junction & Storage Temperature Range	-55 to 150	°C

### Electrical Characteristics ( $T_J = 25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
Off Characteristics						
V <sub>(BR)DSS</sub>	Drain-Source Breakdown Voltage	I <sub>D</sub> = 250μA, V <sub>GS</sub> = 0V	30	-	-	V
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	V <sub>DS</sub> = 30V, V <sub>GS</sub> = 0V	-	-	1.0	μA
I <sub>GSS</sub>	Gate-Body Leakage Current	V <sub>DS</sub> = 0V, V <sub>GS</sub> = ±12V	-	-	±100	nA
On Characteristics						
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250μA	0.5	0.9	1.3	V
R <sub>DS(ON)</sub>	Static Drain-Source ON-Resistance <sup>(3)</sup>	V <sub>GS</sub> = 10V, I <sub>D</sub> = 3A	-	23	30	mΩ
		V <sub>GS</sub> = 4.5V, I <sub>D</sub> = 2A	-	25	33	mΩ
		V <sub>GS</sub> = 2.5V, I <sub>D</sub> = 1.5A	-	30	39	mΩ
Dynamic Characteristics						
C <sub>iss</sub>	Input Capacitance	V <sub>GS</sub> = 0V, V <sub>DS</sub> = 15V, f = 1MHz	-	876	-	pF
C <sub>oss</sub>	Output Capacitance		-	59	-	pF
C <sub>rss</sub>	Reverse Transfer Capacitance		-	50	-	pF
Q <sub>g</sub>	Total Gate Charge	V <sub>GS</sub> = 0 to 10V V <sub>DS</sub> = 15V, I <sub>D</sub> = 3A	-	19	-	nC
Q <sub>gs</sub>	Gate Source Charge		-	2	-	nC
Q <sub>gd</sub>	Gate Drain("Miller") Charge		-	2.1	-	nC
Switching Characteristics						
t <sub>d(on)</sub>	Turn-On DelayTime	V <sub>GS</sub> = 10V, V <sub>DD</sub> = 15V I <sub>D</sub> = 3A, R <sub>GEN</sub> = 3Ω	-	4	-	ns
t <sub>r</sub>	Turn-On Rise Time		-	11	-	ns
t <sub>d(off)</sub>	Turn-Off DelayTime		-	24	-	ns
t <sub>f</sub>	Turn-Off Fall Time		-	2	-	ns
Drain-Source Diode Characteristics and Max Ratings						
I <sub>S</sub>	Maximum Continuous Drain to Source Diode Forward Current	V <sub>GS</sub> = 0V, I <sub>S</sub> = 3A	-	-	5.8	A
I <sub>SM</sub>	Maximum Pulsed Drain to Source Diode Forward Current		-	-	23.2	A
V <sub>SD</sub>	Drain to Source Diode Forward Voltage		-	-	1.2	V
trr	Body Diode Reverse Recovery Time		-	8.4	-	ns
Qrr	Body Diode Reverse Recovery Charge		-	3.3	-	nC

- Notes:
1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature.
  2.  $R_{\theta JA}$  is measured with the device mounted on a 1inch<sup>2</sup> pad of 2oz copper FR4 PCB
  3. Pulse Test: Pulse Width  $\leq 300\mu\text{s}$ , Duty Cycle  $\leq 0.5\%$ .

## Typical Performance Characteristics

Figure 1: Output Characteristics

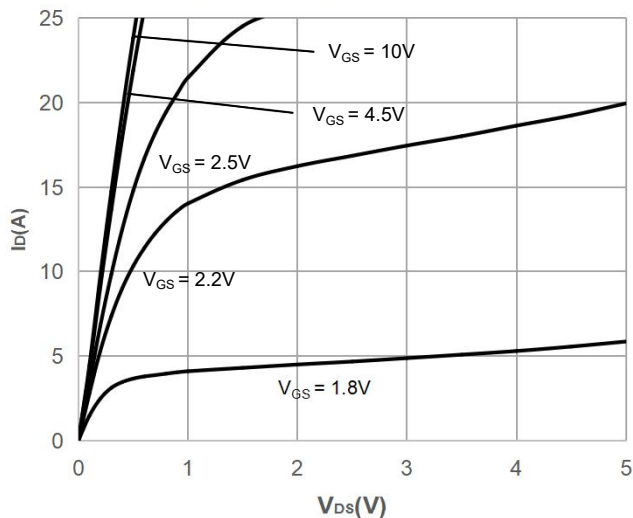


Figure 2: Typical Transfer Characteristics

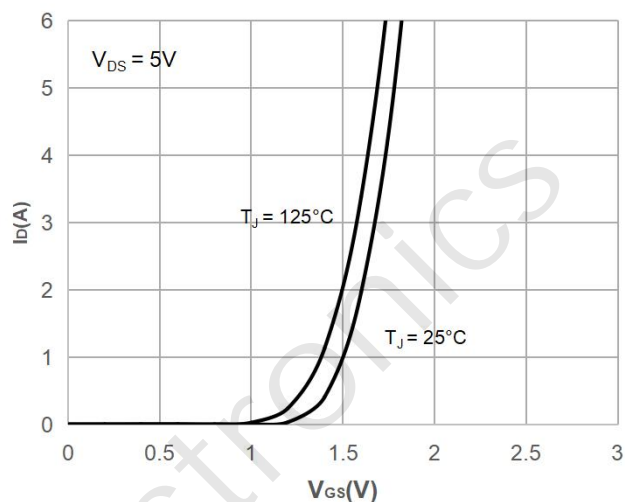


Figure 3: On-resistance vs. Drain Current

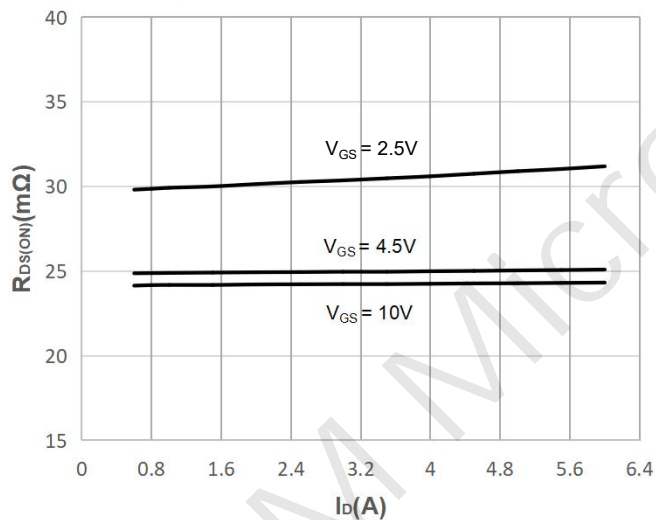


Figure 4: Body Diode Characteristics

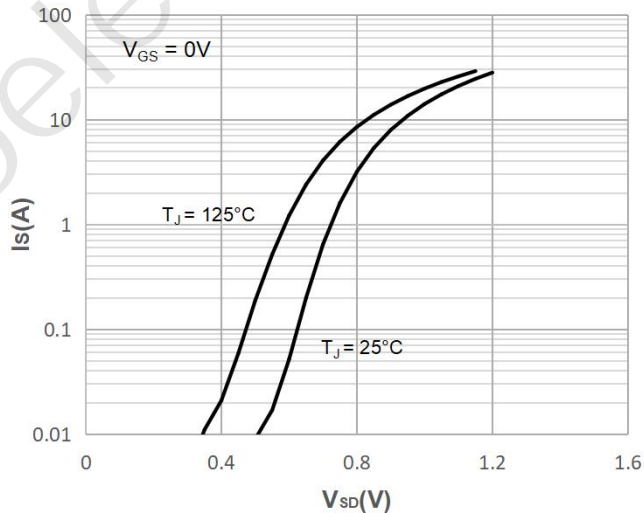


Figure 5: Gate Charge Characteristics

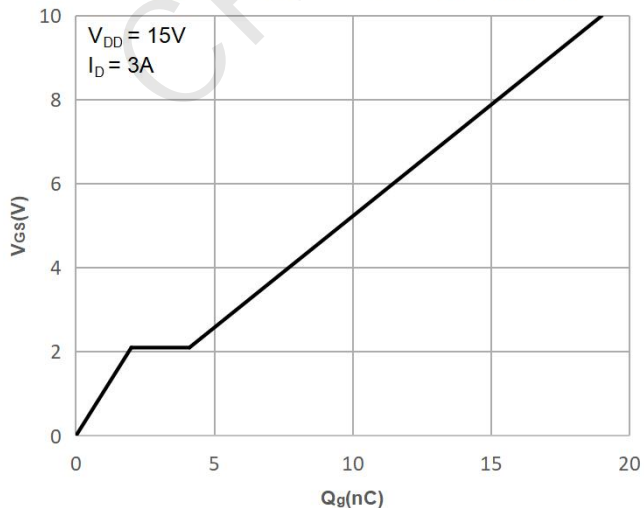
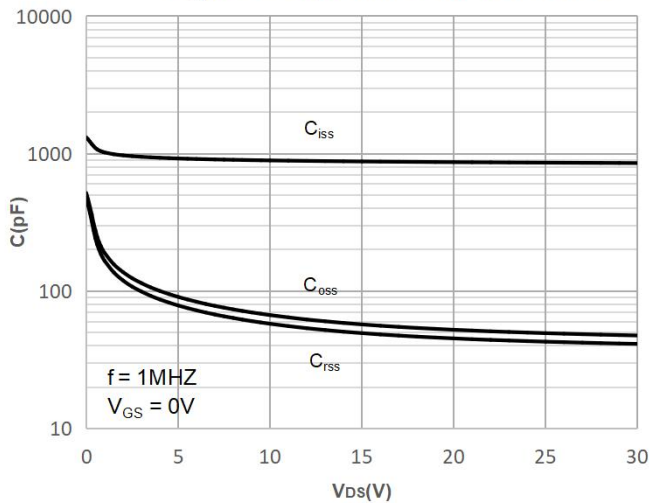


Figure 6: Capacitance Characteristics



## Typical Performance Characteristics

Figure 7: Normalized Breakdown voltage vs. Junction Temperature

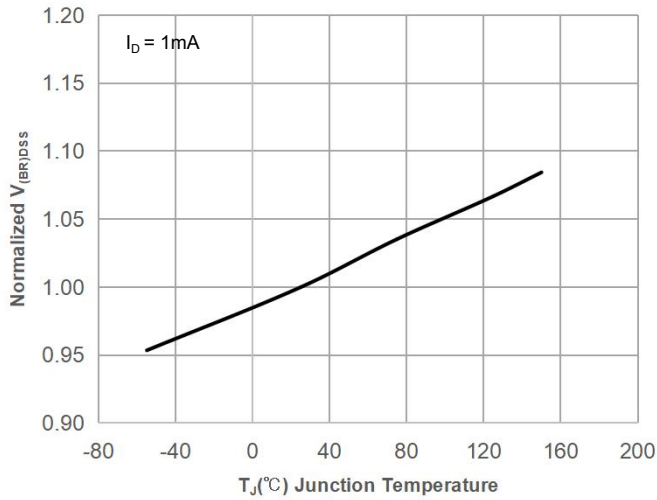


Figure 8: Normalized on Resistance vs. Junction Temperature

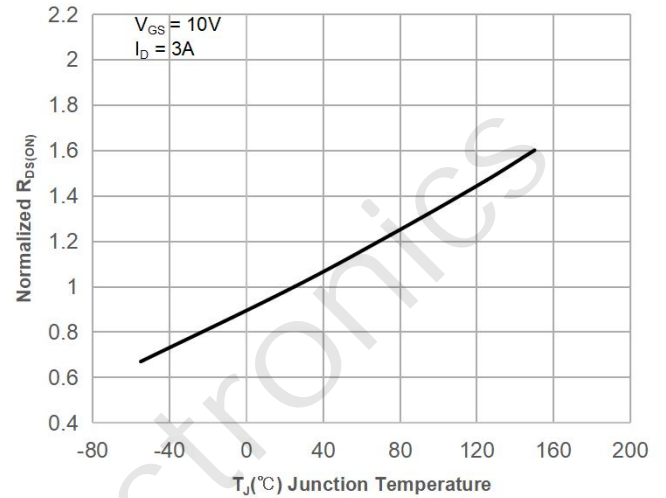


Figure 9: Maximum Safe Operating Area

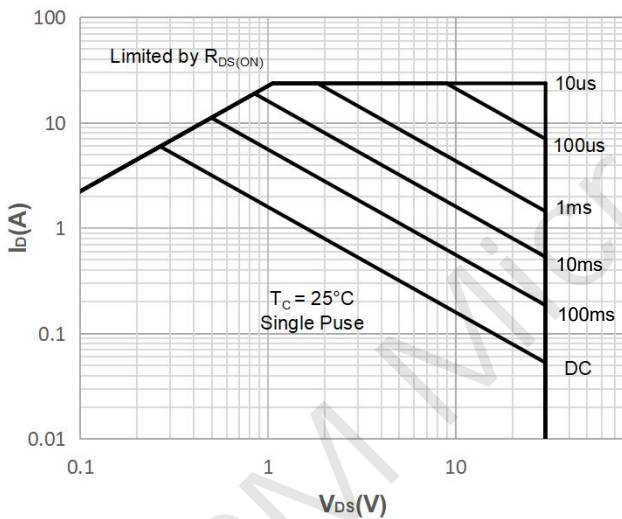


Figure 10: Maximum Continuous Drain Current vs. Case Temperature

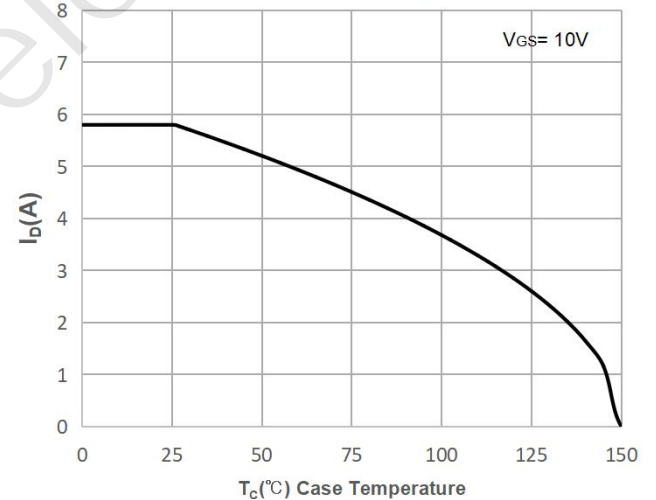


Figure 11: Normalized Maximum Transient Thermal Impedance

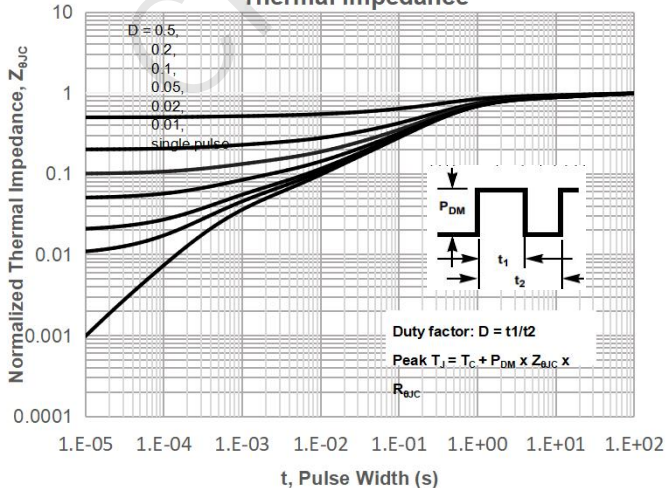
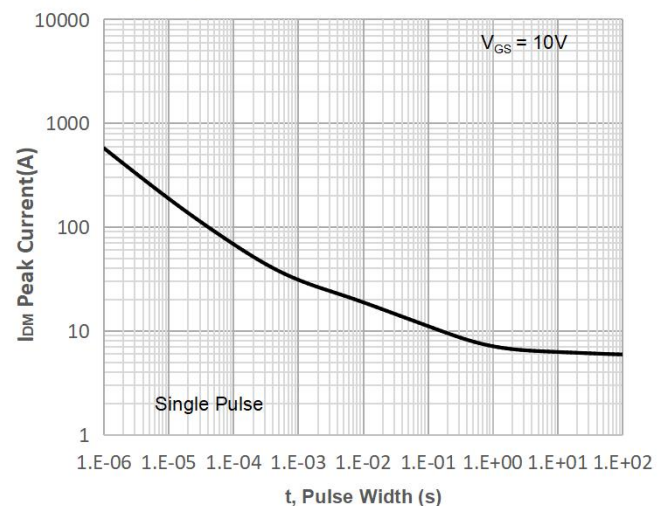


Figure 12: Peak Current Capacity



## Test Circuit

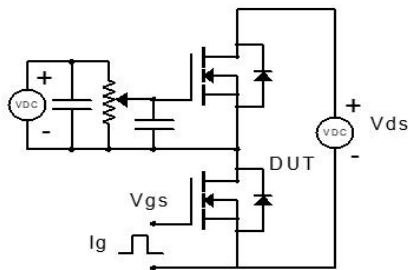


Figure 1: Gate Charge Test Circuit & Waveform



Figure 2: Resistive Switching Test Circuit & Waveform

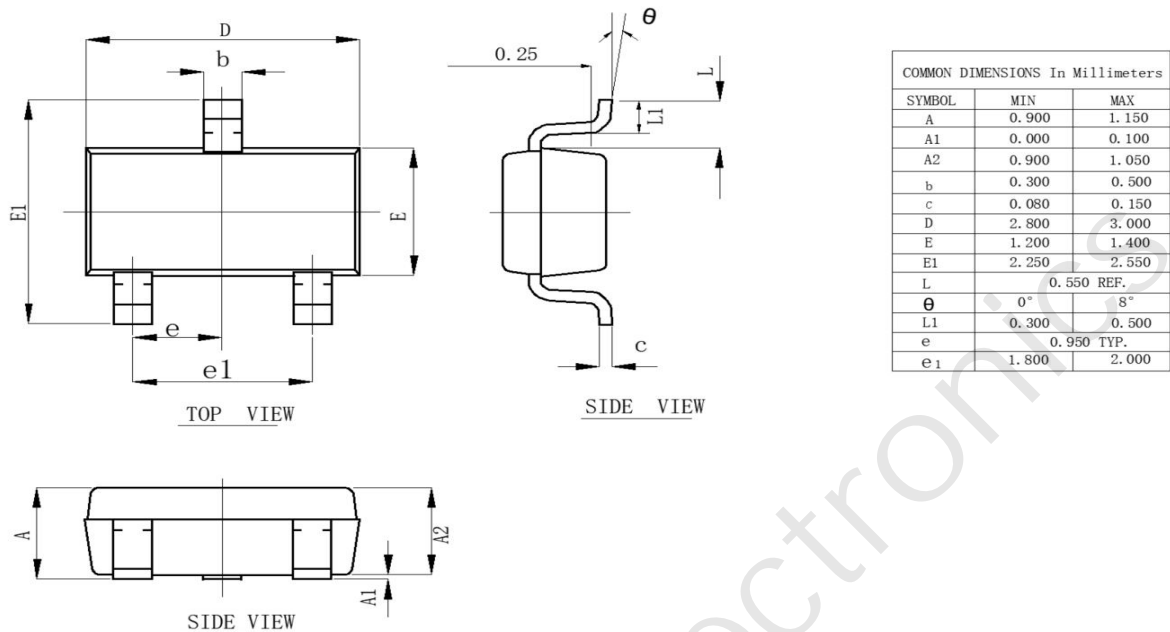


Figure 3: Unclamped Inductive Switching Test Circuit & Waveform



Figure 4: Diode Recovery Test Circuit & Waveform

### Package Mechanical Data(SOT-23)




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