CRMGTL0306A

N-Channel 30V, 4.0mΩ Typ. Power MOSFET

Description

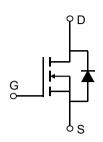
Features

• 30V, 67A

$$R_{DS(ON)}$$
 Typ = 4.0m Ω @ V_{GS} = 10V

$$R_{DS(ON)}$$
 Typ = 6.3m Ω @ V_{GS} = 4.5V

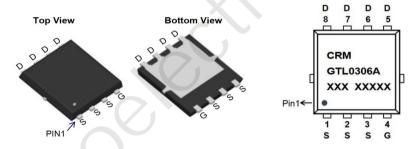
- Advanced Trench Technology
- Excellent R_{DS(ON)} and Low Gate Charge
- 100% UIS TESTED!
- 100% ΔVds TESTED!



Schematic Diagram

Application

- Load Switch
- PWM Application
- Power Management



Marking and Pin Assignment

Package Marking and Ordering Information

Device	Marking	Package	Outline	Reel Size	Reel (pcs)	Per Carton (pcs)
CRMGTL0306A	CRMGTL0306A	PDFN5x6-8L	TAPING	13"	5000	50000

Absolute Maximum Ratings (@ T_J = 25°C unless otherwise specified)

Symbol	Parameter		Value	Units
V_{DS}	Drain-to-Source Voltage		30	V
V _{GS}	Gate-to-Source Voltage		±20	V
	Continuous Drain Current	T _C = 25°C	67	Α
I _D	Continuous Drain Current	T _C = 100°C	40.2	А
I _{DM}	Pulsed Drain Current (1)		268	А
E _{AS}	Single Pulsed Avalanche Energy (2)		76.5	mJ
P_{D}	Power Dissipation	T _C = 25°C	40.3	W
$R_{ heta JC}$	Thermal Resistance, Junction to Case		3.1	°C/W
T_{J} , T_{STG}	Junction & Storage Temperature Range		-55 to 150	°C

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Electrical Characteristics (T_J = 25°C unless otherwise specified)

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Uni
Off Chara	acteristics					
V _{(BR)DSS}	Drain-Source Breakdown Voltage	$I_D = 250 \mu A, V_{GS} = 0 V$	30	-	-	V
I _{DSS}	Zero Gate Voltage Drain Current	$V_{DS} = 30V, V_{GS} = 0V$	-	-	1.0	μΑ
I _{GSS}	Gate-Body Leakage Current	$V_{DS} = 0V, V_{GS} = \pm 20V$	-	-	±100	nA
On Chara	acteristics				6	
$V_{\text{GS(th)}}$	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	1	1.4	2	V
D	Static Drain Source ON Designation of (3)	$V_{GS} = 10V, I_D = 20A$	-	4	5.2	mΩ
$R_{DS(ON)}$	Static Drain-Source ON-Resistance ⁽³⁾	$V_{GS} = 4.5V, I_D = 15A$	-	6.3	8.2	mΩ
Dynamic	Characteristics					
C _{iss}	Input Capacitance		-(1916	-	pF
C_{oss}	Output Capacitance	$V_{GS} = 0V, V_{DS} = 15V,$ f = 1MHz	X - \	217	-	pF
C_{rss}	Reverse Transfer Capacitance	1 - 1101112	- 1	183	-	pF
Q_g	Total Gate Charge		J -	37	-	nC
Q_gs	Gate Source Charge	$V_{GS} = 0 \text{ to } 10V$ $V_{DS} = 15V, I_{D} = 30A$	-	7.4	-	nC
Q_{gd}	Gate Drain("Miller") Charge	V _{DS} = 10V, I _D = 00A	-	8.6	-	nC
Switchin	g Characteristics					
t _{d(on)}	Turn-On DelayTime	.()	-	8.4	-	ns
t _r	Turn-On Rise Time	$V_{GS} = 10V, V_{DD} = 15V$	-	20	-	ns
$t_{\text{d(off)}}$	Turn-Off DelayTime	$I_D = 30A$, $R_{GEN} = 3\Omega$	-	32	-	ns
$t_{\rm f}$	Turn-Off Fall Time		-	9	-	ns
Drain-So	urce Diode Characteristics and M	Max Ratings				
I _S	Maximum Continuous Drain to Source Di	ode Forward Current	-	-	67	Α
I _{SM}	Maximum Pulsed Drain to Source Diode	Forward Current	-	-	268	Α
V_{SD}	Drain to Source Diode Forward Voltage	V _{GS} = 0V, I _S = 20A	-	-	1.2	V
trr	Body Diode Reverse Recovery Time	1 = 204 di/dt = 4004/	-	10.2	-	ns
Qrr	Body Diode Reverse Recovery Charge	$I_F = 20A$, di/dt = 100A/us	-	2.75	-	nC

Notes:

^{1.} Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature.

^{2.} E_{AS} condition: Starting T_J =25°C, V_{DD} =15V, V_G =10V, R_G =25ohm, L=0.5mH, I_{AS} =17.5A

^{3.} Pulse Test: Pulse Width≤300µs, Duty Cycle≤0.5%.

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Test Circuit

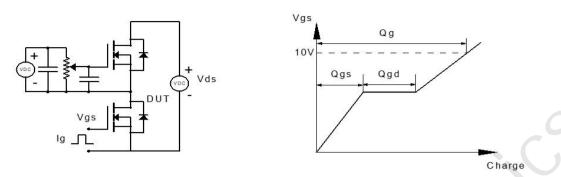


Figure 1: Gate Charge Test Circuit & Waveform

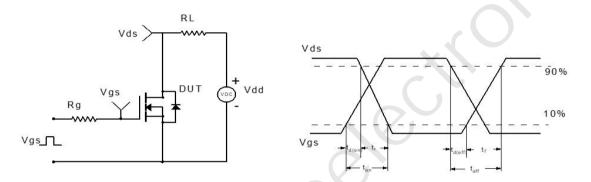


Figure 2: Resistive Switching Test Circuit & Waveform

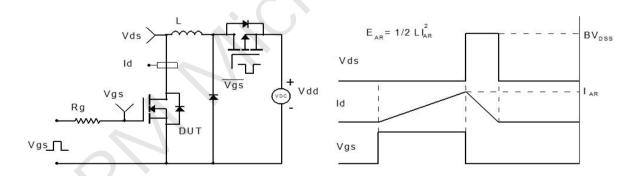


Figure 3: Unclamped Inductive Switching Test Circuit& Waveform

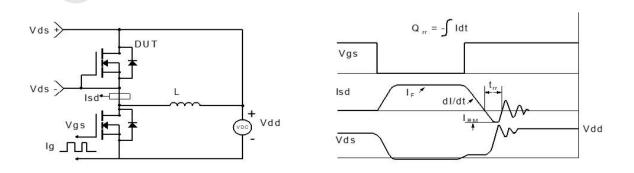
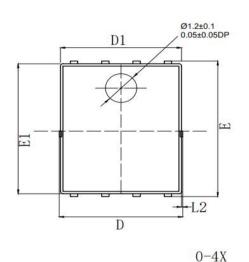


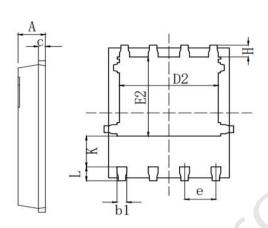
Figure 4: Diode Recovery Test Circuit & Waveform

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Package Mechanical Data(PDFN5x6-8L)





SYMBOL	MILLIMETER				
	MIN	NOM	MAX		
A	0.90	1.00	1.10		
ь	0. 25	0.30	0. 35		
b1	0.30	0.40	0.45		
с	0. 22	0. 25	0. 28		
D			5. 30		
D1	4. 90	5.05	5. 20		
D2	3. 90REF				
Е	6.00	6. 15	6. 30		
E1	5. 70	5. 85	6. 00		
E2	3. 50REF				
е	1.10	1. 27	1.40		
Н	0. 51	0.61	0.71		
K	1.10		-		
L	0. 51	0.61	0.71		
L2			0.10		
Ф	8°		12°		



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