CRMYP5N25A

N-Channel 250V, 480mΩ Typ. Power MOSFET

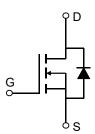
Description

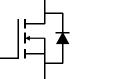
Features

• 250V, 3A

 $R_{DS(ON)}$ Typ = 480m Ω @ V_{GS} = 10V

- Fast Switching
- Improved dv/dt Capability
- Lead Free

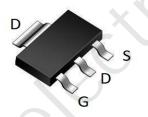


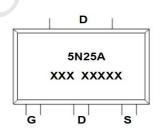


Schematic Diagram

Application

- Switch Mode Power Supply(SMPS)
- Uninterruptible Power





Marking and Pin Assignment

Package Marking and Ordering Information

Device	Marking	Package	Outline	Reel Size	Reel (pcs)	Per Carton (pcs)
CRMYP5N25A	5N25A	SOT-223-3L	TAPING	13"	4000	48000

Absolute Maximum Ratings (@ $T_J = 25^{\circ}C$ unless otherwise specified)

Symbol	Parameter	Value	Units	
V_{DS}	Drain-to-Source Voltage	250	V	
V_{GS}	Gate-to-Source Voltage	±30	V	
	Continuous Drain Current	T _A = 25°C	3	Α
I _D	Continuous Diain Current	T _A = 100°C	1.8	А
I _{DM}	Pulsed Drain Current (1)		12	А
P_{D}	Power Dissipation	T _A = 25°C	8.9	W
$R_{ heta JA}$	Thermal Resistance, Junction to Ambient ⁽²⁾		14	°C/W
T_J, T_STG	Junction & Storage Temperature Range		-55 to 150	°C

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Electrical Characteristics (T_J = 25°C unless otherwise specified)

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
Off Chara	acteristics					
$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	$I_D = 250 \mu A, V_{GS} = 0 V$	250	-	-	V
I _{DSS}	Zero Gate Voltage Drain Current	$V_{DS} = 250V, V_{GS} = 0V$	-	-	1.0	μА
I _{GSS}	Gate-Body Leakage Current	$V_{DS} = 0V, V_{GS} = \pm 30V$	-	-	±100	nA
On Chara	acteristics				5	
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	2.5	3.0	3.5	V
R _{DS(ON)}	Static Drain-Source ON-Resistance ⁽³⁾	V _{GS} = 10V, I _D = 1.5A	-	480	576	mΩ
Dynamic	Characteristics					
C _{iss}	Input Capacitance		-	465	-	pF
C_{oss}	Output Capacitance	$V_{GS} = 0V, V_{DS} = 25V,$ f = 1MHz		68	-	pF
C_{rss}	Reverse Transfer Capacitance	1 – 1101112	X -	9.5	-	pF
Q_g	Total Gate Charge	(-	10	-	nC
Q_gs	Gate Source Charge	$V_{GS} = 0 \text{ to } 10V$ $V_{DS} = 200V, I_{D} = 3A$	U -	3	-	nC
Q_{gd}	Gate Drain("Miller") Charge	V _{DS} - 200 V, I _D - 3A	-	5.2	-	nC
Switchin	g Characteristics					
t _{d(on)}	Turn-On DelayTime		-	6	-	ns
t _r	Turn-On Rise Time	$V_{GS} = 10V, V_{DD} = 150V$	-	25	-	ns
$t_{\text{d(off)}}$	Turn-Off DelayTime	I_D = 3A, R_{GEN} = 25 Ω	-	22	-	ns
t_f	Turn-Off Fall Time		-	24	-	ns
Drain-So	urce Diode Characteristics and M	Max Ratings				
I _S	Maximum Continuous Drain to Source Diode Forward Current		-	-	3	Α
I _{SM}	Maximum Pulsed Drain to Source Diode	Forward Current	-	-	12	Α
V_{SD}	Drain to Source Diode Forward Voltage	$V_{GS} = 0V, I_{S} = 3A$	-	-	1.2	V
trr	Body Diode Reverse Recovery Time	1 - 00 -1:/-14 - 40004	-	423	-	ns
Qrr	Body Diode Reverse Recovery Charge	$I_F = 3A$, di/dt = 100A/us	-	4.3	-	nC

Notes:

^{1.} Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature.

^{2.} R_{BJA} is measured with the device mounted on a 1inch² pad of 2oz copper FR4 PCB

^{3.} Pulse Test: Pulse Width $\!\!\leqslant\! 300\mu s,$ Duty Cycle $\!\!\leqslant\! 0.5\%.$



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Test Circuit

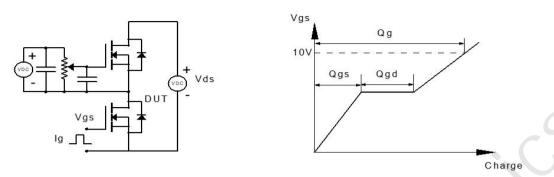


Figure 1: Gate Charge Test Circuit & Waveform

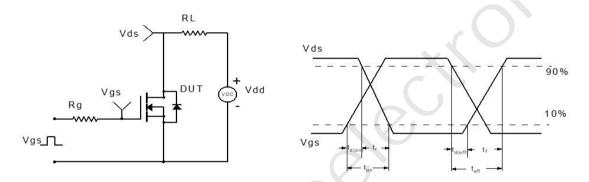


Figure 2: Resistive Switching Test Circuit & Waveform

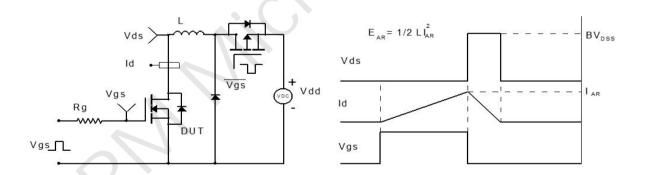


Figure 3: Unclamped Inductive Switching Test Circuit& Waveform

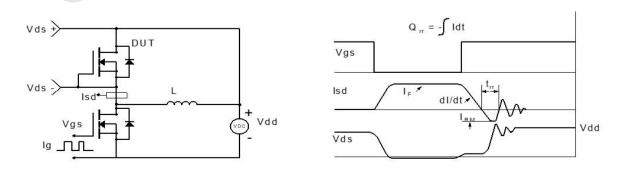
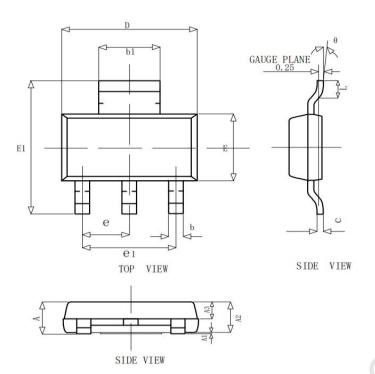


Figure 4: Diode Recovery Test Circuit & Waveform

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Package Mechanical Data(SOT-223-3L)



COMMON DIMENSIONS (UNITS OF MEASURE=mm)

SYMBOL	MIN	NOM	MAX		
Α			1.80		
A1	0.00	0.05	0.10		
A2	1.50	1.60	1.70		
A3	0.85	0.90	0.95		
b	0.66	0.70	0.80		
b1	2.96	3.00	3.10		
С	0.25	0.30	0.35		
D	6.30	6.50	6.70		
E	3.30	3.50	3.70		
E1	6.80	7.00	7.20		
е	2.3BSC				
e1	4.40	4.60	4.80		
L	0.90		1.15		
θ	0°	5°	10°		

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