

Description

Features

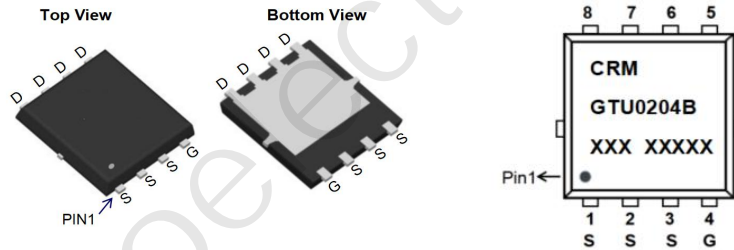
- 20V, 80A
- $R_{DS(ON)}$ Typ = 2.8mΩ @ $V_{GS} = 4.5V$
- $R_{DS(ON)}$ Typ = 3.9mΩ @ $V_{GS} = 2.5V$
- Advanced Trench Technology
- Excellent $R_{DS(ON)}$ and Low Gate Charge
- 100% UIS TESTED!
- 100% ΔV_{ds} TESTED!



Schematic Diagram

Application

- Load Switch
- PWM Application
- Power Management



Marking and Pin Assignment

Package Marking and Ordering Information

Device	Marking	Package	Outline	Reel Size	Reel (pcs)	Per Carton (pcs)
CRMGTU0204B	CRMGTU0204B	PDFN5x6-8L	TAPING	13"	5000	50000

Absolute Maximum Ratings (@ $T_J = 25^\circ C$ unless otherwise specified)

Symbol	Parameter	Value	Units
V_{DS}	Drain-to-Source Voltage	20	V
V_{GS}	Gate-to-Source Voltage	±12	V
I_D	Continuous Drain Current	$T_C = 25^\circ C$	80
		$T_C = 100^\circ C$	48
I_{DM}	Pulsed Drain Current ⁽¹⁾	320	A
E_{AS}	Single Pulsed Avalanche Energy ⁽²⁾	110	mJ
P_D	Power Dissipation	$T_C = 25^\circ C$	40
$R_{\theta JC}$	Thermal Resistance, Junction to Case	3.1	°C/W
T_J, T_{STG}	Junction & Storage Temperature Range	-55 to 150	°C

Electrical Characteristics (T_J = 25°C unless otherwise specified)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
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Off Characteristics

V _{(BR)DSS}	Drain-Source Breakdown Voltage	I _D = 250μA, V _{GS} = 0V	20	-	-	V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = 20V, V _{GS} = 0V	-	-	1.0	μA
I _{GSS}	Gate-Body Leakage Current	V _{DS} = 0V, V _{GS} = ±12V	-	-	±100	nA

On Characteristics

V _{GS(th)}	Gate Threshold Voltage	V _{DS} = V _{GS} , I _D = 250μA	0.4	0.7	1.2	V
R _{DS(ON)}	Static Drain-Source ON-Resistance ⁽³⁾	V _{GS} = 4.5V, I _D = 20A	-	2.8	3.6	mΩ
		V _{GS} = 2.5V, I _D = 10A	-	3.9	5	mΩ

Dynamic Characteristics

C _{iss}	Input Capacitance	V _{GS} = 0V, V _{DS} = 10V, f = 1MHz	-	3266	-	pF
C _{oss}	Output Capacitance		-	402	-	pF
C _{rss}	Reverse Transfer Capacitance		-	367	-	pF
Q _g	Total Gate Charge	V _{GS} = 0 to 10V V _{DS} = 10V, I _D = 30A	-	60	-	nC
Q _{gs}	Gate Source Charge		-	7	-	nC
Q _{gd}	Gate Drain("Miller") Charge		-	11	-	nC

Switching Characteristics

t _{d(on)}	Turn-On DelayTime	V _{GS} = 10V, V _{DD} = 10V I _D = 30A, R _{GEN} = 3Ω	-	7	-	ns
t _r	Turn-On Rise Time		-	17	-	ns
t _{d(off)}	Turn-Off DelayTime		-	67	-	ns
t _f	Turn-Off Fall Time		-	73	-	ns

Drain-Source Diode Characteristics and Max Ratings

I _S	Maximum Continuous Drain to Source Diode Forward Current	V _{GS} = 0V, I _S = 20A	-	-	80	A
I _{SM}	Maximum Pulsed Drain to Source Diode Forward Current		-	-	320	A
V _{SD}	Drain to Source Diode Forward Voltage		-	-	1.2	V
t _{rr}	Body Diode Reverse Recovery Time		-	15	-	ns
Q _{rr}	Body Diode Reverse Recovery Charge		-	5.5	-	nC

- Notes:
1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature.
 2. E_{AS} condition: Starting T_J=25°C, V_{DD}=10V, V_G=10V, R_G=25ohm, L=0.5mH, I_{AS}=21A
 3. Pulse Test: Pulse Width≤300μs, Duty Cycle≤0.5%.

Test Circuit



Figure 1: Gate Charge Test Circuit & Waveform



Figure 2: Resistive Switching Test Circuit & Waveform

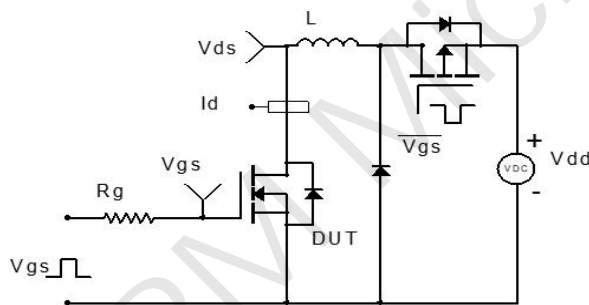
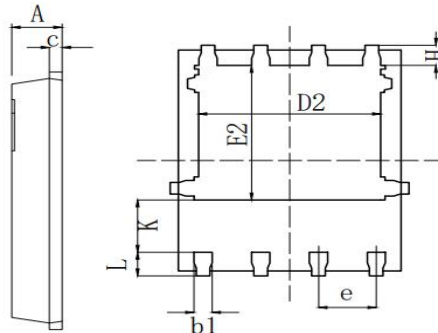
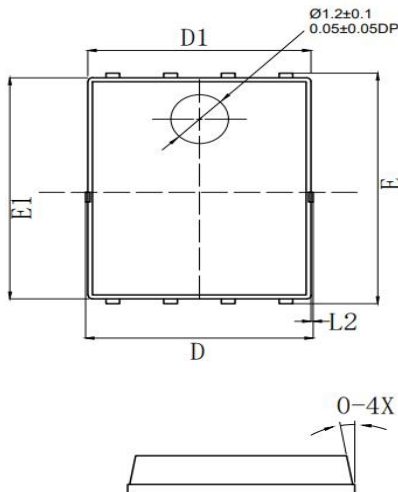


Figure 3: Unclamped Inductive Switching Test Circuit & Waveform



Figure 4: Diode Recovery Test Circuit & Waveform

Package Mechanical Data(PDFN5x6-8L)




SYMBOL	MILLIMETER		
	MIN	NOM	MAX
A	0.90	1.00	1.10
b	0.25	0.30	0.35
b1	0.30	0.40	0.45
c	0.22	0.25	0.28
D	—	—	5.30
D1	4.90	5.05	5.20
D2	3.90REF		
E	6.00	6.15	6.30
E1	5.70	5.85	6.00
E2	3.50REF		
e	1.10	1.27	1.40
H	0.51	0.61	0.71
K	1.10	—	—
L	0.51	0.61	0.71
L2	—	—	0.10
Φ	8°	—	12°

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