CRMGTU0209A

N-Channel 20V, 6.4mΩ Typ. Power MOSFET

Description

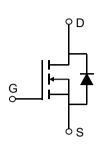
Features

• 20V, 40A

$$R_{DS(ON)}$$
 Typ = 6.4m Ω @ V_{GS} = 4.5V

$$R_{DS(ON)}$$
 Typ = 9.0m Ω @ V_{GS} = 2.5 V

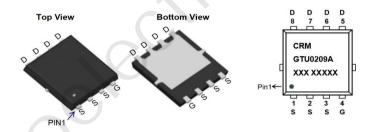
- Advanced Trench Technology
- Excellent R_{DS(ON)} and Low Gate Charge
- 100% UIS TESTED!
- 100% ΔVds TESTED!





Application

- Load Switch
- PWM Application
- Power Management



Marking and Pin Assignment

Initial Version: 1.0

Package Marking and Ordering Information

Device	Marking	Package	Outline	Reel Size	Reel (pcs)	Per Carton (pcs)
CRMGTU0209A	CRMGTU0209A	PDFN5x6-8L	TAPING	13"	5000	50000

Absolute Maximum Ratings (@ $T_J = 25^{\circ}C$ unless otherwise specified)

Symbol	Parameter		Value	Units
V_{DS}	Drain-to-Source Voltage		20	V
V_{GS}	Gate-to-Source Voltage		±12	V
	Continuous Drain Current	T _C = 25°C	40	А
I _D	- Continuous Drain Current	T _C = 100°C	24	А
I _{DM}	Pulsed Drain Current (1)		160	Α
E _{AS}	Single Pulsed Avalanche Energy (2)		30	mJ
P_{D}	Power Dissipation	T _C = 25°C	19.2	W
$R_{ hetaJC}$	Thermal Resistance, Junction to Case		6.5	°C/W
T_J,T_STG	Junction & Storage Temperature Range		-55 to 150	°C



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Electrical Characteristics (T_J = 25°C unless otherwise specified)

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Uni
Off Chara	acteristics					
V _{(BR)DSS}	Drain-Source Breakdown Voltage	$I_D = 250 \mu A, V_{GS} = 0 V$	20	-	-	V
I _{DSS}	Zero Gate Voltage Drain Current	$V_{DS} = 20V, V_{GS} = 0V$	-	-	1.0	μΑ
I _{GSS} Gate-Body Leakage Current		$V_{DS} = 0V, V_{GS} = \pm 12V$	-	-	±100	nA
On Chara	acteristics				6	
$V_{\text{GS(th)}}$	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	0.4	0.7	1.2	V
D	Static Drain-Source ON-Resistance ⁽³⁾	$V_{GS} = 4.5V, I_D = 15A$	-	6.4	8.3	mΩ
$R_{DS(ON)}$		$V_{GS} = 2.5V, I_D = 10A$	-	9.0	11.7	mΩ
Dynamic	Characteristics					
C_{iss}	Input Capacitance			1055	-	pF
C_{oss}	Output Capacitance	$V_{GS} = 0V, V_{DS} = 10V,$ f = 1MHz	X-\	160	-	pF
C_{rss}	Reverse Transfer Capacitance	1 - 1101112		140	-	pF
Q_g	Total Gate Charge		J -	13	-	nC
Q_gs	Gate Source Charge	$V_{GS} = 0 \text{ to } 4.5V$ $V_{DS} = 10V, I_D = 15A$	-	2.5	-	nC
Q_{gd}	Gate Drain("Miller") Charge	VDS = 10 V, 10 = 10/1	-	3.5	-	nC
Switchin	g Characteristics					
$t_{d(on)}$	Turn-On DelayTime	.rO	-	8	-	ns
t_r	Turn-On Rise Time	$V_{GS} = 4.5V, V_{DD} = 10V$	-	19	-	ns
$t_{\text{d(off)}}$	Turn-Off DelayTime	$I_D = 15A$, $R_{GEN} = 3\Omega$	-	30	-	ns
t _f	Turn-Off Fall Time	>		11	<u>-</u>	ns
Drain-So	urce Diode Characteristics and M	Max Ratings				
Is	Maximum Continuous Drain to Source Di	ode Forward Current	-	-	40	Α
I _{SM}	Maximum Pulsed Drain to Source Diode	Forward Current	-	-	160	Α
V_{SD}	Drain to Source Diode Forward Voltage	V _{GS} = 0V, I _S = 10A	-	-	1.2	V
trr	Body Diode Reverse Recovery Time		-	7.5	-	ns
Qrr	Body Diode Reverse Recovery Charge	$I_F = 15A$, di/dt = 100A/us	_	1.5	-	nC

Notes:

^{1.} Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature.

^{2.} E_{AS} condition: Starting T_J =25°C, V_{DD} =10V, V_G =10V, R_G =25ohm, L=0.5mH, I_{AS} =11A

^{3.} Pulse Test: Pulse Width≤300µs, Duty Cycle≤0.5%.

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Test Circuit

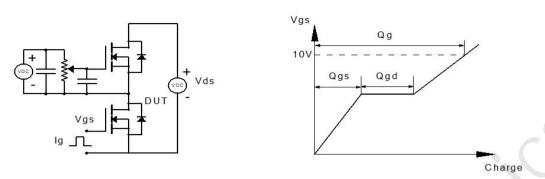


Figure 1: Gate Charge Test Circuit & Waveform

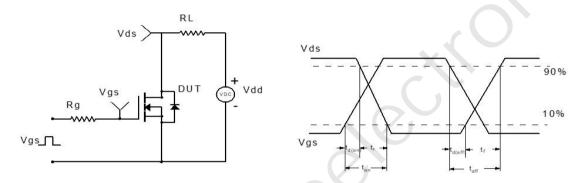


Figure 2: Resistive Switching Test Circuit & Waveform

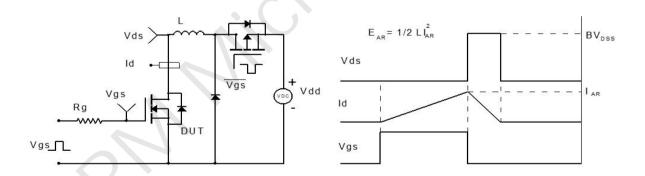


Figure 3: Unclamped Inductive Switching Test Circuit& Waveform

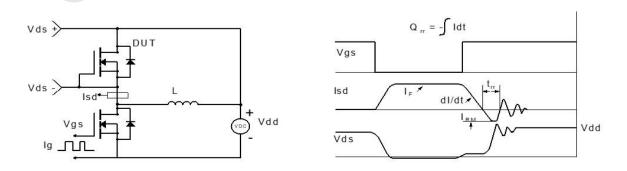
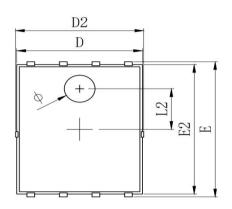


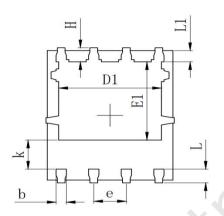
Figure 4: Diode Recovery Test Circuit & Waveform

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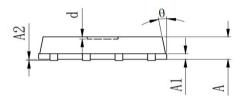
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Package Mechanical Data(PDFN5x6-8L)





SYMBOL	MILLIMETER				
SIMBUL	MIN	Тур.	MAX		
Α	0. 900	1.000	1. 100		
A1	0.254 REF.				
A2	0~0.05				
D	4. 824	4.900	4. 976		
D1	3. 910	4. 010	4. 110		
D2	4. 924	5. 000	5. 076		
Е	5. 924	6.000	6.076		
E1	3. 375	3. 475	3. 575		
E2	5. 674	5. 750	5. 826		
b	0. 350	0. 400	0. 450		
e	1.270 TYP.				
L	0. 534	0.610	0.686		
L1	0. 424	0.500	0. 576		
L2	1.800 REF.				
k	1. 190	1. 290	1. 390		
Н	0. 549	0.625	0.701		
θ	8°	10°	12°		
ф	1.100	1.200	1.300		
d		23	0. 100		



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