CRMSGH1505A

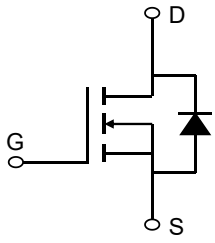
N-Channel 150V, 4.4mΩ Typ. Power MOSFET

Description

Features

- 150V, 150A

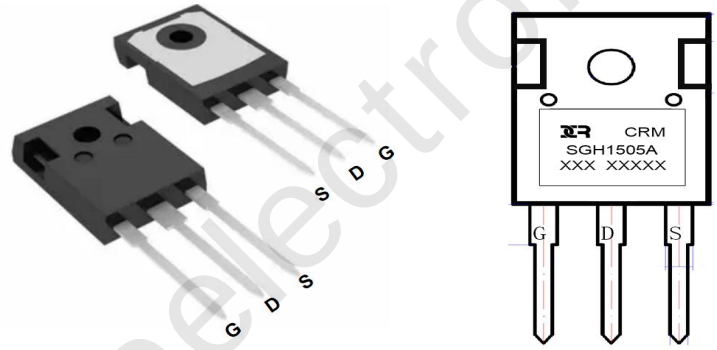
  $R_{DS(ON)}$ Typ = 4.4mΩ @ V_{GS} = 10V
- Advanced Split Gate Trench Technology
- Excellent $R_{DS(ON)}$ and Low Gate Charge
- 100% UIS TESTED!
- 100% ΔVds TESTED!



Schematic Diagram

Application

- Load Switch
- PWM Application
- Power Management



Marking and Pin Assignment

Package Marking and Ordering Information

Device	Marking	Package	Outline	TUBE(pcs)	Inner Box(pcs)	Per Carton (pcs)
CRMSGH1505A	CRMSGH1505A	TO-247-3L	TUBE	25	1000	2000

Absolute Maximum Ratings (@ T_J = 25°C unless otherwise specified)

Symbol	Parameter		Value	Units
V_{DS}	Drain-to-Source Voltage		150	V
V_{GS}	Gate-to-Source Voltage		±20	V
I_D	Continuous Drain Current	T_C = 25°C	150	A
		T_C = 100°C	90	A
I_{DM}	Pulsed Drain Current (1)		600	A
E_{AS}	Single Pulsed Avalanche Energy (2)		870	mJ
P_D	Power Dissipation	T_C = 25°C	312	W
$R_{\theta JC}$	Thermal Resistance, Junction to Case		0.4	°C/W
T_J, T_{STG}	Junction & Storage Temperature Range		-55 to 150	°C

Electrical Characteristics (T_J = 25°C unless otherwise specified)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
Off Characteristics						
$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	I_D = 250μA, V_{GS} = 0V	150	-	-	V
I_{DSS}	Zero Gate Voltage Drain Current	V_{DS} = 150V, V_{GS} = 0V	-	-	1.0	μA
I_{GSS}	Gate-Body Leakage Current	V_{DS} = 0V, V_{GS} = ±20V	-	-	±100	nA
On Characteristics						
$V_{GS(th)}$	Gate Threshold Voltage	V_{DS} = V_{GS}, I_D = 250μA	2.2	3	3.8	V
$R_{DS(ON)}$	Static Drain-Source ON-Resistance(3)	V_{GS} = 10V, I_D = 30A	-	4.4	5.7	mΩ
Dynamic Characteristics						
C_{iss}	Input Capacitance	V_{GS} = 0V, V_{DS} = 25V, f = 1MHz	-	5087	-	pF
C_{oss}	Output Capacitance		-	3682	-	pF
C_{rss}	Reverse Transfer Capacitance		-	180	-	pF
Q_g	Total Gate Charge	V_{GS} = 0 to 10V V_{DS} = 75V, I_D = 20A	-	80	-	nC
Q_{gs}	Gate Source Charge		-	30	-	nC
Q_{gd}	Gate Drain("Miller") Charge		-	15	-	nC
Switching Characteristics						
$t_{d(on)}$	Turn-On DelayTime	V_{GS} = 10V, V_{DD} = 75V I_D= 20A, R_{GEN} = 6Ω	-	50	-	ns
t_r	Turn-On Rise Time		-	89	-	ns
$t_{d(off)}$	Turn-Off DelayTime		-	93	-	ns
t_f	Turn-Off Fall Time		-	58	-	ns
Drain-Source Diode Characteristics and Max Ratings						
I_S	Maximum Continuous Drain to Source Diode Forward Current		-	-	150	A
I_{SM}	Maximum Pulsed Drain to Source Diode Forward Current		-	-	600	A
V_{SD}	Drain to Source Diode Forward Voltage	V_{GS} = 0V, I_S = 30A	-	-	1.2	V
trr	Body Diode Reverse Recovery Time	I_F = 15A, di/dt = 100A/us	-	120	-	ns
Qrr	Body Diode Reverse Recovery Charge		-	250	-	nC

Notes:

1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature.
2. E_{AS} condition: Starting T_J=25°C, V_{DD}=50V, V_G=10V, R_G=25ohm, L=0.5mH, I_{AS}=59A
3 Pulse Test: Pulse Width≤300μs, Duty Cycle≤0.5%.

Typical Performance Characteristics

Figure 1: Output Characteristics

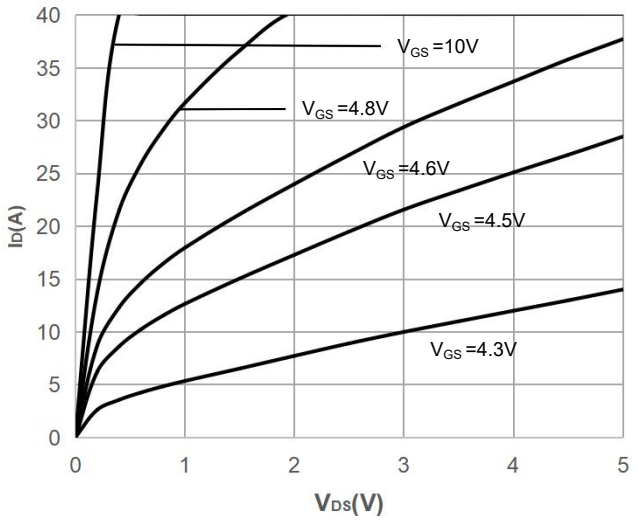


Figure 2: Typical Transfer Characteristics

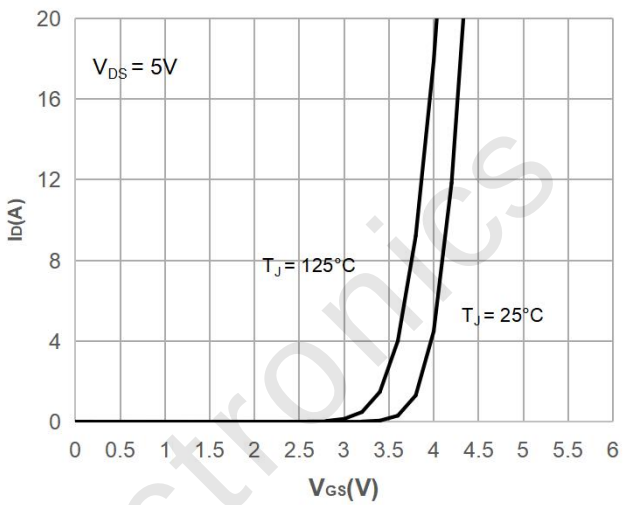


Figure 3: On-resistance vs. Drain Current

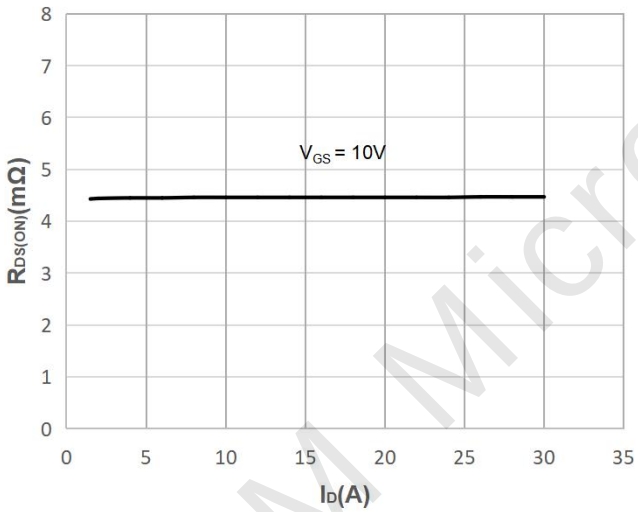


Figure 4: Body Diode Characteristics

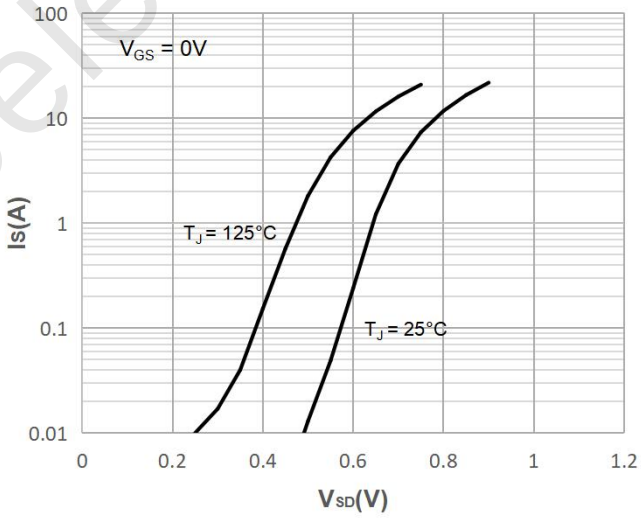


Figure 5: Gate Charge Characteristics

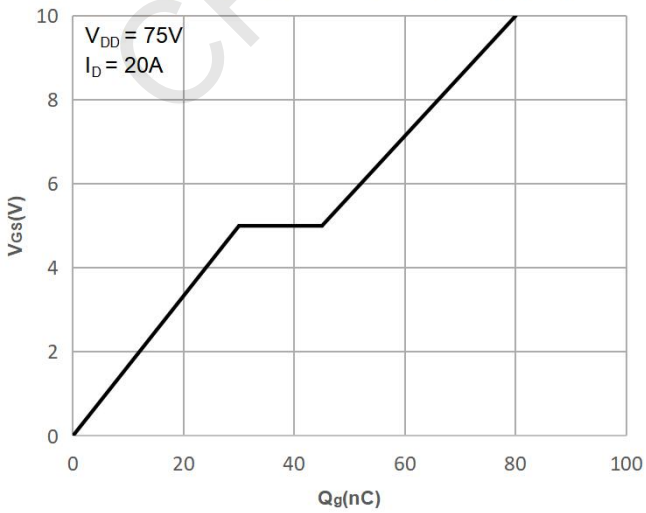
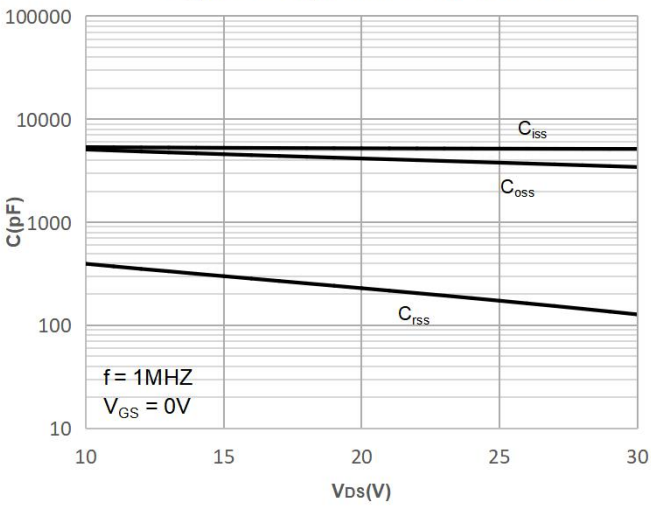


Figure 6: Capacitance Characteristics



Typical Performance Characteristics

Figure 7: Normalized Breakdown voltage vs. Junction Temperature

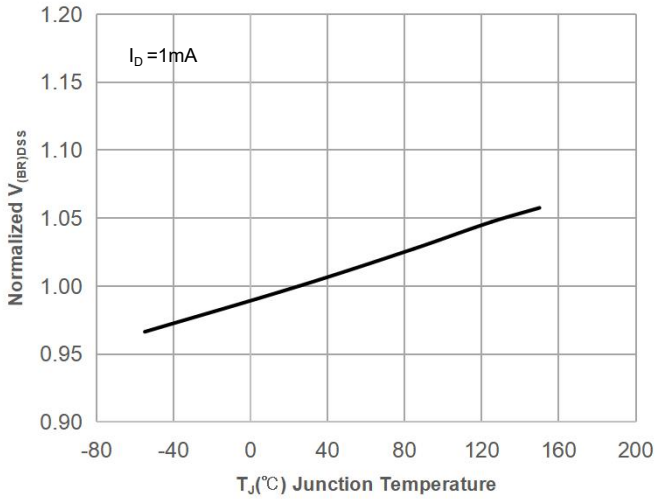


Figure 8: Normalized on Resistance vs. Junction Temperature

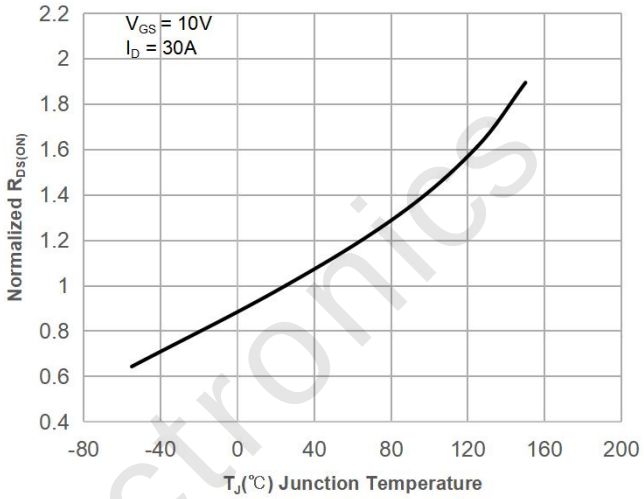


Figure 9: Maximum Safe Operating Area

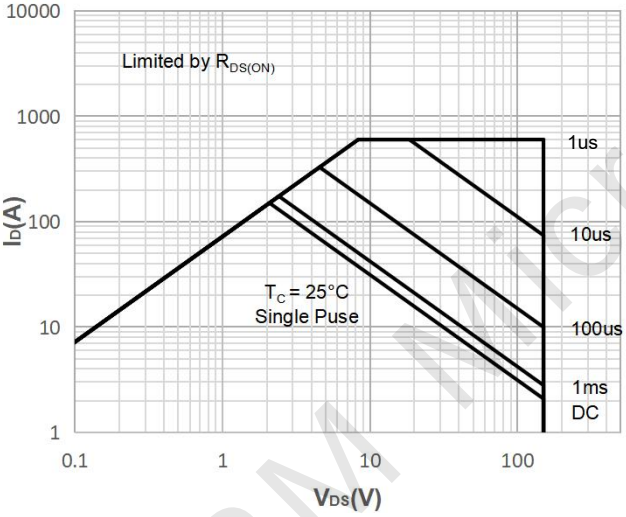


Figure 10: Maximum Continuous Drian Current vs. Case Temperature

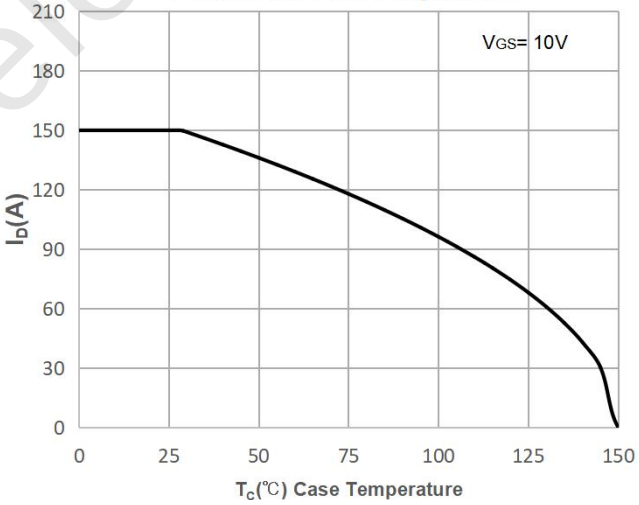


Figure 11: Normalized Maximum Transient Thermal Impedance

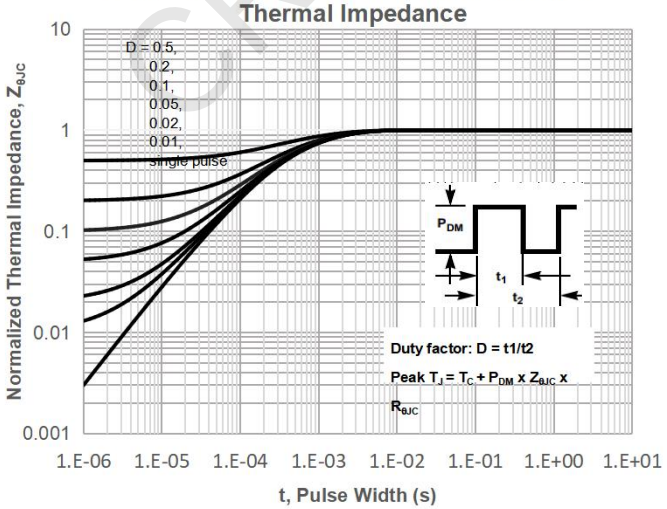
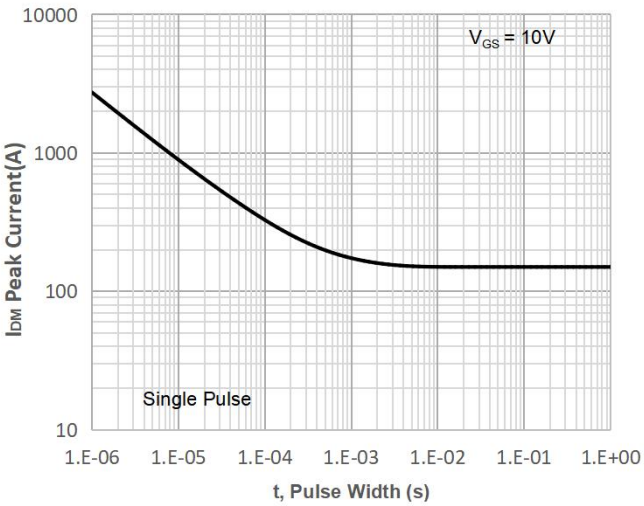


Figure 12: Peak Current Capacity



Test Circuit

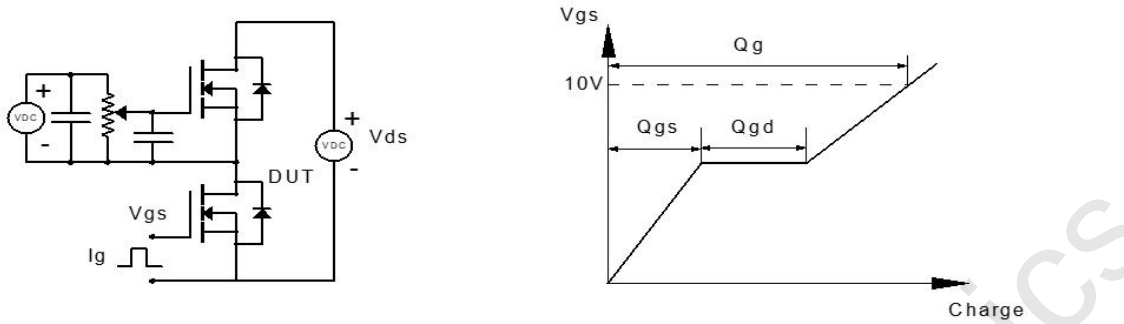


Figure 1: Gate Charge Test Circuit & Waveform

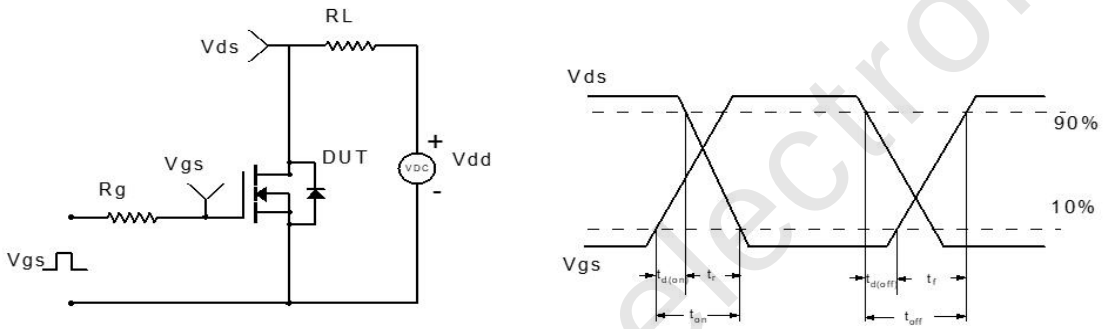


Figure 2: Resistive Switching Test Circuit & Waveform

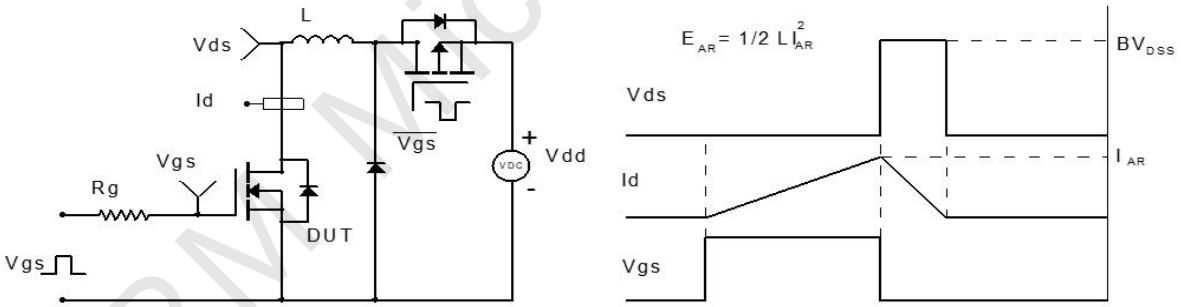


Figure 3: Unclamped Inductive Switching Test Circuit& Waveform

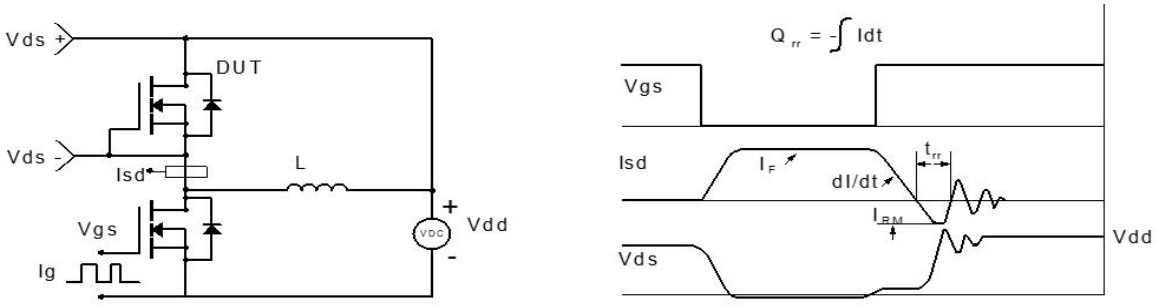
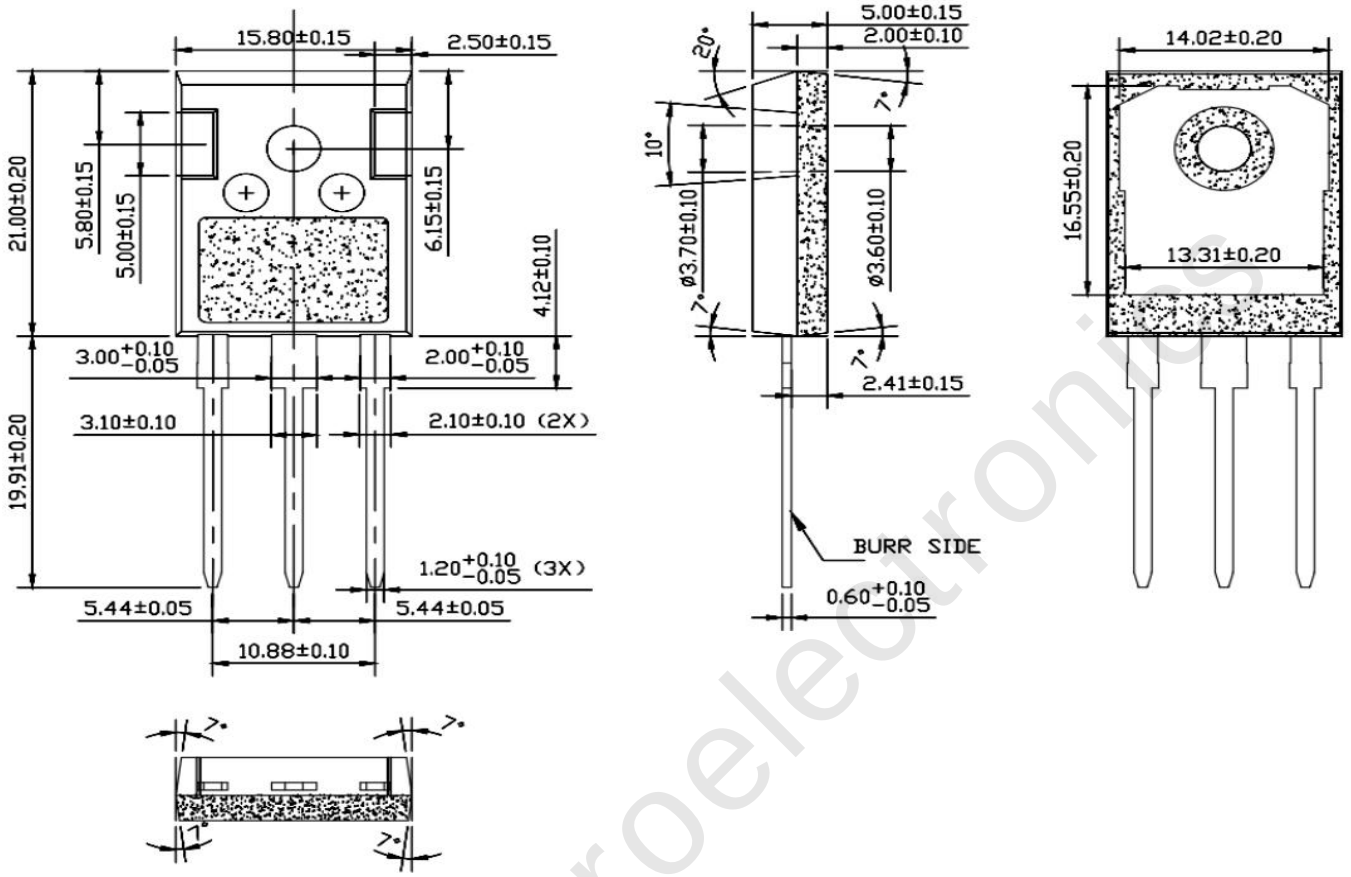


Figure 4: Diode Recovery Test Circuit & Waveform

Package Mechanical Data(TO-247-3L)



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