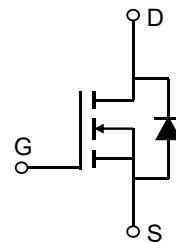


Description

Features

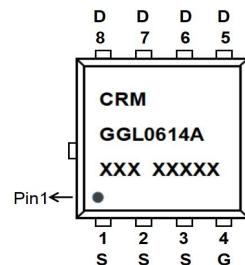
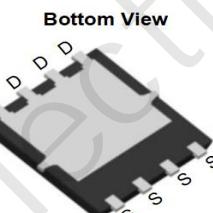
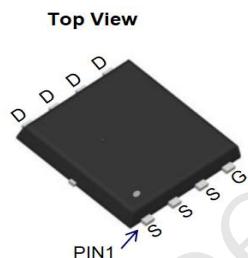
- 60V, 40A
- $R_{DS(ON)}$ Typ = 9mΩ @ V_{GS} = 10V
- $R_{DS(ON)}$ Typ = 12mΩ @ V_{GS} = 4.5V
- Advanced Split Gate Trench Technology
- Excellent $R_{DS(ON)}$ and Low Gate Charge
- 100% UIS TESTED!
- 100% ΔV_{ds} TESTED!



Schematic Diagram

Application

- Load Switch
- PWM Application
- Power Management



Marking and Pin Assignment

Package Marking and Ordering Information

Device	Marking	Package	Outline	Reel Size	Reel (pcs)	Per Carton (pcs)
CRMGGGL0614A	CRMGGGL0614A	PDFN5x6-8L	TAPING	13"	5000	50000

Absolute Maximum Ratings (@ T_J = 25°C unless otherwise specified)

Symbol	Parameter	Value	Units
V_{DS}	Drain-to-Source Voltage	60	V
V_{GS}	Gate-to-Source Voltage	± 20	V
I_D	Continuous Drain Current $T_C = 25^\circ\text{C}$	40	A
	$T_C = 100^\circ\text{C}$	25	A
I_{DM}	Pulsed Drain Current ⁽¹⁾	160	A
E_{AS}	Single Pulsed Avalanche Energy ⁽²⁾	49	mJ
P_D	Power Dissipation $T_C = 25^\circ\text{C}$	33	W
$R_{\theta JC}$	Thermal Resistance, Junction to Case	3.8	°C/W
T_J, T_{STG}	Junction & Storage Temperature Range	-55 to 150	°C

Electrical Characteristics ($T_J = 25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
Off Characteristics						
$V_{(\text{BR})\text{DSS}}$	Drain-Source Breakdown Voltage	$I_D = 250\mu\text{A}, V_{GS} = 0\text{V}$	60	-	-	V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS} = 60\text{V}, V_{GS} = 0\text{V}$	-	-	1.0	μA
I_{GSS}	Gate-Body Leakage Current	$V_{DS} = 0\text{V}, V_{GS} = \pm 20\text{V}$	-	-	± 100	nA
On Characteristics						
$V_{GS(\text{th})}$	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_D = 250\mu\text{A}$	1	1.45	2.5	V
$R_{\text{DS(ON)}}$	Static Drain-Source ON-Resistance ⁽³⁾	$V_{GS} = 10\text{V}, I_D = 20\text{A}$	-	9	12	$\text{m}\Omega$
		$V_{GS} = 4.5\text{V}, I_D = 10\text{A}$	-	12	16	$\text{m}\Omega$
Dynamic Characteristics						
C_{iss}	Input Capacitance		-	915	-	pF
C_{oss}	Output Capacitance	$V_{GS} = 0\text{V}, V_{DS} = 25\text{V}, f = 1\text{MHz}$	-	370	-	pF
C_{rss}	Reverse Transfer Capacitance		-	30	-	pF
Q_g	Total Gate Charge		-	33	-	nC
Q_{gs}	Gate Source Charge	$V_{GS} = 0 \text{ to } 10\text{V}$	-	5.3	-	nC
Q_{gd}	Gate Drain("Miller") Charge	$V_{DS} = 30\text{V}, I_D = 10\text{A}$	-	6.4	-	nC
Switching Characteristics						
$t_{d(on)}$	Turn-On DelayTime		-	9	-	ns
t_r	Turn-On Rise Time	$V_{GS} = 10\text{V}, V_{DD} = 30\text{V}$	-	19.4	-	ns
$t_{d(off)}$	Turn-Off DelayTime	$I_D = 10\text{A}, R_{\text{GEN}} = 4.7\Omega$	-	31.5	-	ns
t_f	Turn-Off Fall Time		-	8.9	-	ns
Drain-Source Diode Characteristics and Max Ratings						
I_S	Maximum Continuous Drain to Source Diode Forward Current		-	-	40	A
I_{SM}	Maximum Pulsed Drain to Source Diode Forward Current		-	-	160	A
V_{SD}	Drain to Source Diode Forward Voltage	$V_{GS} = 0\text{V}, I_S = 10\text{A}$	-	-	1.2	V

Notes:

1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature.

2. E_{AS} condition: Starting $T_J=25^\circ\text{C}$, $V_{DD}=30\text{V}$, $V_G=10\text{V}$, $R_G=25\text{ohm}$, $L=0.5\text{mH}$, $I_{AS}=14\text{A}$

3. Pulse Test: Pulse Width $\leqslant 300\mu\text{s}$, Duty Cycle $\leqslant 0.5\%$.

Typical Performance Characteristics

Figure 1: Output Characteristics

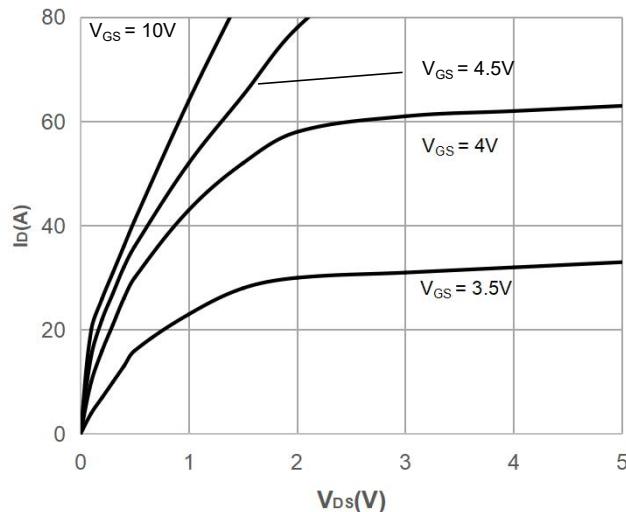


Figure 2: Typical Transfer Characteristics

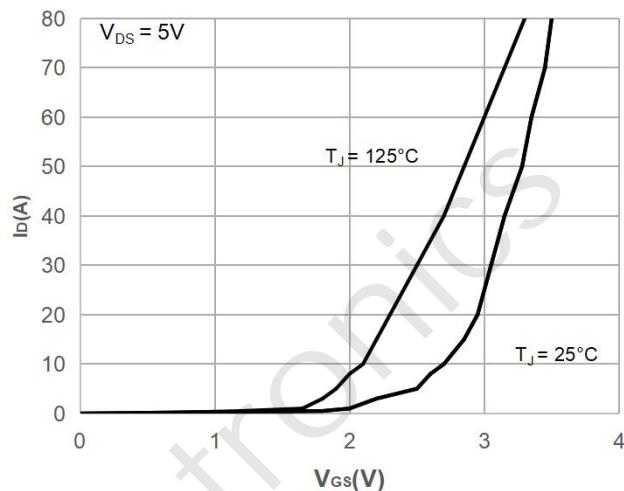


Figure 3: On-resistance vs. Drain Current

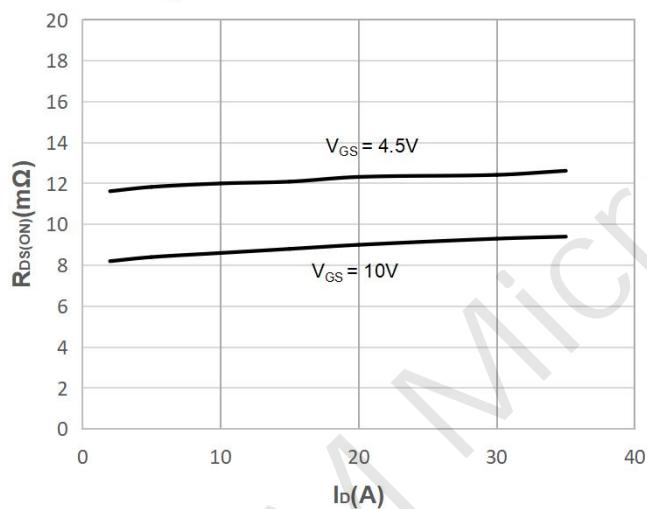


Figure 4: Body Diode Characteristics

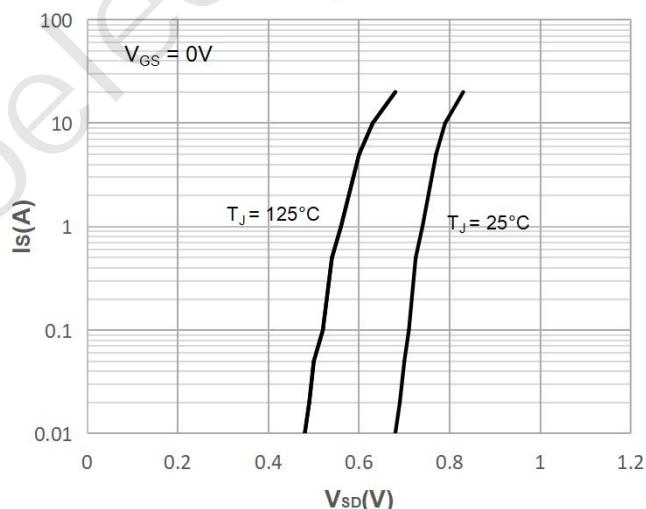


Figure 5: Gate Charge Characteristics

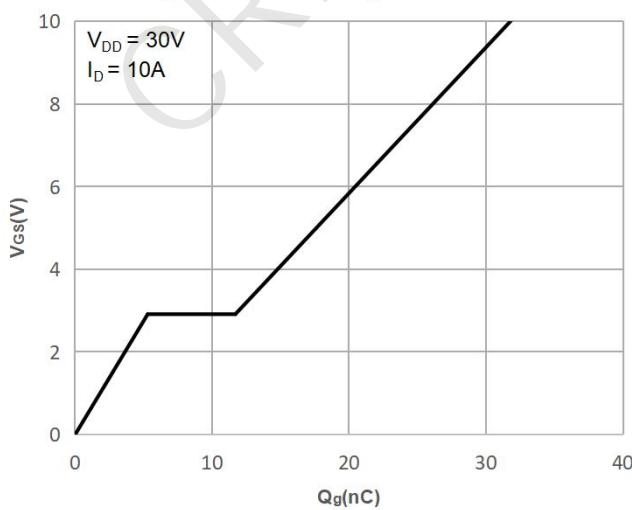
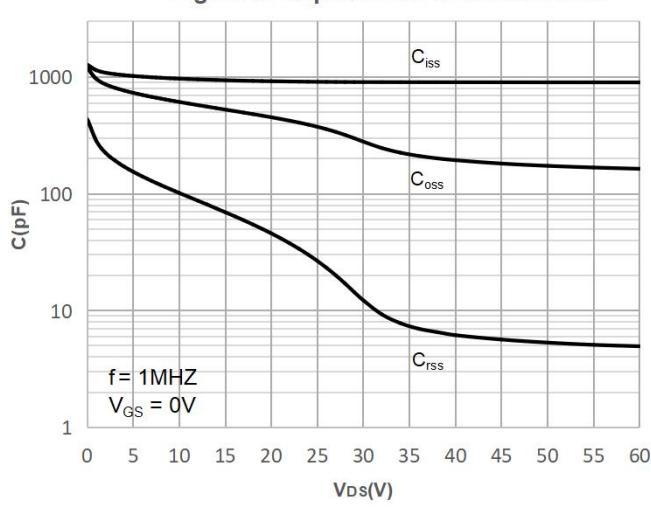


Figure 6: Capacitance Characteristics



Typical Performance Characteristics

Figure 7: Normalized Breakdown voltage vs. Junction Temperature

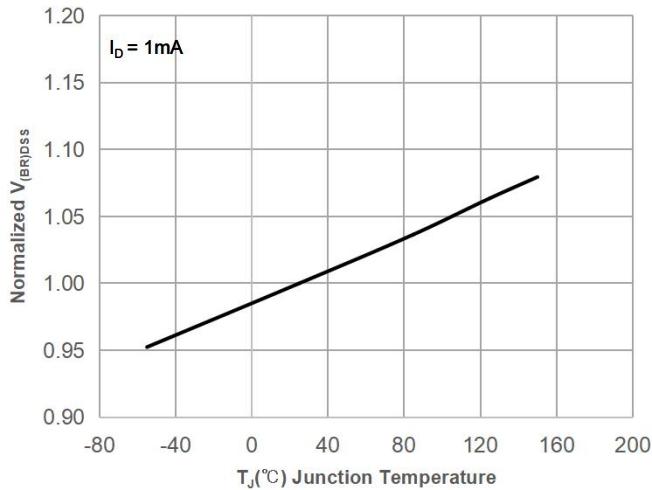


Figure 9: Maximum Safe Operating Area

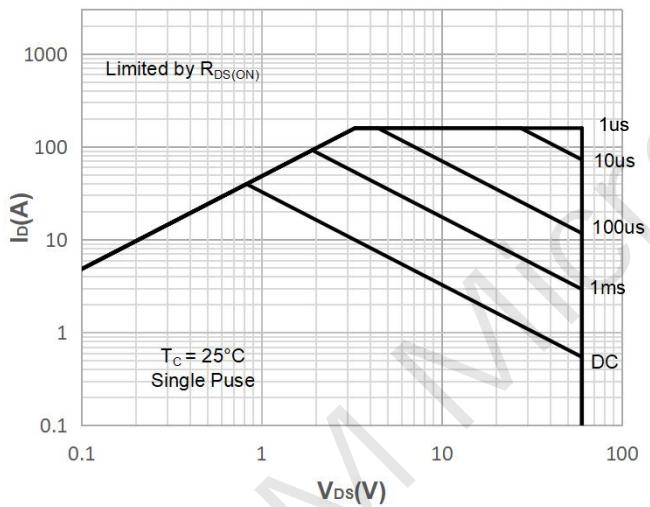


Figure 11: Normalized Maximum Transient Thermal Impedance

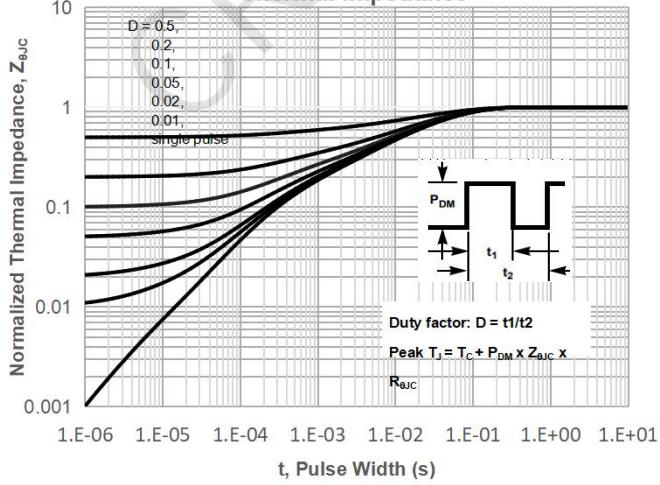


Figure 8: Normalized on Resistance vs. Junction Temperature

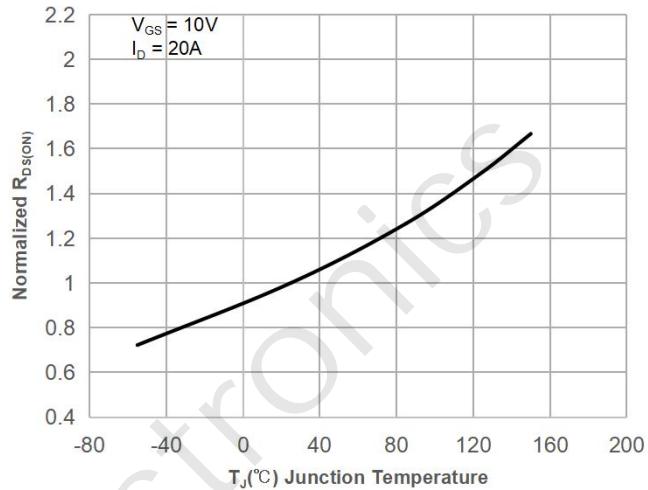


Figure 10: Maximum Continuous Drain Current vs. Case Temperature

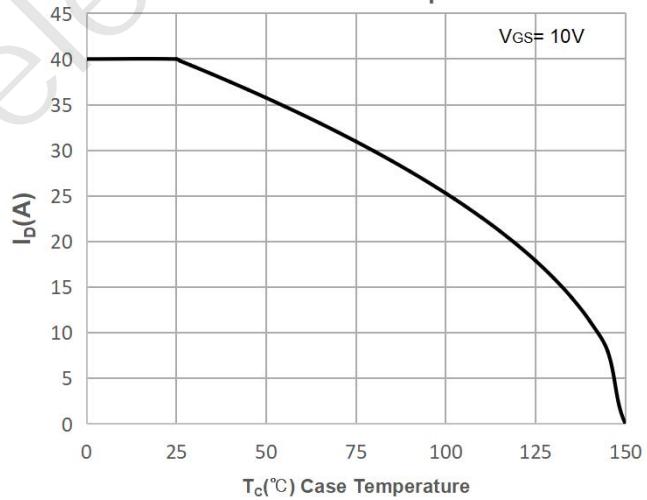
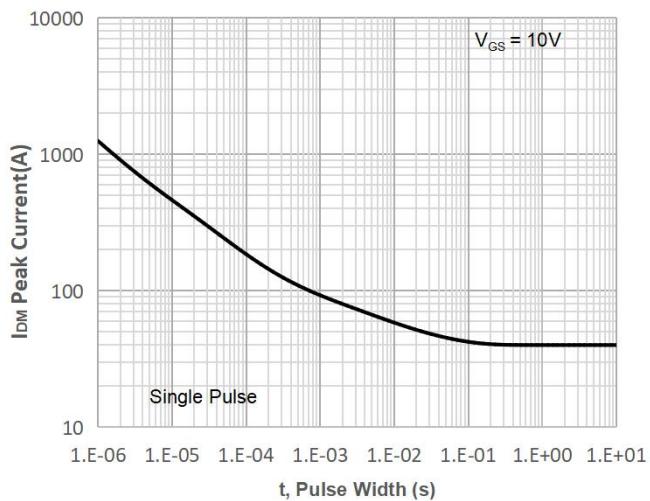


Figure 12: Peak Current Capacity



Test Circuit

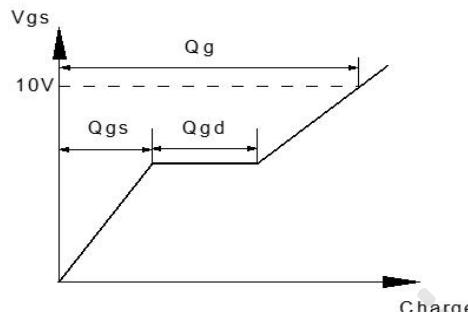
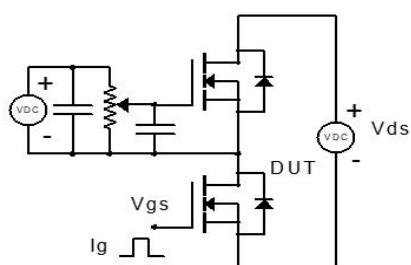


Figure 1: Gate Charge Test Circuit & Waveform

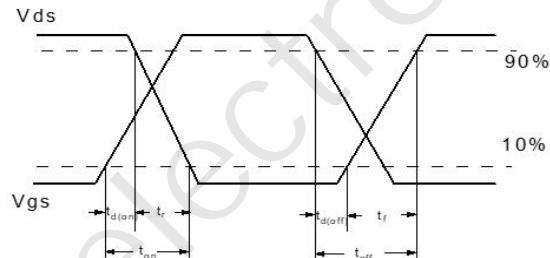
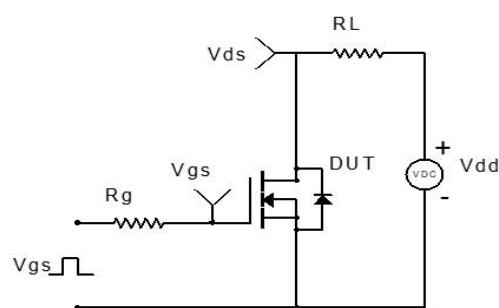


Figure 2: Resistive Switching Test Circuit & Waveform

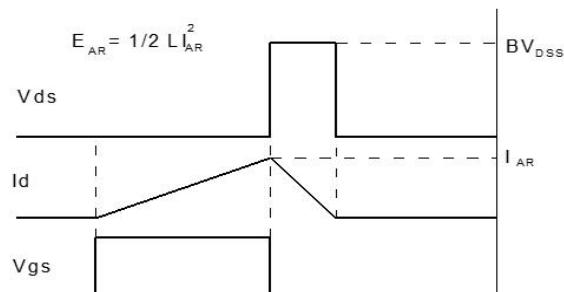
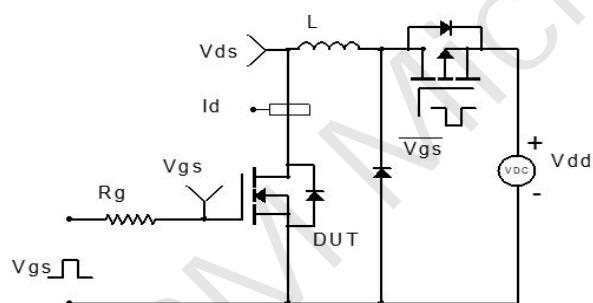


Figure 3: Unclamped Inductive Switching Test Circuit& Waveform

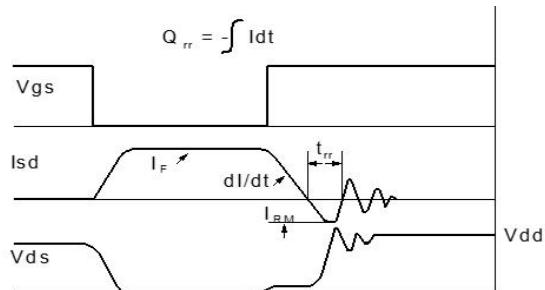
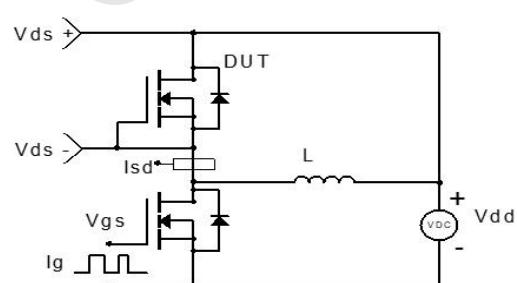
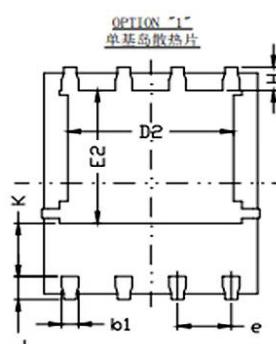
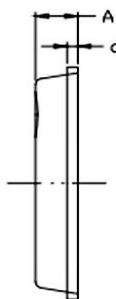
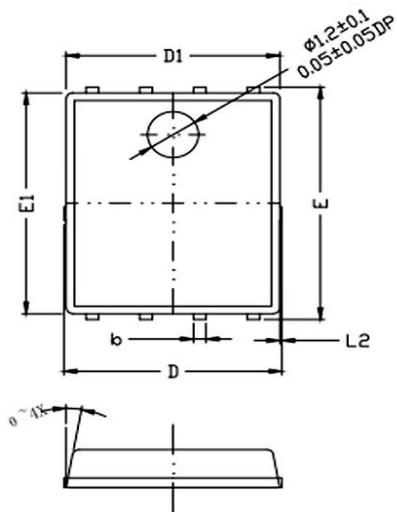


Figure 4: Diode Recovery Test Circuit & Waveform

Package Mechanical Data(PDFN5x6-8L)



SYM BOL	MILLIMETER		
	MIN	NOM	MAX
A	0.90	1.00	1.10
b	0.25	0.30	0.35
b1	0.30	0.40	0.45
c	0.22	0.25	0.28
D	--	--	5.30
D1	4.90	5.05	5.20
D2	3.90 REF		
D3	1.70 REF		
E	6.00	6.15	6.30
E1	5.70	5.85	6.00
E2	3.50 REF		
e	1.10	1.27	1.40
H	0.51	0.61	0.71
K	1.10	--	--
L	0.51	0.61	0.71
L2	--	--	0.10
Φ	8*	~	12*

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