Description

N-channel Enhancement Mode Power MOSFET

Features

- 100V, 60A $R_{DS(ON)}$ Typ =10.5m Ω @ V_{GS} = 10V
- Advanced Split Gate Trench Technology
- Excellent R_{DS(ON)} and Low Gate Charge

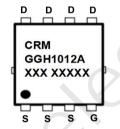
Applications

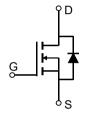
- Load Switch
- **PWM Application**
- **Power Management**

100% UIS TESTED! 100% ΔVds TESTED!









PDFN5x6-8L

Marking and Pin Assignment

Schematic Diagram

Package Marking and Ordering Information

Device Marking	Device	Outline	Package	Reel Size	Reel(pcs)	Per Carton (pcs)
CRMGGH1012A	CRMGGH1012A	TAPING	PDFN5x6-8L	13"	5000	50000

Absolute Maximum Ratings (@ T_J = 25°C unless otherwise specified)

Symbol	Parameter		Value	Units	
V_{DS}	Drain-to-Source Voltage		100	V	
V_{GS}	Gate-to-Source Voltage		±20	V	
	Continuous Drain Current	T _C = 25°C	60	^	
I _D	Continuous Drain Current	T _C = 100°C	36	A	
I _{DM}	Pulsed Drain Current (1)		240	А	
E _{AS}	Single Pulsed Avalanche Energ	y ⁽²⁾	81	mJ	
P_{D}	Power Dissipation	T _C = 25°C	83.3	W	
$R_{\theta JC}$	Thermal Resistance, Junction to	Case	1.5	°C/W	
T_{J}, T_{STG}	Junction & Storage Temperature F	Range	-55 to 150	°C	



Electrical Characteristics (T_J = 25°C unless otherwise specified)

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
Off Cha	aracteristics	•				
V _{(BR)DSS}	Drain-Source Breakdown Voltage	$I_D = 250 \mu A, V_{GS} = 0 V$	100	-	-	V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = 100V, V _{GS} = 0V	-	-	1.0	μА
I _{GSS}	Gate-Body Leakage Current	$V_{DS} = 0V, V_{GS} = \pm 20V$	-	-	±100	nA
On Cha	aracteristics					
V _{GS(th)}	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	2.4	3.0	3.6	V
R _{DS(ON)}	Static Drain-Source ON-Resistance ⁽³⁾	$V_{GS} = 10V, I_D = 30A$	-	10.5	13.6	mΩ
Dynami	ic Characteristics					
C _{iss}	Input Capacitance		-	1207	-	pF
C _{oss}	Output Capacitance	$V_{GS} = 0V$, $V_{DS} = 25V$, f = 1MHz	-	799	-	pF
C_{rss}	Reverse Transfer Capacitance	I – IIVITZ		30	-	pF
Q_g	Total Gate Charge		-	28	-	nC
Q_{gs}	Gate Source Charge	$V_{GS} = 0 \text{ to } 10V$ $V_{DS} = 50V, I_D = 20A$	-	4.9	-	nC
Q_{gd}	Gate Drain("Miller") Charge	V _{DS} - 30V, I _D - 20A	<u></u>	7	-	nC
Switchi	ing Characteristics					
t _{d(on)}	Turn-On DelayTime		-	13.5	-	ns
t _r	Turn-On Rise Time	$V_{GS} = 10V, V_{DD} = 50V$	-	17	-	ns
t _{d(off)}	Turn-Off DelayTime	$I_D = 20A$, $R_{GEN} = 6\Omega$	-	30	-	ns
t _f	Turn-Off Fall Time		-	18.0	-	ns
Drain-S	Source Diode Characteristics and I	Max Ratings				
I _s	Maximum Continuous Drain to Source Dioc	de Forward Current	-	-	60	Α
I _{SM}	Maximum Pulsed Drain to Source Diode Fo	orward Current	-	-	240	Α
V_{SD}	Drain to Source Diode Forward Voltage	$V_{GS} = 0V, I_{S} = 30A$	-	-	1.2	V
trr	Body Diode Reverse Recovery Time	1 004 11/11 4004/	-	50	-	ns
Qrr	Body Diode Reverse Recovery Charge	$I_F = 20A$, di/dt = 100A/us	-	80	-	nC

Notes:

^{1.} Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature.

^{2.} E_{AS} condition: Starting T_J =25°C, V_{DD} =50V, V_G =10V, R_G =25ohm, L=0.5mH, I_{AS} =18A

^{3.} Pulse Test: Pulse Width≤300µs, Duty Cycle≤0.5%.

Test Circuit

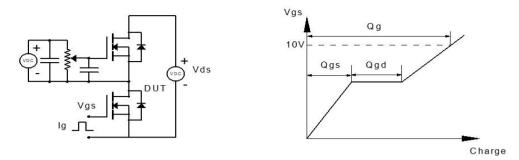


Figure 1: Gate Charge Test Circuit & Waveform

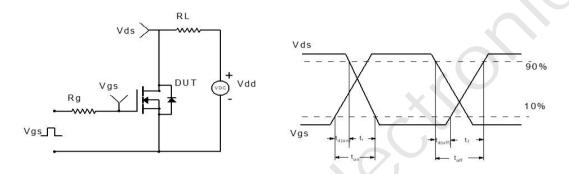


Figure 2: Resistive Switching Test Circuit & Waveform

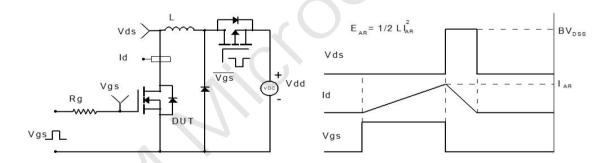


Figure 3: Unclamped Inductive Switching Test Circuit& Waveform

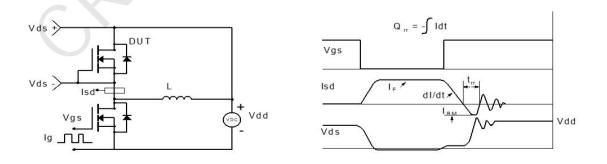
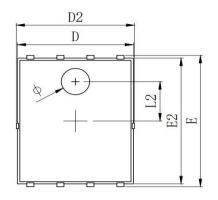
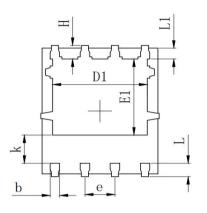


Figure 4: Diode Recovery Test Circuit & Waveform

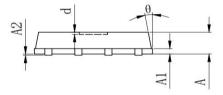


Package Mechanical Data(PDFN5x6-8L)





SYMBOL	MILLIMETER				
	MIN	Тур.	MAX		
A	0.900	1.000	1.100		
A1		0. 254 REF.	·		
A2	0~0.05				
D	4.824	4.900	4.976		
D1	3.910	4.010	4. 110		
D2	4.924	5. 000	5. 076		
E	5.924	6.000	6.076		
E1	3.375	3. 475	3. 575		
E2	5.674	5. 750	5. 826		
b	0.350	0.400	0.450		
е	1. 270 TYP.				
L	0.534	0.610	0.686		
L1	0.424	0.500	0. 576		
L2	1. 800 REF.				
k	1.190	1. 290	1.390		
Н	0.549	0.625	0.701		
θ	8°	10°	12°		
ф	1.100	1. 200	1. 300		
d			0.100		



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