CRMKTL0608A

Description

N-channel Enhancement Mode Power MOSFET

Features

• 60V, 80A

 $R_{DS(ON)}$ Typ= 5.2m Ω @ V_{GS} = 10V $R_{DS(ON)}$ Typ= 5.9m Ω @ V_{GS} = 4.5V

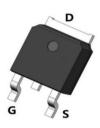
- Advanced Trench Technology
- Excellent R_{DS(ON)} and Low Gate Charge

Applications

- Load Switch
- PWM Application
- Power Management

100% UIS TESTED! 100% ΔVds TESTED!

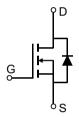








Marking and Pin Assignment



Schematic Diagram

Package Marking and Ordering Information

Device Marking	Device	Outline	Package	Reel Size	Reel(pcs)	Per Carton (pcs)
CRMKTL0608A	CRMKTL0608A	TAPING	TO-252-3L	13"	2500	25000

Absolute Maximum Ratings (@ T_J = 25°C unless otherwise specified)

Symbol	Parameter		Value	Units	
V _{DS}	Drain-to-Source Voltage		60	V	
V _{GS}	Gate-to-Source Voltage		±20	V	
I _D	Continuous Drain Current	T _C = 25°C	80	_	
		T _C = 100°C	48	A	
I _{DM}	Pulsed Drain Current (1)		320	А	
E _{AS}	Single Pulsed Avalanche Energy (2)		156	mJ	
P_D	Power Dissipation	T _C = 25°C	114	W	
$R_{\theta JC}$	Thermal Resistance, Junction to Case		1.10	°C/W	
T_J , T_{STG}	Junction & Storage Temperature Range		-55 to 150	°C	



CRMKTL0608A

Electrical Characteristics (T_J = 25°C unless otherwise specified)

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
Off Cha	aracteristics					
V _{(BR)DSS}	Drain-Source Breakdown Voltage	$I_D = 250 \mu A, V_{GS} = 0 V$	60	-	-	V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = 60V, V _{GS} = 0V	-	-	1.0	μА
I _{GSS}	Gate-Body Leakage Current	$V_{DS} = 0V, V_{GS} = \pm 20V$	-	-	±100	nA
On Cha	racteristics					
V _{GS(th)}	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	1.2	1.6	2.2	V
		$V_{GS} = 10V, I_D = 30A$	-	5.2	6.8	mΩ
$R_{DS(ON)}$	Static Drain-Source ON-Resistance ⁽⁴⁾	$V_{GS} = 4.5V, I_{D} = 20A$	-	5.9	7.7	mΩ
Dynami	ic Characteristics					
C _{iss}	Input Capacitance		- (4765	-	pF
C _{oss}	Output Capacitance	$V_{GS} = 0V, V_{DS} = 25V,$	(277	-	pF
C _{rss}	Reverse Transfer Capacitance	f = 1MHz	-	245	-	pF
Q_g	Total Gate Charge		_	98	-	nC
Q_{gs}	Gate Source Charge	$V_{GS} = 0 \text{ to } 10V$	<u></u>	12.5	-	nC
Q_{gd}	Gate Drain("Miller") Charge	$V_{DS} = 30V, I_{D} = 10A$	-	32	-	nC
Switchi	ing Characteristics					
t _{d(on)}	Turn-On DelayTime		-	9	-	ns
t _r	Turn-On Rise Time	$V_{GS} = 10V, V_{DD} = 30V$	-	6.1	-	ns
$t_{d(off)}$	Turn-Off DelayTime	$I_D = 15A, R_{GEN} = 1.8\Omega$	-	33.2	-	ns
t _f	Turn-Off Fall Time		-	7.5	-	ns
Drain-S	Source Diode Characteristics and I	Max Ratings				
I _S	Maximum Continuous Drain to Source Diode Forward Current		-	-	80	Α
I _{SM}	Maximum Pulsed Drain to Source Diode Forward Current		-	-	320	А
V_{SD}	Drain to Source Diode Forward Voltage	$V_{GS} = 0V, I_{S} = 30A$	-	-	1.2	V
trr	Body Diode Reverse Recovery Time		-	31	-	ns
Qrr	Body Diode Reverse Recovery Charge	I _F = 15A, di/dt = 100A/us	-	48	-	nC

Notes:

^{1.} Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature.

^{2.} E_{AS} condition: Starting T_J =25C, V_{DD} =30V, V_G =10V, R_G =25ohm, L=0.5mH, I_{AS} =25A

^{3.} Pulse Test: Pulse Width $\!\! \leqslant \! 300 \mu s,$ Duty Cycle $\!\! \leqslant \! 0.5 \%.$



Test Circuit

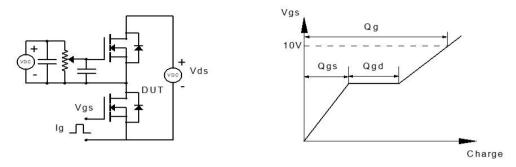


Figure 1: Gate Charge Test Circuit & Waveform

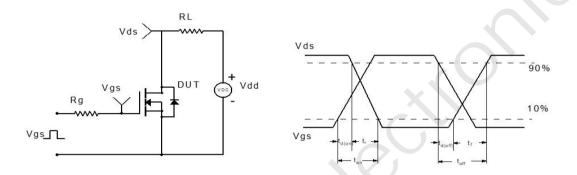


Figure 2: Resistive Switching Test Circuit & Waveform

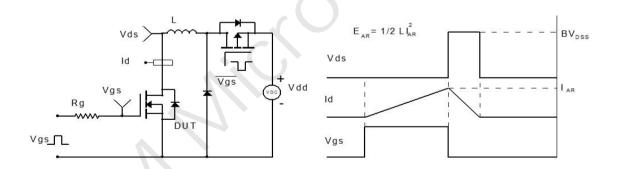


Figure 3: Unclamped Inductive Switching Test Circuit& Waveform

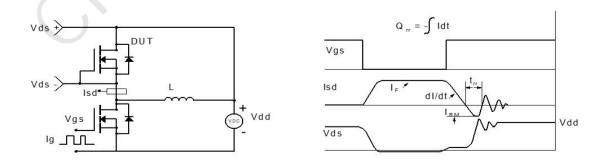
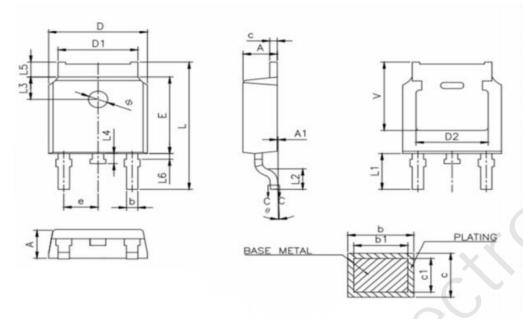


Figure 4: Diode Recovery Test Circuit & Waveform





Package Mechanical Data(TO-252-3L)



SYMBOL	MILLIMETER				
STWIDOL	MIN	NOM	MAX		
A	2.20	2.30	2.40		
A1	0.00		0.127		
ь	0.66		0.86		
ь1	0.65	0.76	0.81		
D	6.50	6.60	6.70		
D1	5.10	5.33	5.46		
С	0.47		0.60		
c1	0.46	0.51	0.56		
D2	4.83 REF.				
E	6.00	6.10	6.20		
е	2.186	2.286	2.386		
L	9.80	10.10	10.40		
L1	2.90 REF.				
L2	1.40	1.50	1.60		
L3	1.80 REF.				
L4	0.60	0.80	1.00		
L5	0.90		1.25		
L6	0.15		0.75		
Ф	1.10		1.30		
0	0.		8.		
V	5.40 REF				

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