CRMKTH0608A

Description

N-channel Enhancement Mode Power MOSFET

Features

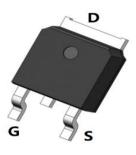
- 60V, 80A
 - $R_{DS(ON)}$ Typ= $6m\Omega$ @ V_{GS} = 10V
- Advanced Trench Technology
- Excellent R_{DS(ON)} and Low Gate Charge

Applications

- Load Switch
- PWM Application
- Power Management

100% UIS TESTED! 100% ΔVds TESTED!

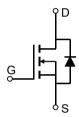








Marking and Pin Assignment



Schematic Diagram

Package Marking and Ordering Information

Device Marking	Device	Outline	Package	Reel Size	Reel(pcs)	Per Carton (pcs)
CRMKTH0608A	CRMKTH0608A	TAPING	TO-252-3L	13"	2500	25000

Absolute Maximum Ratings (@ T_J = 25°C unless otherwise specified)

Symbol	Parameter		Value	Units
V _{DS}	Drain-to-Source Voltage		60	V
V _{GS}	Gate-to-Source Voltage		±20	V
	Continuous Drain Current	T _C = 25°C	80	
I _D		T _C = 100°C	48	A
I _{DM}	Pulsed Drain Current (1)		320	Α
E _{AS}	Single Pulsed Avalanche Energy (2)		182	mJ
P _D	Power Dissipation	T _C = 25°C	114	W
$R_{\theta JC}$	Thermal Resistance, Junction to Case		1.1	°C/W
T _J , T _{STG}	Junction & Storage Temperature Range		-55 to 150	°C



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Electrical Characteristics (T_J = 25°C unless otherwise specified)

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
Off Cha	racteristics					
V _{(BR)DSS}	Drain-Source Breakdown Voltage	$I_D = 250 \mu A, V_{GS} = 0 V$	60	-	-	V
I _{DSS}	Zero Gate Voltage Drain Current	$V_{DS} = 60V, V_{GS} = 0V$	-	-	1.0	μА
I _{GSS}	Gate-Body Leakage Current	$V_{DS} = 0V, V_{GS} = \pm 20V$	-	-	±100	nA
On Cha	racteristics					
V _{GS(th)}	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	2	2.8	4	V
R _{DS(ON)}	Static Drain-Source ON-Resistance ⁽³⁾	V _{GS} = 10V, I _D = 30A	-	6	7.8	mΩ
Dynami	c Characteristics					
C _{iss}	Input Capacitance		-	3820	-	pF
C _{oss}	Output Capacitance	$V_{GS} = 0V, V_{DS} = 25V,$ f = 1MHz	-	295	-	pF
C _{rss}	Reverse Transfer Capacitance	— I − IIVI⊓Z		240	-	pF
Q _g	Total Gate Charge	., ., ., .,	-	77	-	nC
Q_{gs}	Gate Source Charge	$V_{GS} = 0 \text{ to } 10V$ $V_{DS} = 30V, I_{D} = 30A$		21	-	nC
Q_{gd}	Gate Drain("Miller") Charge	V _{DS} = 30 V, I _D = 30A	<u> </u>	24	-	nC
Switchi	ng Characteristics					
$t_{d(on)}$	Turn-On DelayTime		-	18	1	ns
t _r	Turn-On Rise Time	$V_{GS} = 10V, V_{DD} = 30V$	-	88	1	ns
$t_{d(off)}$	Turn-Off DelayTime	I_{D} = 30A, R_{GEN} = 1.8 Ω	-	37	-	ns
t _f	Turn-Off Fall Time		-	85	1	ns
Drain-S	ource Diode Characteristics and M	Max Ratings				
Is	Maximum Continuous Drain to Source Diode Forward Current		-	-	80	А
I _{SM}	Maximum Pulsed Drain to Source Diode Forward Current		-	-	320	Α
V _{SD}	Drain to Source Diode Forward Voltage	$V_{GS} = 0V, I_{S} = 30A$	-	-	1.2	V

Notes:

^{1.} Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature.

^{2.} E_{AS} condition: Starting T_J =25C, V_{DD} =30V, V_G =10V, R_G =25ohm, L=0.5mH, I_{AS} =27A

^{3.} Pulse Test: Pulse Width≤300µs, Duty Cycle≤0.5%.



Test Circuit

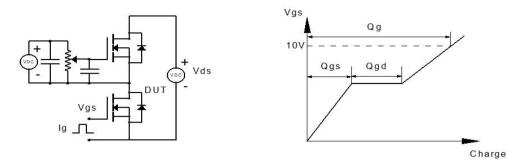


Figure 1: Gate Charge Test Circuit & Waveform

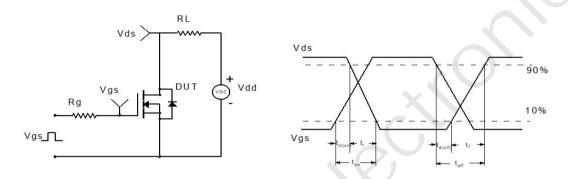


Figure 2: Resistive Switching Test Circuit & Waveform

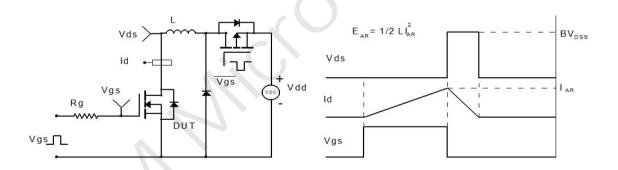


Figure 3: Unclamped Inductive Switching Test Circuit& Waveform

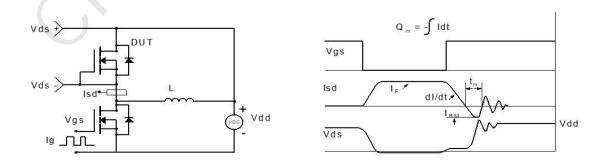
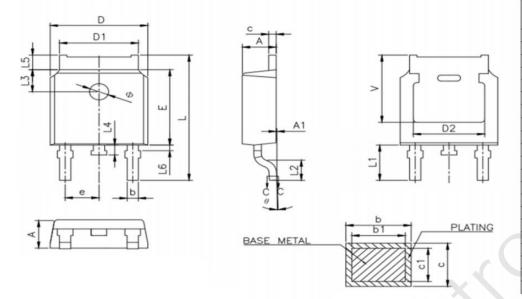


Figure 4: Diode Recovery Test Circuit & Waveform



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Package Mechanical Data(TO-252-3L)



SYMBOL	MILLIMETER					
STWIDOL	MIN	NOM	MAX			
Α	2.20	2.30	2.40			
A1	0.00		0.127			
b	0.66		0.86			
b1	0.65	0.76	0.81			
D	6.50	6.60	6.70			
D1	5.10	5.33	5.46			
С	0.47		0.60			
c1	0.46	0.51	0.56			
D2	4.83 REF.					
E	6.00	6.10	6.20			
е	2.186	2.286	2.386			
L	9.80	10.10	10.40			
L1	2.90 REF.					
L2	1.40	1.50	1.60			
L3	1.80 REF.					
L4	0.60	0.80	1.00			
L5	0.90		1.25			
L6	0.15		0.75			
Ф	1.10		1.30			
θ	0.		8.			
V	5.40 REF					

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