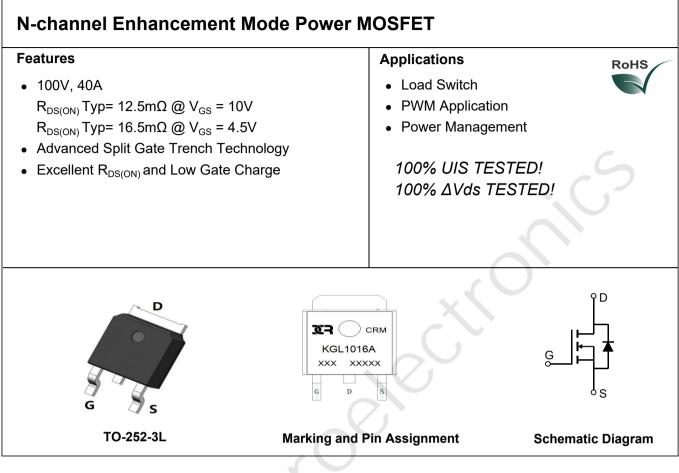


Description



Package Marking and Ordering Information

Device Marking	Device	Outline	Package	Reel Size	Reel(pcs)	Per Carton (pcs)
CRMKGL1016A	CRMKGL1016A	TAPING	TO-252-3L	13"	2500	25000

Absolute Maximum Ratings (@ T_J = 25°C unless otherwise specified)

Symbol	Parameter		Value	Units	
V _{DS}	Drain-to-Source Voltage		100	V	
V _{GS}	Gate-to-Source Voltage		±20	V	
	Continuous Drain Current	T _C = 25°C	40	Α	
Ι _D		T _C = 100°C	25		
I _{DM}	Pulsed Drain Current ⁽¹⁾		160	А	
E _{AS}	Single Pulsed Avalanche Energy ⁽²⁾		64	mJ	
P _D	Power Dissipation	T _C = 25°C	57	W	
$R_{ extsf{ heta}JC}$	Thermal Resistance, Junction to Case		2.2	°C/W	
T_{J}, T_{STG}	Junction & Storage Temperature Range		-55 to 150	°C	



Electrical Characteristics (T_J = 25°C unless otherwise specified)

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
Off Cha	aracteristics		<u>_</u>		<u> </u>	
V _{(BR)DSS}	Drain-Source Breakdown Voltage	$I_{D} = 250 \mu A, V_{GS} = 0 V$	100	-	-	V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = 100V, V _{GS} = 0V	-	-	1.0	μΑ
I _{GSS}	Gate-Body Leakage Current	$V_{DS} = 0V, V_{GS} = \pm 20V$	-	-	±100	nA
On Cha	iracteristics				6	
V _{GS(th)}	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	1.2	1.8	2.5	V
		V _{GS} = 10V, I _D = 15A	-	12.5	16.3	mΩ
R _{DS(ON)}	Static Drain-Source ON-Resistance ⁽³⁾	V _{GS} = 4.5V, I _D = 10A -		16.5	21.5	mΩ
Dynami	ic Characteristics					
C _{iss}	Input Capacitance		-	1130	-	pF
C _{oss}	Output Capacitance	$V_{GS} = 0V, V_{DS} = 25V,$	-	655	-	pF
C _{rss}	Reverse Transfer Capacitance	f = 1MHz	- \	25	-	pF
Qg	Total Gate Charge			15.5	-	nC
Q_{gs}	Gate Source Charge	$V_{GS} = 0$ to 10V $V_{DS} = 50V$, $I_D = 10A$	<u> </u>	4	-	nC
Q_{gd}	Gate Drain("Miller") Charge	$v_{\rm DS} = 50 v, v_{\rm D} = 10 A$	-	3.8	-	nC
	·					
Switchi	ing Characteristics					
t _{d(on)}	Turn-On DelayTime		-	4.5	-	ns
t _r	Turn-On Rise Time	V _{GS} = 10V, V _{DD} = 50V	-	5.5	-	ns
t _{d(off)}	Turn-Off DelayTime	I_D = 10A, R_{GEN} = 6 Ω	-	16	-	ns
t _f	Turn-Off Fall Time		-	9	-	ns
Drain-S	Source Diode Characteristics and	Max Ratings				
ا _s	Maximum Continuous Drain to Source Diode Forward Current		-	-	40	А
I _{SM}	Maximum Pulsed Drain to Source Diode Forward Current		-	-	160	Α
$V_{\rm SD}$	Drain to Source Diode Forward Voltage	V _{GS} = 0V, I _S = 15A	-	-	1.2	V
trr	Body Diode Reverse Recovery Time		-	40	-	ns
Qrr	Body Diode Reverse Recovery Charge	I _F = 15A, di/dt = 100A/us	-	32	_	nC

Notes: 1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature.

2. E_{AS} condition: Starting T_J=25C, V_{DD}=50V, V_G=10V, R_G=25ohm, L=0.5mH, I_{AS}=16A

3. Pulse Test: Pulse Width ${\leqslant}300\mu s,$ Duty Cycle ${\leqslant}0.5\%.$



CRMKGL1016A

Test Circuit

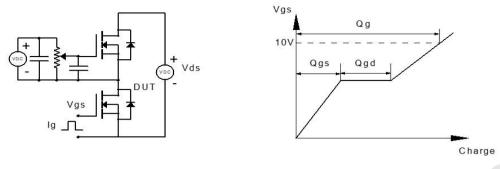


Figure 1: Gate Charge Test Circuit & Waveform

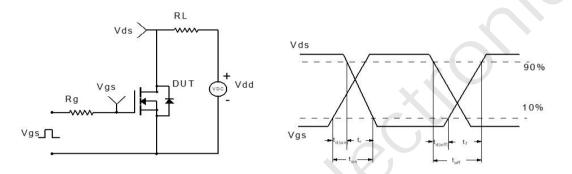
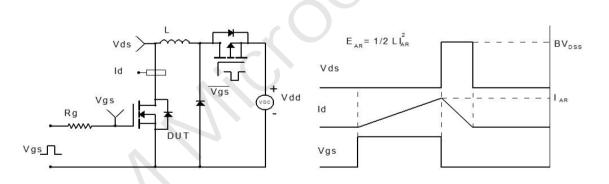
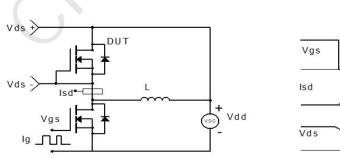
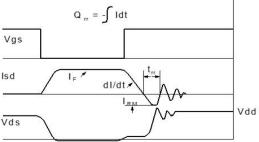


Figure 2: Resistive Switching Test Circuit & Waveform





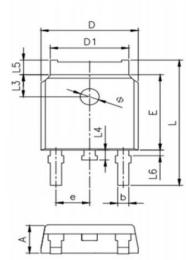


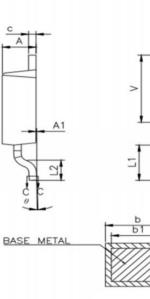






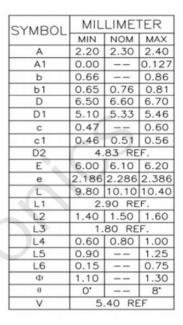
Package Mechanical Data(TO-252-3L)





D2

PLATING



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