

## Description

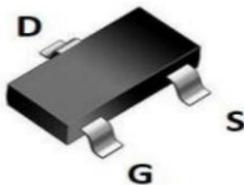
### N-channel Enhancement Mode Power MOSFET

#### Features

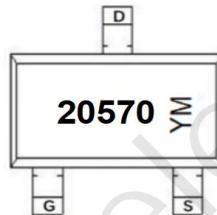
- 200V, 1.3A  
 $R_{DS(ON)}$  Typ= 480mΩ @  $V_{GS} = 10V$
- Advanced Trench Technology
- Excellent  $R_{DS(ON)}$  and Low Gate Charge
- Lead Free

#### Applications

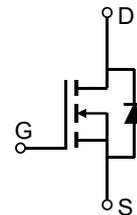
- Load Switch
- PWM Application
- Power Management



SOT-23-3L



Marking and Pin Assignment



Schematic Diagram

### Package Marking and Ordering Information

Device Marking	Device	Outline	Package	Reel Size	Reel(pcs)	Per Carton (pcs)
20570	CRMJTL20570A	TAPING	SOT-23-3L	7"	3000	120000

### Absolute Maximum Ratings (@ $T_J = 25^\circ C$ unless otherwise specified)

Symbol	Parameter	Value	Units
$V_{DS}$	Drain-to-Source Voltage	200	V
$V_{GS}$	Gate-to-Source Voltage	±20	V
$I_D$	Continuous Drain Current	$T_A = 25^\circ C$	1.3
		$T_A = 100^\circ C$	0.8
$I_{DM}$	Pulsed Drain Current <sup>(1)</sup>	5.2	A
$P_D$	Power Dissipation	$T_A = 25^\circ C$	2.5
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient <sup>(2)</sup>	50	°C/W
$T_J, T_{STG}$	Junction & Storage Temperature Range	-55 to 150	°C



## Electrical Characteristics (T<sub>J</sub> = 25°C unless otherwise specified)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
<b>Off Characteristics</b>						
V <sub>(BR)DSS</sub>	Drain-Source Breakdown Voltage	I <sub>D</sub> = 250μA, V <sub>GS</sub> = 0V	200	-	-	V
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	V <sub>DS</sub> = 200V, V <sub>GS</sub> = 0V	-	-	1.0	μA
I <sub>GSS</sub>	Gate-Body Leakage Current	V <sub>DS</sub> = 0V, V <sub>GS</sub> = ±20V	-	-	±100	nA
<b>On Characteristics</b>						
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250μA	1.3	2.1	2.7	V
R <sub>DS(ON)</sub>	Static Drain-Source ON-Resistance <sup>(3)</sup>	V <sub>GS</sub> = 10V, I <sub>D</sub> = 1A	-	480.0	576.0	mΩ
<b>Dynamic Characteristics</b>						
C <sub>iss</sub>	Input Capacitance	V <sub>GS</sub> = 0V, V <sub>DS</sub> = 25V, f = 1MHz	-	495	-	pF
C <sub>oss</sub>	Output Capacitance		-	24	-	pF
C <sub>rss</sub>	Reverse Transfer Capacitance		-	18	-	pF
Q <sub>g</sub>	Total Gate Charge	V <sub>GS</sub> = 0 to 10V V <sub>DS</sub> = 100V, I <sub>D</sub> = 1A	-	12	-	nC
Q <sub>gs</sub>	Gate Source Charge		-	2.5	-	nC
Q <sub>gd</sub>	Gate Drain("Miller") Charge		-	3.8	-	nC
<b>Switching Characteristics</b>						
t <sub>d(on)</sub>	Turn-On DelayTime	V <sub>GS</sub> = 10V, V <sub>DD</sub> = 100V I <sub>D</sub> = 1A, R <sub>GEN</sub> = 2.5Ω	-	10	-	ns
t <sub>r</sub>	Turn-On Rise Time		-	12	-	ns
t <sub>d(off)</sub>	Turn-Off DelayTime		-	15	-	ns
t <sub>f</sub>	Turn-Off Fall Time		-	15	-	ns
<b>Drain-Source Diode Characteristics and Max Ratings</b>						
I <sub>S</sub>	Maximum Continuous Drain to Source Diode Forward Current		-	-	1.3	A
I <sub>SM</sub>	Maximum Pulsed Drain to Source Diode Forward Current		-	-	5.2	A
V <sub>SD</sub>	Drain to Source Diode Forward Voltage	V <sub>GS</sub> = 0V, I <sub>S</sub> = 1A	-	-	1.2	V
t <sub>rr</sub>	Body Diode Reverse Recovery Time	I <sub>F</sub> = 1A, di/dt = 100A/us	-	50	-	ns
Q <sub>rr</sub>	Body Diode Reverse Recovery Charge		-	98	-	nC

- Notes:
1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature.
  2. R<sub>θJA</sub> is measured with the device mounted on a 1inch<sup>2</sup> pad of 2oz copper FR4 PCB
  3. Pulse Test: Pulse Width ≤ 300μs, Duty Cycle ≤ 0.5%.

## Test Circuit

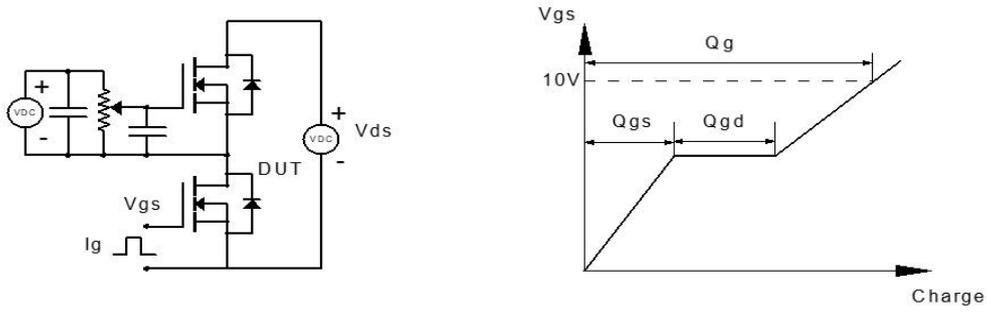


Figure 1: Gate Charge Test Circuit & Waveform

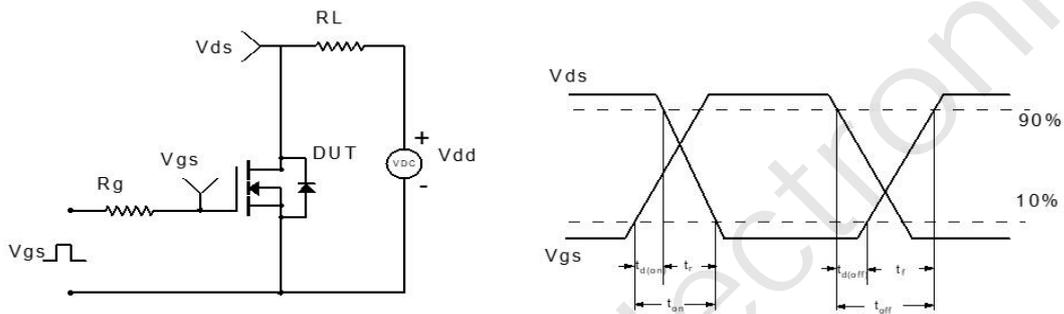


Figure 2: Resistive Switching Test Circuit & Waveform

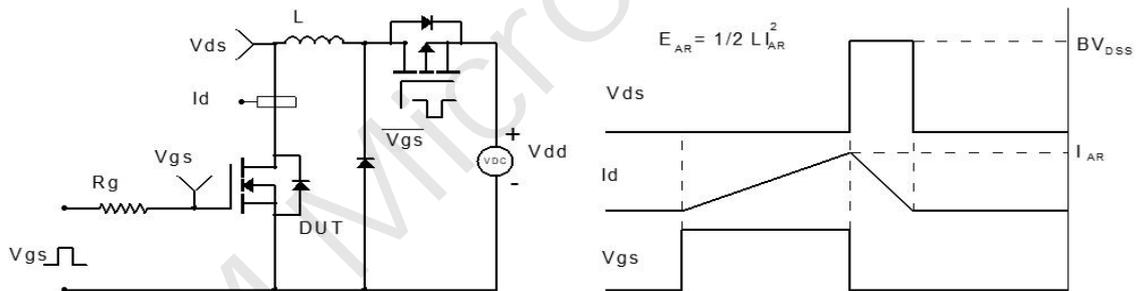


Figure 3: Unclamped Inductive Switching Test Circuit & Waveform

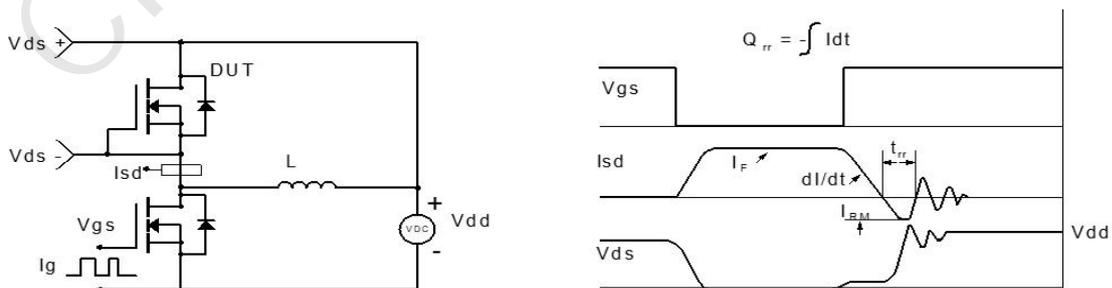
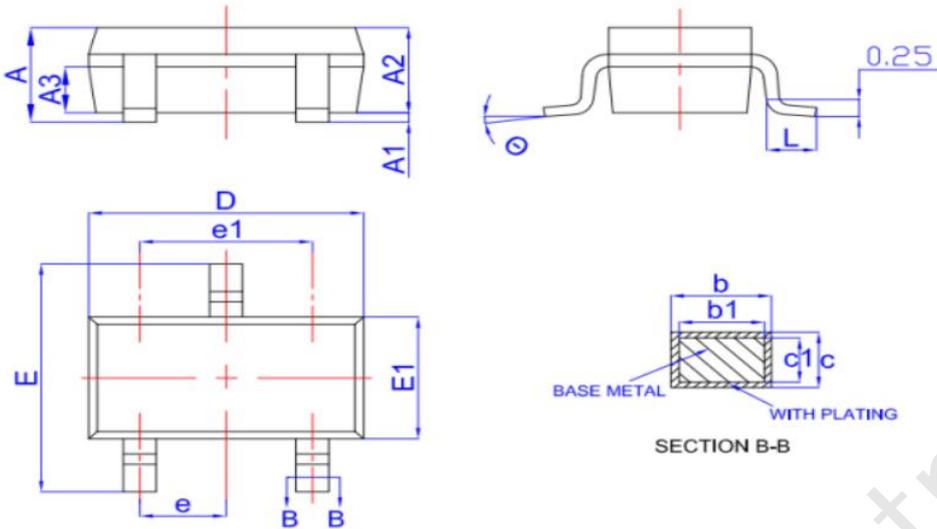


Figure 4: Diode Recovery Test Circuit & Waveform



## Package Mechanical Data(SOT-23-3L)



SYMBOL	MILLIMETER		
	MIN	NOM	MAX
A	—	—	1.25
A1	0.04	—	0.10
A2	1.00	1.10	1.20
A3	0.55	0.65	0.75
b	0.3	—	0.4
b1	0.37	0.40	0.43
c	0.11	—	0.21
c1	0.10	0.13	0.16
D	2.72	2.92	3.12
E	2.60	2.80	3.00
E1	1.40	1.60	1.80
e	0.95BSC		
e1	1.90BSC		
L	0.30	—	0.60
⊖	0	—	8°

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