

## Description

### N-channel Enhancement Mode Power MOSFET

#### Features

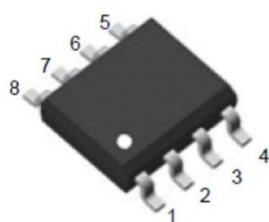
- 60V, 10A  
 $R_{DS(ON)} < 17m\Omega @ V_{GS} = 10V$   
 $R_{DS(ON)} < 21m\Omega @ V_{GS} = 4.5V$
- Advanced Trench Technology
- Excellent  $R_{DS(ON)}$  and Low Gate Charge
- Lead Free

#### Applications

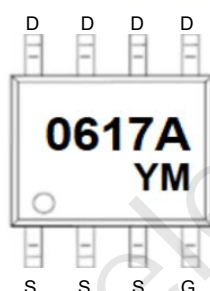
- Load Switch
- PWM Application
- Power Management



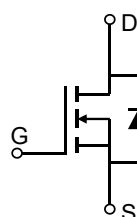
*100% UIS TESTED!*



SOP-8



Marking and Pin Assignment



Schematic Diagram

### Package Marking and Ordering Information

Device Marking	Device	Outline	Package	Reel Size	Reel(pcs)	Per Carton (pcs)
0617A	CRMPTL0617A	TAPING	SOP-8	13"	4000	40000

### Absolute Maximum Ratings (@ $T_C = 25^\circ C$ unless otherwise specified)

Symbol	Parameter	Value	Units
$V_{DS}$	Drain-to-Source Voltage	60	V
$V_{GS}$	Gate-to-Source Voltage	$\pm 20$	V
$I_D$	Continuous Drain Current	$T_C = 25^\circ C$	A
		$T_C = 100^\circ C$	
$I_{DM}$	Pulsed Drain Current <sup>(1)</sup>	40	A
$E_{AS}$	Single Pulsed Avalanche Energy <sup>(2)</sup>	72	mJ
$P_D$	Power Dissipation	$T_C = 25^\circ C$	W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient <sup>(3)</sup>	76	$^\circ C/W$
$T_J, T_{STG}$	Junction & Storage Temperature Range	-55 to 150	$^\circ C$

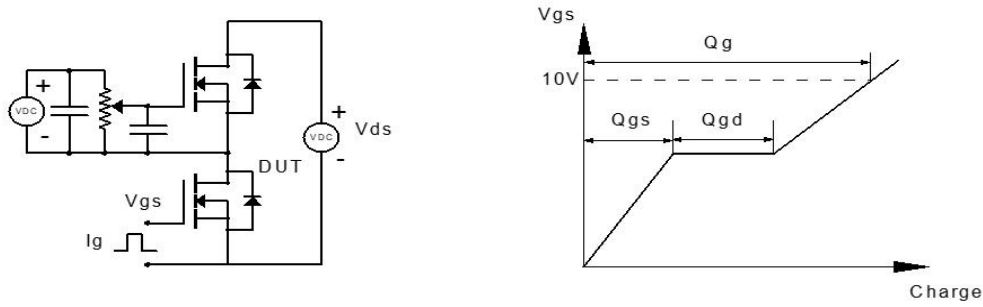


## Electrical Characteristics ( $T_J = 25^\circ\text{C}$ unless otherwise specified)

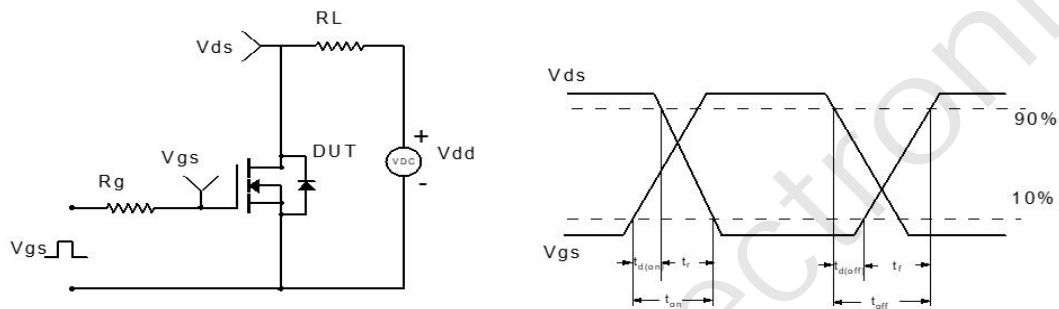
Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
Off Characteristics						
V <sub>(BR)DSS</sub>	Drain-Source Breakdown Voltage	I <sub>D</sub> = 250μA, V <sub>GS</sub> = 0V	60	-	-	V
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	V <sub>DS</sub> = 60V, V <sub>GS</sub> = 0V	-	-	1.0	μA
I <sub>GSS</sub>	Gate-Body Leakage Current	V <sub>DS</sub> = 0V, V <sub>GS</sub> = ±20V	-	-	±100	nA
On Characteristics						
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250μA	1	1.5	2.5	V
R <sub>DS(ON)</sub>	Static Drain-Source ON-Resistance <sup>(4)</sup>	V <sub>GS</sub> = 10V, I <sub>D</sub> = 10A	-	13	17	mΩ
		V <sub>GS</sub> = 4.5V, I <sub>D</sub> = 5A	-	16	21	mΩ
Dynamic Characteristics						
C <sub>iss</sub>	Input Capacitance	V <sub>GS</sub> = 0V, V <sub>DS</sub> = 25V, f = 1MHz	-	1990	-	pF
C <sub>oss</sub>	Output Capacitance		-	135	-	pF
C <sub>rss</sub>	Reverse Transfer Capacitance		-	115	-	pF
Q <sub>g</sub>	Total Gate Charge	V <sub>GS</sub> = 0 to 10V V <sub>DS</sub> = 30V, I <sub>D</sub> = 5A	-	46	-	nC
Q <sub>gs</sub>	Gate Source Charge		-	7	-	nC
Q <sub>gd</sub>	Gate Drain("Miller") Charge		-	10	-	nC
Switching Characteristics						
t <sub>d(on)</sub>	Turn-On DelayTime	V <sub>GS</sub> = 10V, V <sub>DD</sub> = 30V I <sub>D</sub> = 5A, R <sub>GEN</sub> = 1.8Ω	-	9	-	ns
t <sub>r</sub>	Turn-On Rise Time		-	15	-	ns
t <sub>d(off)</sub>	Turn-Off DelayTime		-	36	-	ns
t <sub>f</sub>	Turn-Off Fall Time		-	6	-	ns
Drain-Source Diode Characteristics and Max Ratings						
I <sub>S</sub>	Maximum Continuous Drain to Source Diode Forward Current		-	-	10	A
I <sub>SM</sub>	Maximum Pulsed Drain to Source Diode Forward Current		-	-	40	A
V <sub>SD</sub>	Drain to Source Diode Forward Voltage	V <sub>GS</sub> = 0V, I <sub>S</sub> = 10A	-	-	1.2	V

- Notes:
1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature.
  2.  $E_{AS}$  condition: Starting  $T_J = 25^\circ\text{C}$ ,  $V_{DD} = 30\text{V}$ ,  $V_G = 10\text{V}$ ,  $R_G = 25\Omega$ ,  $L = 0.5\text{mH}$ ,  $I_{AS} = 17\text{A}$
  3.  $R_{\theta JA}$  is measured with the device mounted on a  $1\text{inch}^2$  pad of 2oz copper FR4 PCB
  4. Pulse Test: Pulse Width  $\leq 300\mu\text{s}$ , Duty Cycle  $\leq 0.5\%$ .

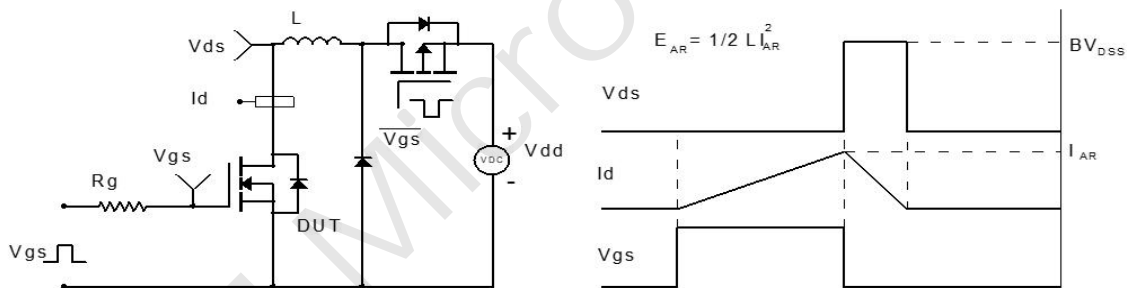
## Test Circuit



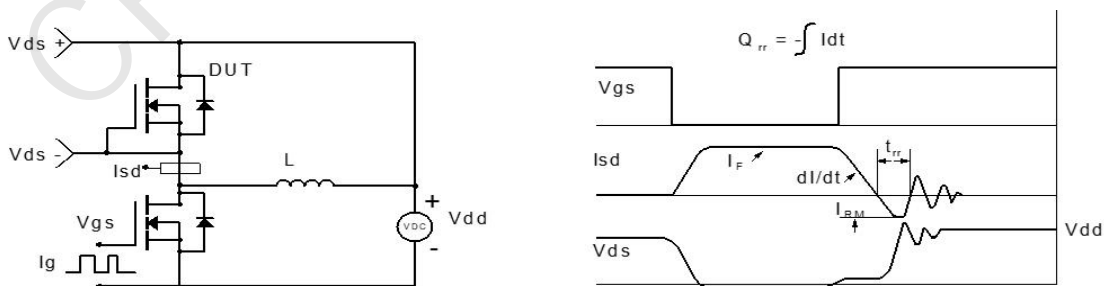
**Figure 1: Gate Charge Test Circuit & Waveform**



**Figure 2: Resistive Switching Test Circuit & Waveform**



**Figure 3: Unclamped Inductive Switching Test Circuit & Waveform**



**Figure 4: Diode Recovery Test Circuit & Waveform**

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