

Description

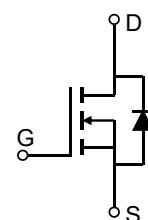
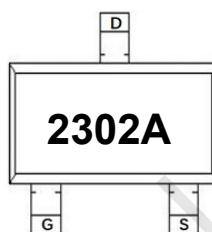
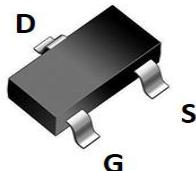
N-channel Enhancement Mode Power MOSFET

Features

- 20V, 4.3A
- $R_{DS(ON)}$ Typ= 20mΩ @ V_{GS} = 4.5V
- $R_{DS(ON)}$ Typ= 27mΩ @ V_{GS} = 2.5V
- Advanced Trench Technology
- Excellent $R_{DS(ON)}$ and Low Gate Charge
- Lead Free

Applications

- Load Switch
- PWM Application
- Power Management



SOT-23 Top

Marking and Pin Assignment

Schematic Diagram

Package Marking and Ordering Information

Device Marking	Device	Outline	Package	Reel Size	Reel(pcs)	Per Carton (pcs)
2302A	CRMLTU2302A	TAPING	SOT-23	7"	3000	120000

Absolute Maximum Ratings (@ T_J = 25°C unless otherwise specified)

Symbol	Parameter		Value	Units
V_{DS}	Drain-to-Source Voltage		20	V
V_{GS}	Gate-to-Source Voltage		± 10	V
I_D	Continuous Drain Current	$T_A = 25^\circ C$	4.3	A
		$T_A = 100^\circ C$	3.4	
I_{DM}	Pulsed Drain Current ⁽¹⁾		17.2	A
P_D	Power Dissipation	$T_A = 25^\circ C$	1.38	W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient ⁽²⁾		90.5	°C/W
T_J, T_{STG}	Junction & Storage Temperature Range		-55 to 150	°C

**Electrical Characteristics** ($T_J = 25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
Off Characteristics						
$V_{(\text{BR})\text{DSS}}$	Drain-Source Breakdown Voltage	$I_D = 250\mu\text{A}, V_{GS} = 0\text{V}$	20	-	-	V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS} = 20\text{V}, V_{GS} = 0\text{V}$	-	-	1.0	μA
I_{GSS}	Gate-Body Leakage Current	$V_{DS} = 0\text{V}, V_{GS} = \pm 10\text{V}$	-	-	± 100	nA
On Characteristics						
$V_{GS(\text{th})}$	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_D = 250\mu\text{A}$	0.4	0.70	1.0	V
$R_{DS(\text{ON})}$	Static Drain-Source ON-Resistance ⁽³⁾	$V_{GS} = 4.5\text{V}, I_D = 4.3\text{A}$	-	20.0	27.0	$\text{m}\Omega$
		$V_{GS} = 2.5\text{V}, I_D = 3\text{A}$	-	27.0	36.0	$\text{m}\Omega$
Dynamic Characteristics						
C_{iss}	Input Capacitance	$V_{GS} = 0\text{V}, V_{DS} = 10\text{V}, f = 1\text{MHz}$	-	354	-	pF
C_{oss}	Output Capacitance		-	71	-	pF
C_{rss}	Reverse Transfer Capacitance		-	67	-	pF
Q_g	Total Gate Charge	$V_{GS} = 0$ to 10V $V_{DS} = 5\text{V}, I_D = 4.3\text{A}$	-	4	-	nC
Q_{gs}	Gate Source Charge		-	0.6	-	nC
Q_{gd}	Gate Drain("Miller") Charge		-	1.9	-	nC
Switching Characteristics						
$t_{d(\text{on})}$	Turn-On DelayTime	$V_{GS} = 5\text{V}, V_{DD} = 10\text{V}$ $I_D = 4.3\text{A}, R_{\text{GEN}} = 6\Omega$	-	5.2	-	ns
t_r	Turn-On Rise Time		-	37	-	ns
$t_{d(\text{off})}$	Turn-Off DelayTime		-	15	-	ns
t_f	Turn-Off Fall Time		-	5.7	-	ns
Drain-Source Diode Characteristics and Max Ratings						
I_s	Maximum Continuous Drain to Source Diode Forward Current	-	-	4.3	A	
I_{SM}	Maximum Pulsed Drain to Source Diode Forward Current	-	-	17.2	A	
V_{SD}	Drain to Source Diode Forward Voltage	$V_{GS} = 0\text{V}, I_s = 4.3\text{A}$	-	-	1.2	V
trr	Body Diode Reverse Recovery Time	$I_F = 2\text{A}, di/dt = 60\text{A/us}$	-	6	-	ns
Qrr	Body Diode Reverse Recovery Charge		-	0.8	-	nC

Notes: 1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature.

2. $R_{\theta JA}$ is measured with the device mounted on a 1inch² pad of 2oz copper FR4 PCB3. Pulse Test: Pulse Width $\leq 300\mu\text{s}$, Duty Cycle $\leq 0.5\%$.

Test Circuit

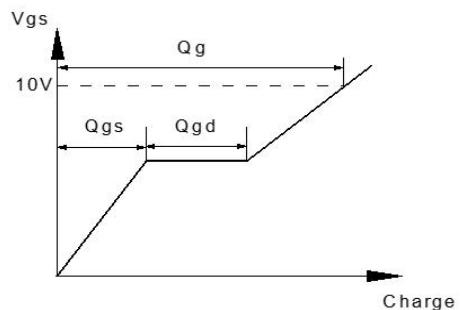
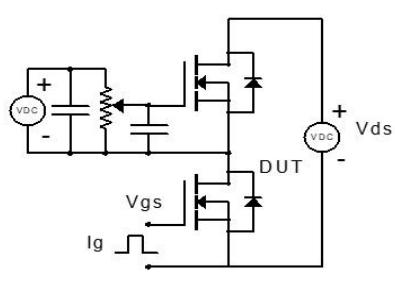


Figure 1: Gate Charge Test Circuit & Waveform

Static Drain-Source ON-Resistance⁽³⁾

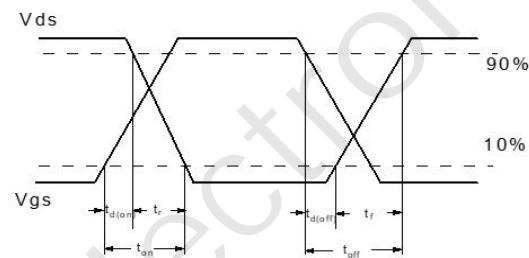
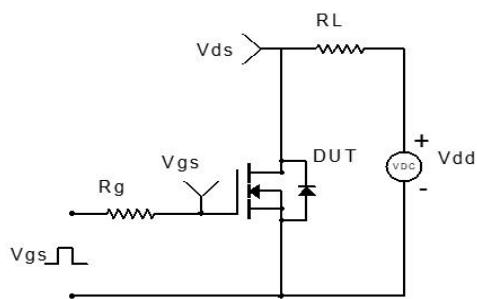


Figure 2: Resistive Switching Test Circuit & Waveform

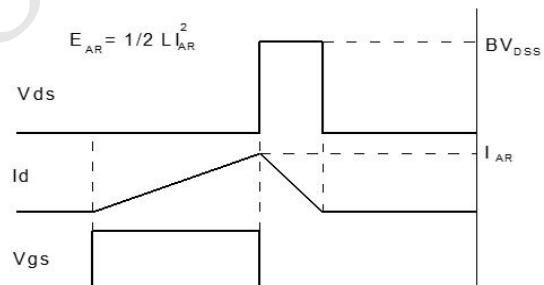
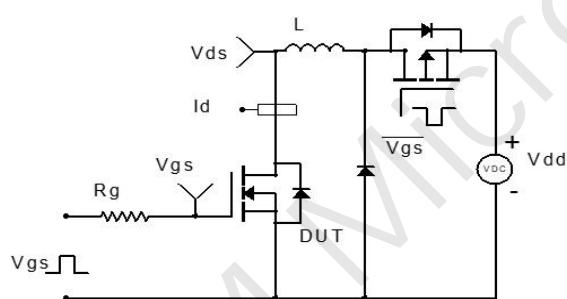


Figure 3: Unclamped Inductive Switching Test Circuit & Waveform

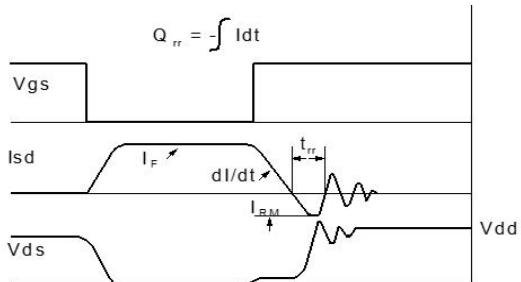
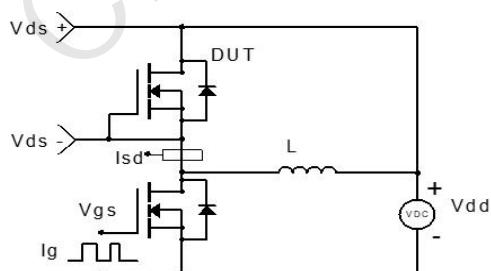
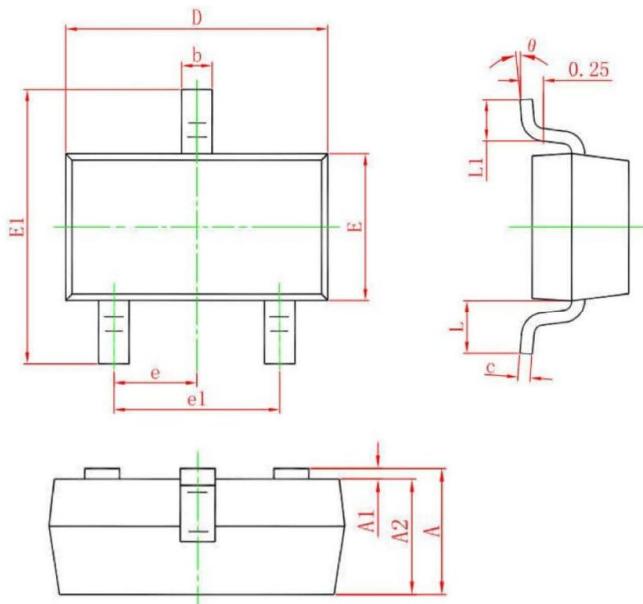


Figure 4: Diode Recovery Test Circuit & Waveform

Package Mechanical Data(SOT-23)



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	0.900	1.150	0.035	0.045
A1	0.000	0.100	0.000	0.004
A2	0.900	1.050	0.035	0.041
b	0.300	0.500	0.012	0.020
c	0.080	0.150	0.003	0.006
D	2.800	3.000	0.110	0.118
E	1.200	1.400	0.047	0.055
E1	2.250	2.550	0.089	0.100
e	0.950 TYP.		0.037 TYP.	
e1	1.800	2.000	0.071	0.079
L	0.550 REF.		0.022 REF.	
L1	0.300	0.500	0.012	0.020
θ	0°	8°	0°	8°

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