CRMCGH1512A

Description

N-channel Enhancement Mode Power MOSFET

Features

- 150V, 80A
 - $R_{DS(ON)}$ < 15m Ω @ V_{GS} = 10V
- Advanced Split Gate Trench Technology
- Excellent R_{DS(ON)} and Low Gate Charge

Applications

- Load Switch
- PWM Application
- Power Management

100% UIS TESTED! 100% ΔVds TESTED!

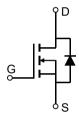








Marking and Pin Assignment



Schematic Diagram

Package Marking and Ordering Information

Device Marking	Device	Outline	Package	TUBE (pcs)	Inner Box (pcs)	Per Carton (pcs)
CRMCGH1512A	CRMCGH1512A	TUBE	TO-220C-3L	50	1000	5000

Absolute Maximum Ratings (@ T_C = 25°C unless otherwise specified)

Symbol	Parameter		Value	Units
V _{DS}	Drain-to-Source Voltage		150	V
V _{GS}	Gate-to-Source Voltage		±20	V
I _D	Continuous Drain Current	T _C = 25°C	80	А
		T _C = 100°C	48	
I _{DM}	Pulsed Drain Current ⁽¹⁾		320	Α
E _{AS}	Single Pulsed Avalanche Energy (2)		306	mJ
P_D	Power Dissipation	T _C = 25°C	156	W
$R_{\theta JC}$	Thermal Resistance, Junction to Case		0.8	°C/W
T_{J} , T_{STG}	Junction & Storage Temperature	Range	-55 to 150	°C



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Electrical Characteristics (T_J = 25°C unless otherwise specified)

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
Off Cha	racteristics					
V _{(BR)DSS}	Drain-Source Breakdown Voltage	$I_D = 250 \mu A, V_{GS} = 0 V$	150	-	-	V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = 150V, V _{GS} = 0V	-	-	1.0	μА
I _{GSS}	Gate-Body Leakage Current	$V_{DS} = 0V, V_{GS} = \pm 20V$	-	-	±100	nA
On Cha	racteristics					
V _{GS(th)}	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	2.5	3.0	3.5	V
R _{DS(ON)}	Static Drain-Source ON-Resistance ⁽³⁾	$V_{GS} = 10V, I_D = 30A$	-	11.5	15.0	mΩ
Dynami	ic Characteristics			.0		
C _{iss}	Input Capacitance		-	1350	-	pF
C _{oss}	Output Capacitance	$V_{GS} = 0V, V_{DS} = 25V,$ f = 1MHz	-	1900	-	pF
C _{rss}	Reverse Transfer Capacitance			45	-	pF
Q_g	Total Gate Charge		-	35	-	nC
Q_{gs}	Gate Source Charge	$V_{GS} = 0 \text{ to } 10V$ $V_{DS} = 75V, I_D = 20A$		12	-	nC
Q_{gd}	Gate Drain("Miller") Charge	V _{DS} - 75V, I _D - 20A	U -	6	-	nC
Switchi	ing Characteristics					
t _{d(on)}	Turn-On DelayTime		-	14	-	ns
t _r	Turn-On Rise Time	$V_{GS} = 10V, V_{DD} = 75V$	-	15	-	ns
$t_{d(off)}$	Turn-Off DelayTime	I_D = 20A, R_{GEN} = 6Ω	-	41	-	ns
t _f	Turn-Off Fall Time		-	13	-	ns
Drain-S	Source Diode Characteristics and I	Max Ratings				
I _s	Maximum Continuous Drain to Source Dioc	le Forward Current	-	-	80	Α
I _{SM}	Maximum Pulsed Drain to Source Diode Fo	orward Current	-	-	320	Α
V_{SD}	Drain to Source Diode Forward Voltage	$V_{GS} = 0V, I_{S} = 30A$	-	-	1.2	V
trr	Body Diode Reverse Recovery Time	1 - 004 - 4:/-4: 4004/	-	75	-	ns
Qrr	Body Diode Reverse Recovery Charge	$I_F = 20A$, di/dt = 100A/us	-	290	-	nC

Notes:

^{1.} Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature.

^{2.} E_{AS} condition: Starting T_J =25C, V_{DD} =50V, V_G =10V, R_G =25ohm, L=0.5mH, I_{AS} =35A

^{3.} Pulse Test: Pulse Width≤300µs, Duty Cycle≤0.5%.



Test Circuit

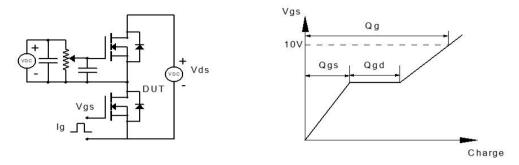


Figure 1: Gate Charge Test Circuit & Waveform

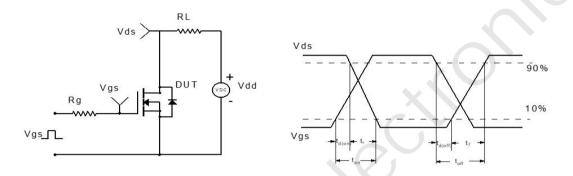


Figure 2: Resistive Switching Test Circuit & Waveform

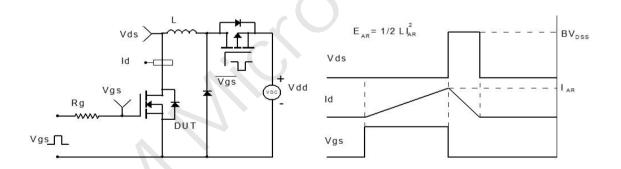


Figure 3: Unclamped Inductive Switching Test Circuit& Waveform

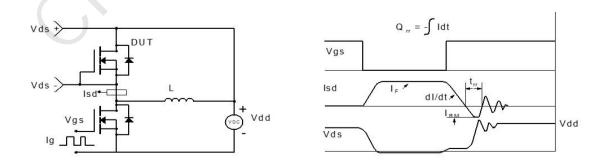
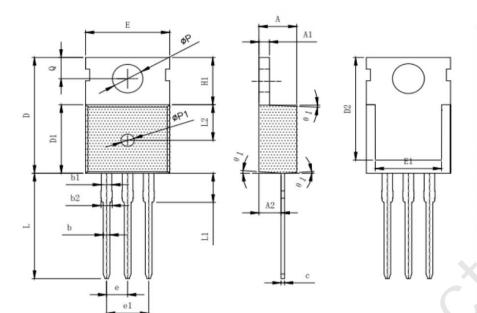


Figure 4: Diode Recovery Test Circuit & Waveform



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Package Mechanical Data(TO-220C-3L)



SYMBOL	MI	LLIMETER		
SIMBUL	MIN	NOM	MAX	
A	4.40	4. 50	4.60	
A1	1.25	1.30	1.35	
A2	2, 30	2.40	2.50	
ь	0.70	0.80	0.90	
b1	1. 21	1. 27	1.40	
b2	1.25	1. 35	1.45	
c	0.40	0.50	0.60	
D	15.50	15. 80	16. 10	
D1	9.10	9. 20	9.30	
D2	13. 14	13. 24	13.70	
E	9.70	9. 90	10.20	
E1	7. 60	8. 00	8.40	
e	2. 54 (BSC)			
el		5. 08 (BSC)		
H1	6.30	6.50	6.80	
L	12.75	13.08	13.50	
L1	<u> </u>		3. 10	
L2	4.30	4. 60	4.90	
øP	3. 50	3.60	3. 70	
ØP1	1.40	1.50	1.60	
a	2. 70		2.90	
0 1	1*	3°	5*	

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